Ordering number : ENA1456A

SANYO Semiconductors

DATA SHEET

An ON Semiconductor Company

P-Channel Silicon MOSFET

ECH8671 — General-Purpose Switching Device Applications

Features

- 1.8V drive
- Composite type, facilitating high-density mounting
- Halogen free compliance

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DSS}		−12	V
Gate-to-Source Voltage	V_{GSS}		±10	V
Drain Current (DC)	I_D		−3.5	A
Drain Current (Pulse)	I_{DP}	PW≤10μs, duty cycle≤1%	−30	A
Allowable Power Dissipation	P_D	When mounted on ceramic substrate (1200mm²×0.8mm) 1unit	1.3	W
Total Power Dissipation	P_T	When mounted on ceramic substrate (1200mm²×0.8mm)	1.5	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		−55 to +150	°C

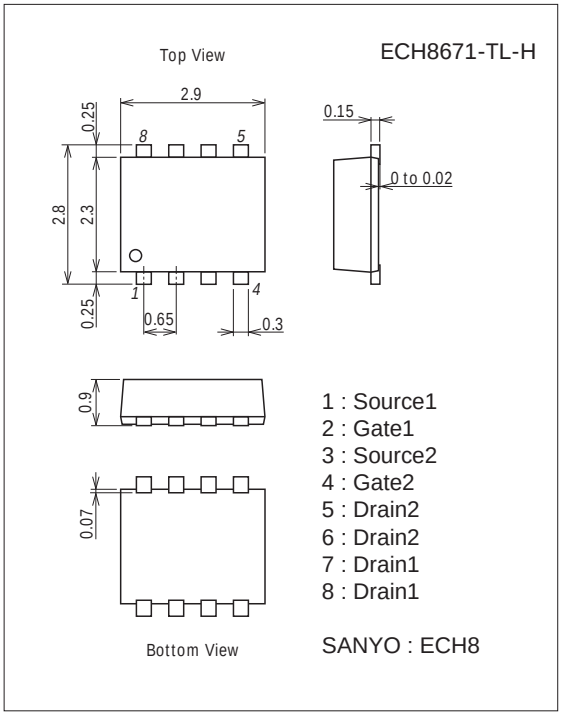
This product is designed to "ESD immunity < 200V*", so please take care when handling.

* Machine Model

Package Dimensions

unit : mm (typ)

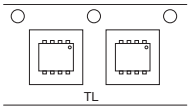
7011A-001



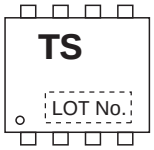
Product & Package Information

- Package : ECH8
- JEITA, JEDEC : -
- Minimum Packing Quantity : 3,000 pcs./reel

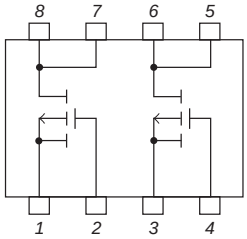
Packing Type : TL



Marking



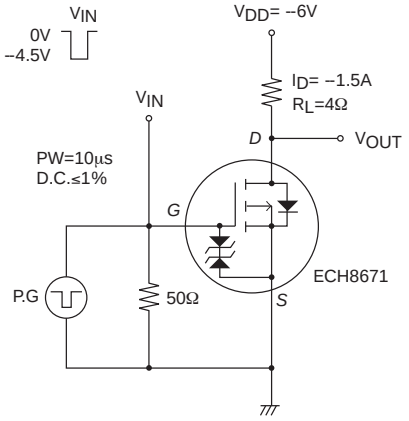
Electrical Connection



Electrical Characteristics at Ta=25°C

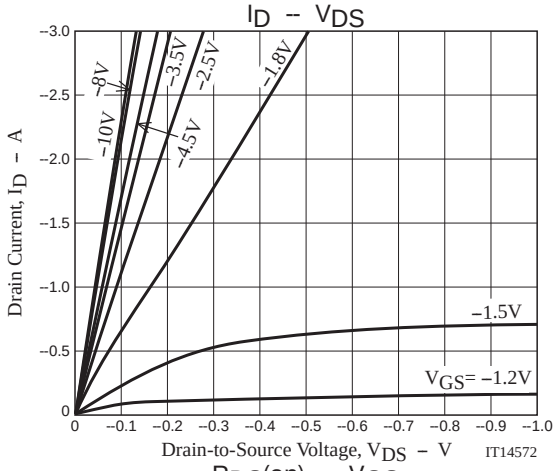
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	I_D=−1mA, V_{GS}=0V	−12			V
Zero-Gate Voltage Drain Current	I_{DSS}	V_{DS}=−12V, V_{GS}=0V			−10	μA
Gate-to-Source Leakage Current	I_{GSS}	V_{GS}=±8V, V_{DS}=0V			±10	μA
Cutoff Voltage	V_{GS}(off)	V_{DS}=−6V, I_D=−1mA	−0.4		−1.3	V
Forward Transfer Admittance	| yfs |	V_{DS}=−6V, I_D=−1.5A	2.7	4.6		S
Static Drain-to-Source On-State Resistance	R_{DS}(on)1	I_D=−1.5A, V_{GS}=−4.5V		59	77	mΩ
	R_{DS}(on)2	I_D=−1A, V_{GS}=−2.5V		90	126	mΩ
	R_{DS}(on)3	I_D=−0.5A, V_{GS}=−1.8V		145	215	mΩ
Input Capacitance	Ciss	V_{DS}=−6V, f=1MHz		330		pF
Output Capacitance	Coss			110		pF
Reverse Transfer Capacitance	Crss			88		pF
Turn-ON Delay Time	t_{d}(on)	See specified Test Circuit.		7.1		ns
Rise Time	t_r			30		ns
Turn-OFF Delay Time	t_{d}(off)			31		ns
Fall Time	t_f			32		ns
Total Gate Charge	Qg	V_{DS}=−6V, V_{GS}=−4.5V, I_D=−3.5A		3.9		nC
Gate-to-Source Charge	Qgs			0.6		nC
Gate-to-Drain “Miller” Charge	Qgd			1.1		nC
Diode Forward Voltage	V_{SD}	I_S=−3.5A, V_{GS}=0V		−0.85	−1.2	V

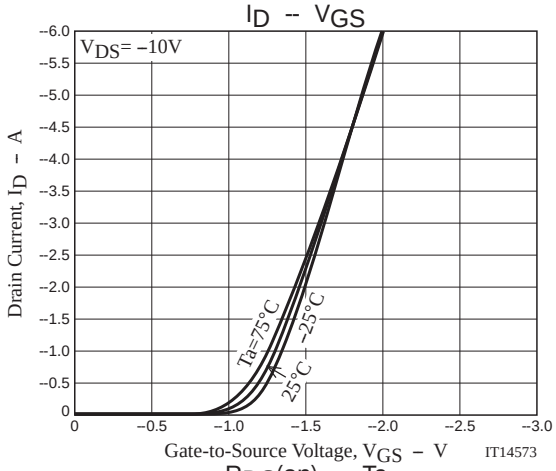
Switching Time Test Circuit

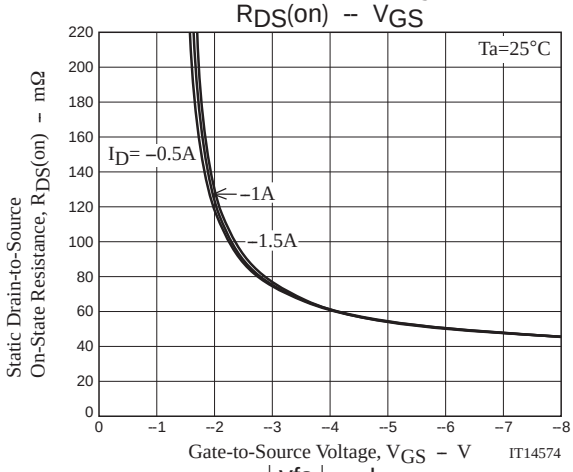


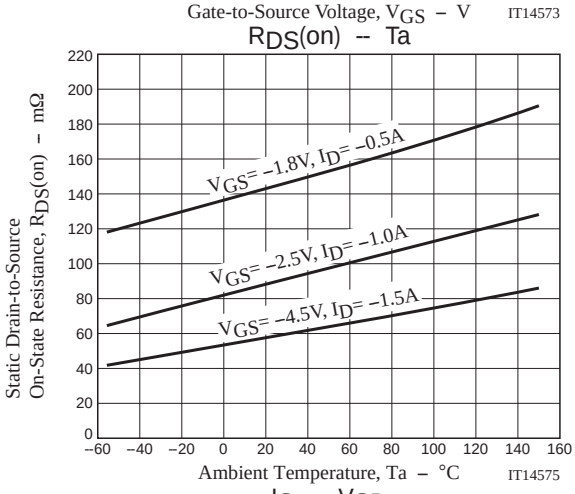
Ordering Information

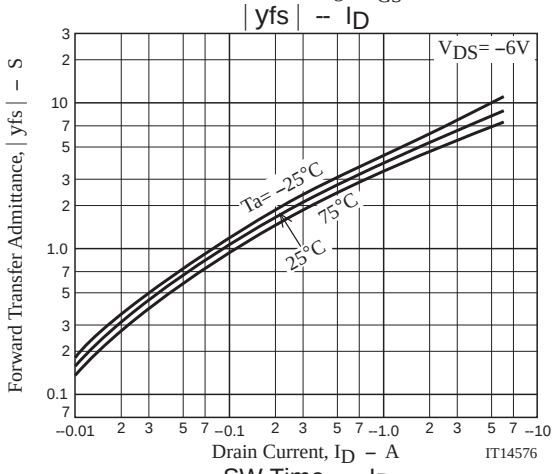
Device	Package	Shipping	memo
ECH8671-TL-H	ECH8	3,000pcs./reel	Pb Free and Halogen Free

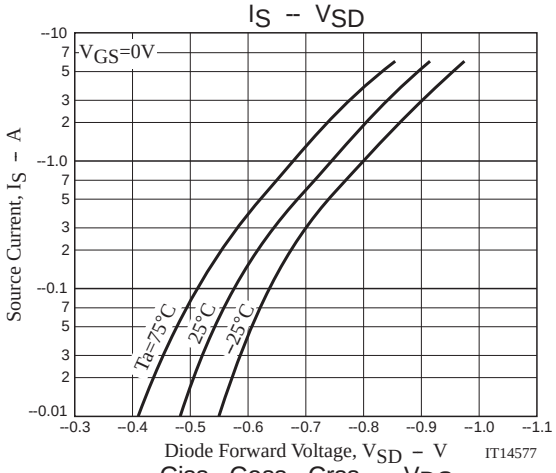


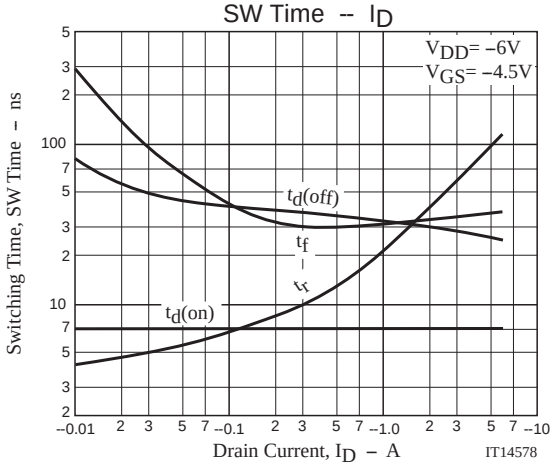


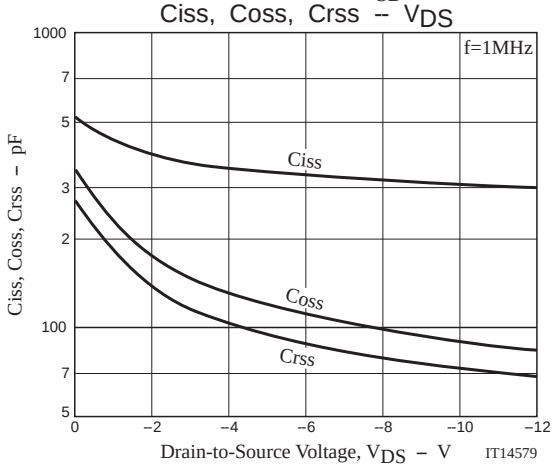


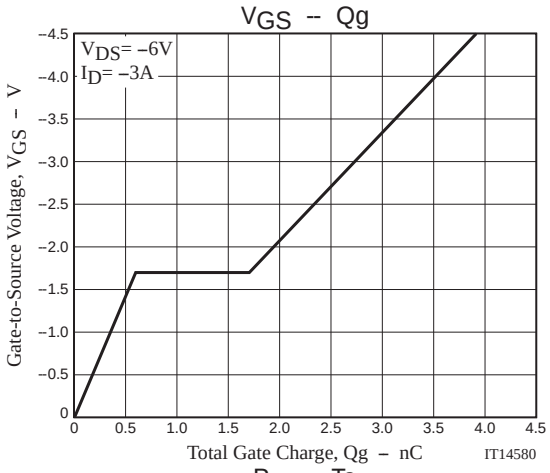


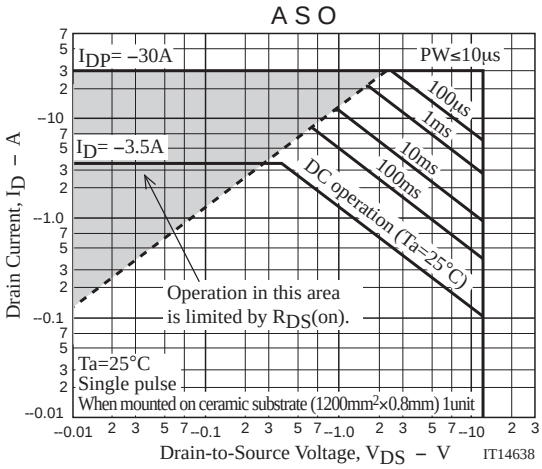


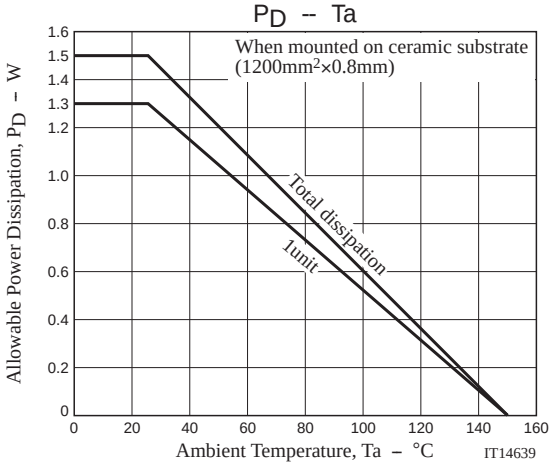












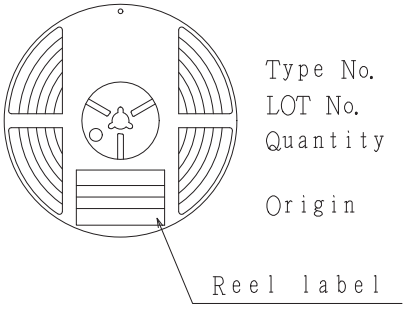
Embossed Taping Specification

ECH8671-TL-H

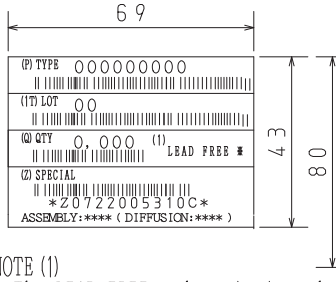
1. Packing Format

Package Name	Carrier Tape Type	Maximum Number of devices contained (pcs)			Packing format	
		Reel	Inner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)
ECH8	CPH6	3,000	15,000	90,000	5 reels contained Dimensions:mm (external) 183×72×185	6 inner boxes contained Dimensions:mm (external) 440×195×210

Packing method

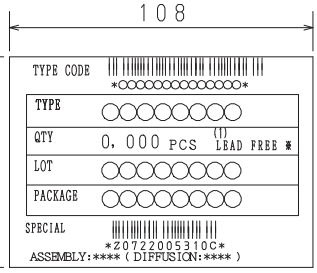


Reel label, Inner box label (unit:mm)



Outer box label

It is a label at the time of factory shipments. The form of a label may change in physical distribution process.

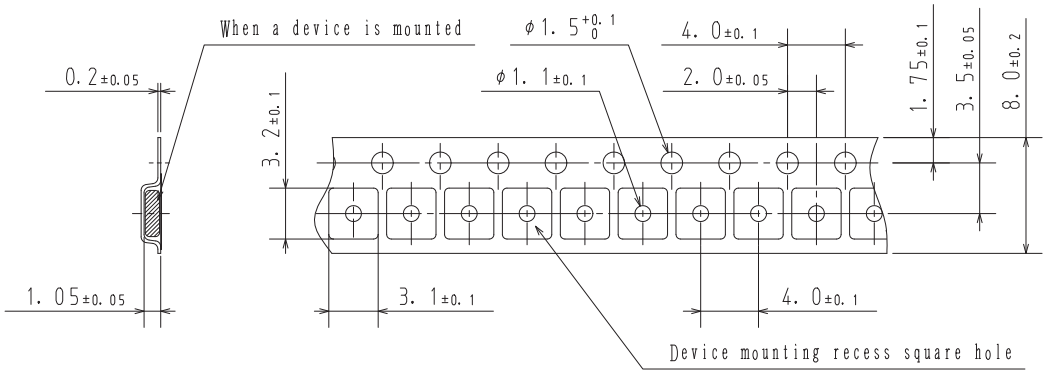


NOTE (1)
The LEAD FREE ※ description shows that the surface treatment of the terminal is lead free.

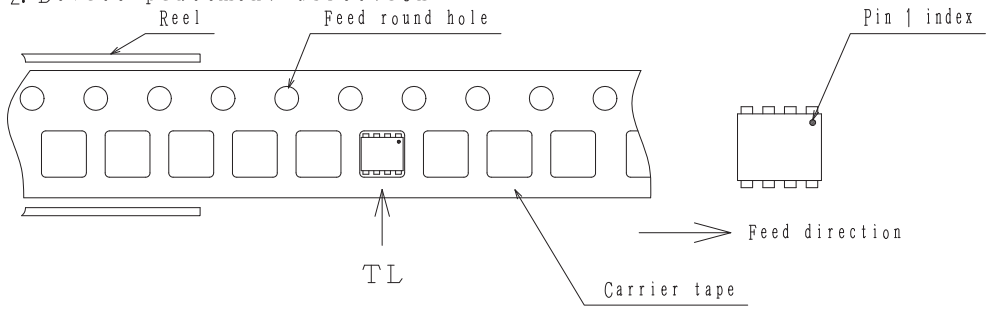
Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

2. Taping configuration

2-1. Carrier tape size (unit:mm)



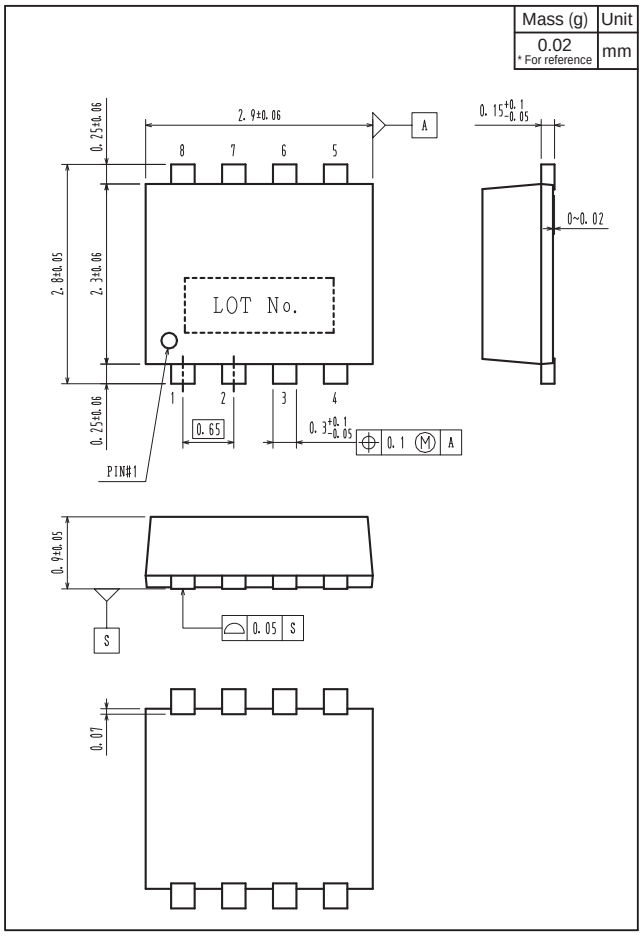
2-2. Device placement direction



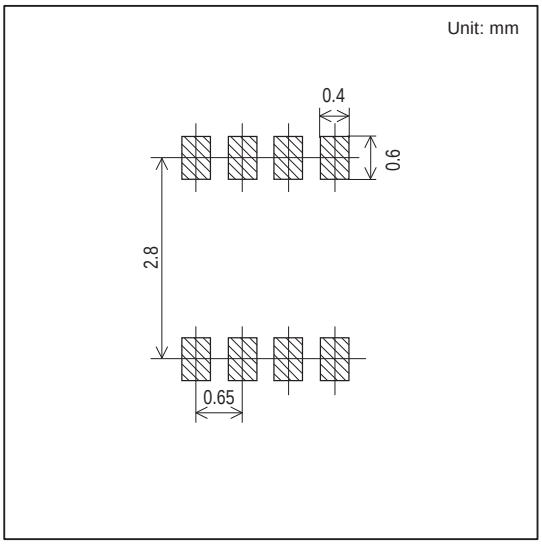
Those with pin 1 index on the feed hole side·····TL

Outline Drawing

ECH8671-TL-H



Land Pattern Example



Note on usage : Since the ECH8671 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

This catalog provides information as of June, 2012. Specifications and information herein are subject to change without notice.