

# ZXMP3F36N8 30V SO8 P-channel enhancement mode MOSFET

### Summary

V <sub>(BR)DSS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A)
-30	0.020 @ V <sub>GS</sub> =-10V	-12.6
	0.028 @ V <sub>GS</sub> =-4.5V	



## Description

This new generation Trench MOSFET from Zetex has been designed to minimize the on-state resistance ( $R_{DS(on)}$ ) and yet maintain superior switching performance.

### Features

- Low on-resistance
- SO8 package

### Applications

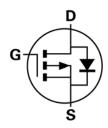
- Battery Protection
- Battery disconnect
- Power management functions

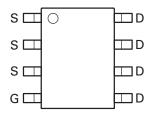
### **Ordering information**

Device	Reel size	Tape width	Quantity
	(inches)	(mm)	per reel
ZXMP3F36N8TA	7	12	500

### **Device marking**

ZXMP 3F36





Top view

### Absolute maximum ratings

Parameter	Symbol	Limit	Unit
Drain-Source voltage	V <sub>DSS</sub>	-30	V
Gate-Source voltage	V <sub>GS</sub>	±20	V
Continuous Drain current @ $V_{GS}$ = -10V; $T_A$ =25°C (b)	ID	-9.6	V
@ V <sub>GS</sub> = -10V; T <sub>A</sub> =70°C <sup>(b)</sup>		-7.7	
@ V <sub>GS</sub> = -10V; T <sub>A</sub> =25°C <sup>(a)</sup>		-7.2	
@ $V_{GS}$ = -10V; $T_L$ =25°C <sup>(d)</sup>		-12.6	
Pulsed Drain current <sup>(c)</sup>	I <sub>DM</sub>	-45	А
Continuous Source current (Body diode) (b)	I <sub>S</sub>	-4.7	А
Pulsed Source current (Body diode) (c)	I <sub>SM</sub>	-45	А
Power dissipation at $T_A = 25^{\circ}C^{(a)}$	PD	1.56	W
Linear derating factor		12.5	mW/°C
Power dissipation at $T_A = 25^{\circ}C^{(b)}$	PD	2.8	W
Linear derating factor	_	22.2	mW/°C
Power dissipation at $T_L = 25 \degree C^{(d)}$	PD	4.7	W
Linear derating factor		37.9	mW/°C
Operating and storage temperature range	T <sub>j</sub> , T <sub>stg</sub>	-55 to 150	°C

### Thermal resistance

Parameter	Symbol	Value	Unit
Junction to ambient <sup>(a)</sup>	$R_{\theta JA}$	80	°C/W
Junction to ambient <sup>(b)</sup>	$R_{\theta JA}$	45	°C/W
Junction to lead <sup>(d)</sup>	$R_{\theta JL}$	26.4	°C/W

NOTES:

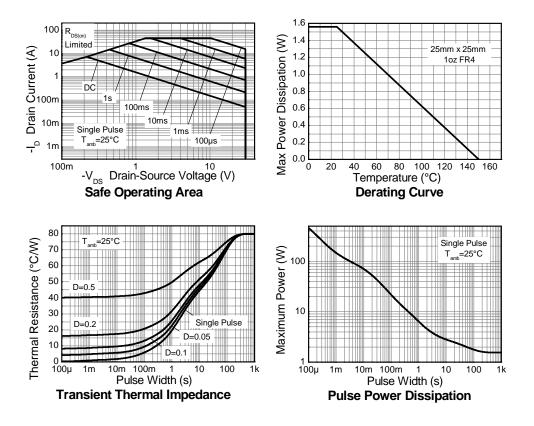
(a) For a device surface mounted on 25mm x 25mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.

(b) Mounted on FR4 PCB measured at t ≤ 10 sec.
(c) Repetitive rating on 25mm x 25mm FR4 PCB, D=0.02, pulse width 300us – pulse width limited by maximum junction temperature.

(d) Thermal resistance from junction to solder-point (at the end of the drain lead).

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### **Thermal characteristics**



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Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Static	· · ·				-	
Drain-Source breakdown voltage	V <sub>(BR)DSS</sub>	-30			V	$I_{D} = -250 \mu A, V_{GS} = 0 V$
Zero Gate voltage Drain current	I <sub>DSS</sub>			-1.0	μA	$V_{DS}$ =-30V, $V_{GS}$ =0V
Gate-Body leakage	I <sub>GSS</sub>			100	nA	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V
Gate-Source threshold voltage	V <sub>GS(th)</sub>	-1.3		-2.5	V	$I_D$ = -250 $\mu$ A, $V_{DS}$ = $V_{GS}$
Static Drain-Source on-state resistance <sup>(*)</sup>	R <sub>DS(on)</sub>			0.020 0.028	Ω	V <sub>GS</sub> = -10V, I <sub>D</sub> = -10A V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -8.0A
Forward Transconductance <sup>(*) (†)</sup>	<b>g</b> fs		29		S	V <sub>DS</sub> = -15V, I <sub>D</sub> = -10A
Dynamic <sup>(†)</sup>					•	
Input capacitance	C <sub>iss</sub>		2265		pF	
Output capacitance	C <sub>oss</sub>		424		pF	V <sub>DS</sub> = -15V, V <sub>GS</sub> =0V
Reverse transfer capacitance	C <sub>rss</sub>		266		pF	f=1MHz
Switching <sup>(‡) (†)</sup>					•	
Turn-on-delay time	t <sub>d(on)</sub>		3.1		ns	
Rise time	tr		5		ns	$V_{DD}$ = -15V, $V_{GS}$ = -10V
Turn-off delay time	t <sub>d(off)</sub>		75		ns	I <sub>D</sub> = -1A
Fall time	t <sub>f</sub>		40		ns	R <sub>G</sub> ≅ 6.0Ω,
Gate charge					1	1
Total Gate charge	Qg		43.9		nC	
Gate-Source charge	Q <sub>gs</sub>		6		nC	V <sub>DS</sub> = -15V, V <sub>GS</sub> = -10V
Gate-Drain charge	Q <sub>gd</sub>		9.8		nC	I <sub>D</sub> = -10A
Source-Drain diode	· ·		•			•
Diode forward voltage (*)	V <sub>SD</sub>		-0.73	-1.2	V	I <sub>S</sub> = -1.7A,V <sub>GS</sub> =0V
Reverse recovery time $^{(\ddagger)}$	t <sub>rr</sub>		17.7		ns	I <sub>S</sub> = -2.9A,di/dt=100A/μs
Reverse recovery charge <sup>(‡)</sup>	Q <sub>rr</sub>		11.7		nC	15- 2.0π,αι/ατ-100π/μ3

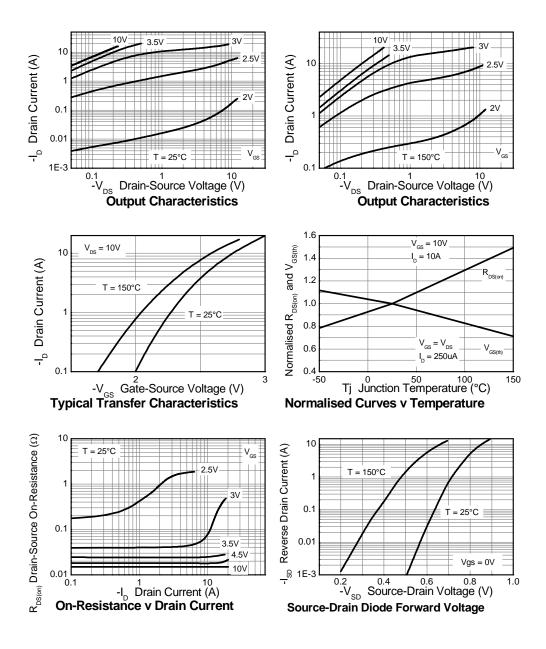
## Electrical characteristics (at T<sub>amb</sub> = 25°C unless otherwise stated)

### NOTES:

(\*) Measured under pulsed conditions. Pulse width  $\leq$  300 $\mu$ s; duty cycle  $\leq$  2%.

(†)Switching characteristics are independent of operating junction temperature.
 (‡)For design aid only, not subject to production testing

## **Typical characteristics**

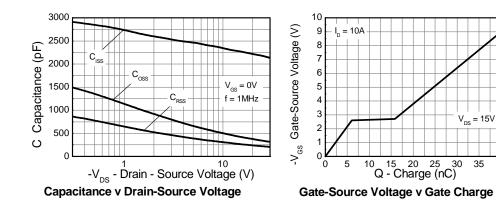


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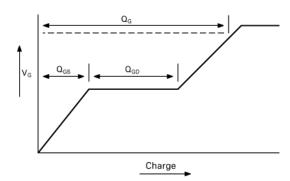
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40 45

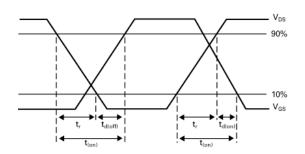
## **Typical characteristics**



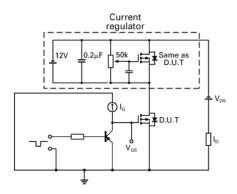
**Test circuits** 



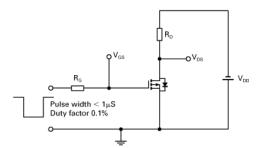
Basic gate charge waveform



Switching time waveforms



Gate charge test circuit

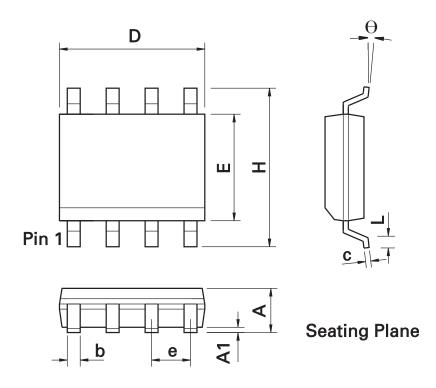


Switching time test circuit

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## Package outline SO8



**SO8 Package Information** 

DIM	Inc	hes	Millin	neters	DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
А	0.053	0.069	1.35	1.75	е	0.050	BSC	1.27	BSC
A1	0.004	0.010	0.10	0.25	b	0.013	0.020	0.33	0.51
D	0.189	0.197	4.80	5.00	с	0.008	0.010	0.19	0.25
н	0.228	0.244	5.80	6.20	U	0°	8°	0°	8°
E	0.150	0.157	3.80	4.00	h	0.010	0.020	0.25	0.50
L	0.016	0.050	0.40	1.27	-	-	-	-	-

Note: Controlling dimensions are in inches. Approximate dimensions are provided in millimeters

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