



SANYO Semiconductors

# DATA SHEET

An ON Semiconductor Company

## SCH1333 — P-Channel Silicon MOSFET General-Purpose Switching Device Applications

### Features

- 1.8V drive
- Halogen free compliance
- Protection diode in

### Specifications

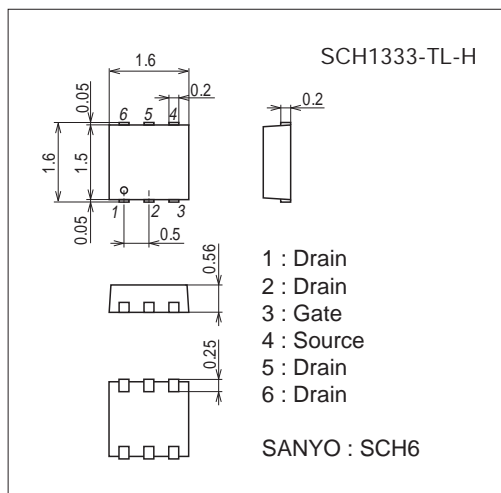
Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		-20	V
Gate-to-Source Voltage	VGSS		±10	V
Drain Current (DC)	ID		-2	A
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	-8	A
Allowable Power Dissipation	PD	When mounted on ceramic substrate (900mm <sup>2</sup> ×0.8mm)	0.8	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

### Package Dimensions

unit : mm (typ)

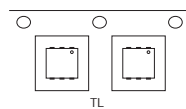
7028-002



### Product & Package Information

- Package : SCH6
- JEITA, JEDEC : SOT-563
- Minimum Packing Quantity : 5,000 pcs./reel

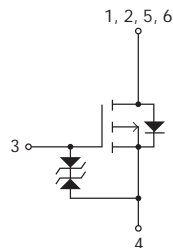
### Packing Type : TL



### Marking



### Electrical Connection

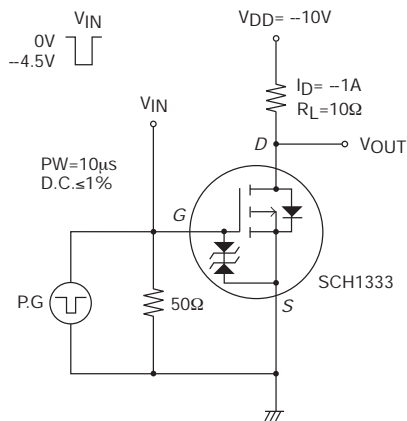


# SCH1333

## Electrical Characteristics at Ta=25°C

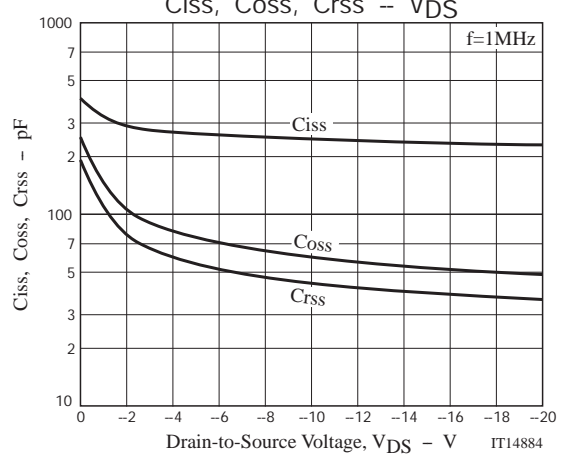
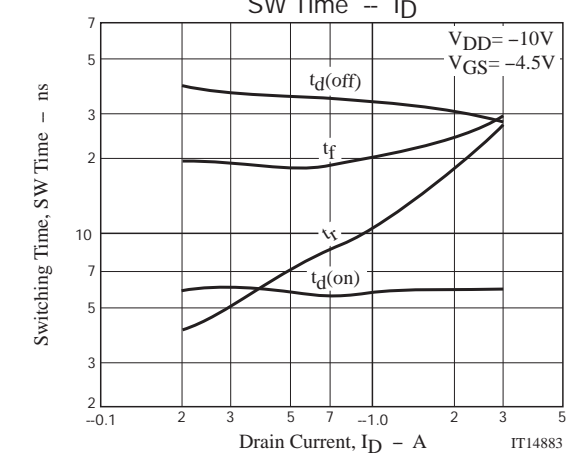
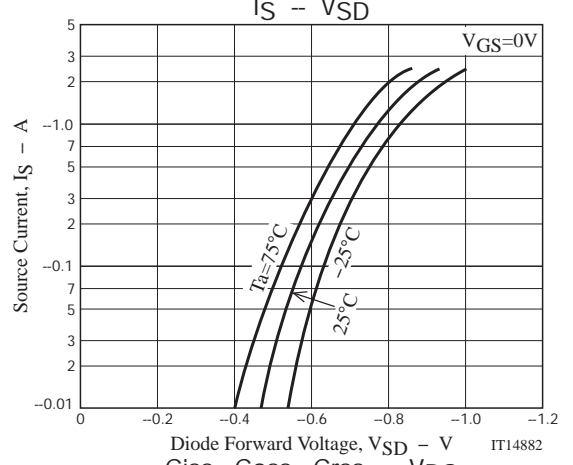
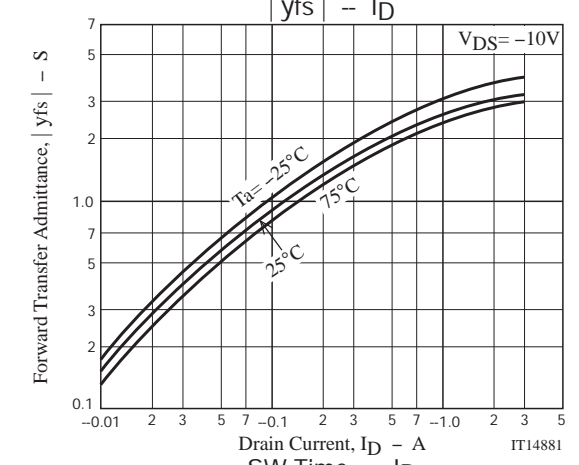
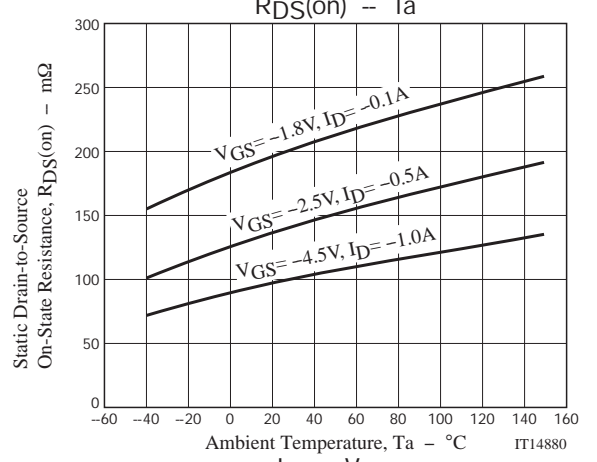
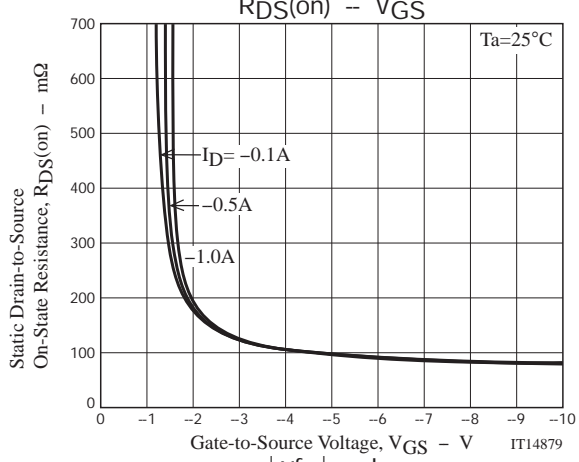
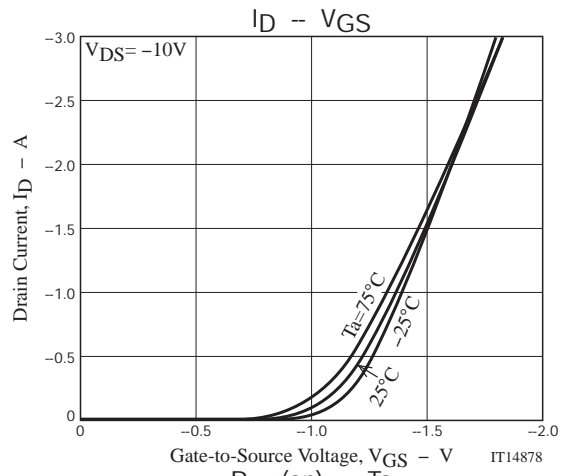
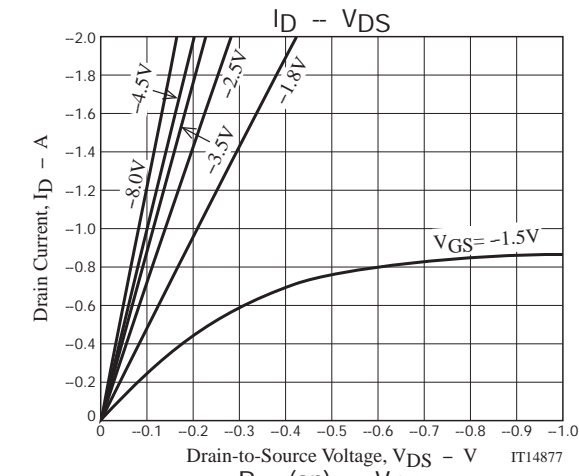
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D = -1\text{mA}, V_{GS} = 0\text{V}$	-20			V
Zero-Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = -20\text{V}, V_{GS} = 0\text{V}$			-1	$\mu\text{A}$
Gate-to-Source Leakage Current	$I_{GSS}$	$V_{GS} = \pm 8\text{V}, V_{DS} = 0\text{V}$			$\pm 10$	$\mu\text{A}$
Cutoff Voltage	$V_{GS(off)}$	$V_{DS} = -10\text{V}, I_D = -1\text{mA}$	-0.4		-1.4	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS} = -10\text{V}, I_D = -1\text{A}$		2.7		S
Static Drain-to-Source On-State Resistance	$R_{DS(on)1}$	$I_D = -1\text{A}, V_{GS} = -4.5\text{V}$		100	130	$\text{m}\Omega$
	$R_{DS(on)2}$	$I_D = -0.5\text{A}, V_{GS} = -2.5\text{V}$		140	196	$\text{m}\Omega$
	$R_{DS(on)3}$	$I_D = -0.1\text{A}, V_{GS} = -1.8\text{V}$		210	315	$\text{m}\Omega$
Input Capacitance	$C_{iss}$	$V_{DS} = -10\text{V}, f = 1\text{MHz}$		250		pF
Output Capacitance	$C_{oss}$			60		pF
Reverse Transfer Capacitance	$C_{rss}$			45		pF
Turn-ON Delay Time	$t_{d(on)}$		See specified Test Circuit.		5.7	
Rise Time	$t_r$			11		ns
Turn-OFF Delay Time	$t_{d(off)}$			34		ns
Fall Time	$t_f$			20		ns
Total Gate Charge	$Q_g$	$V_{DS} = -10\text{V}, V_{GS} = -4.5\text{V}, I_D = -2\text{A}$			3.3	
Gate-to-Source Charge	$Q_{gs}$			0.65		nC
Gate-to-Drain "Miller" Charge	$Q_{gd}$			0.72		nC
Diode Forward Voltage	$V_{SD}$	$I_S = -2\text{A}, V_{GS} = 0\text{V}$		-0.85	-1.2	V

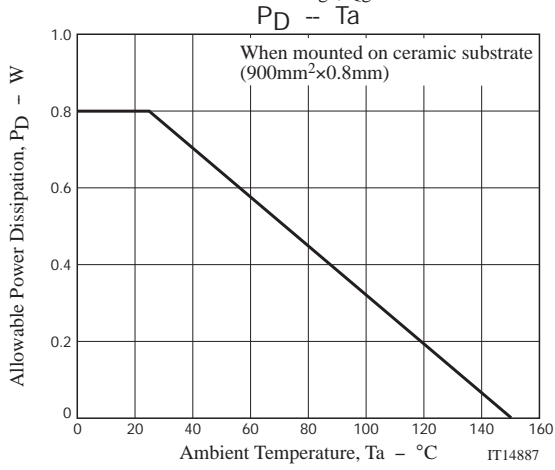
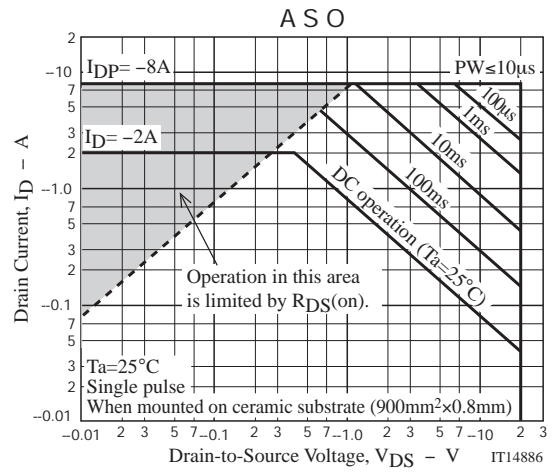
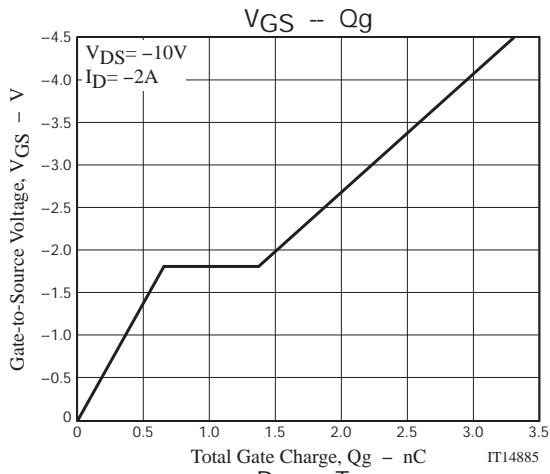
## Switching Time Test Circuit



## Ordering Information

Device	Package	Shipping	memo
SCH1333-TL-H	SCH6	5,000pcs./reel	Pb Free and Halogen Free





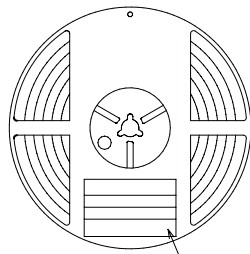
Taping Specification

SCH1333-TL-H

1. Packing Format

Package Name	Carrier Tape Type	Maximum Number of devices contained (pcs)			Packing format	
		Reel	Inner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)
SCH6	SCH6	5,000	25,000	150,000	5 reels contained Dimensions:mm (external) 183×72×185	6 inner boxes contained Dimensions:mm (external) 440×195×210

Packing method



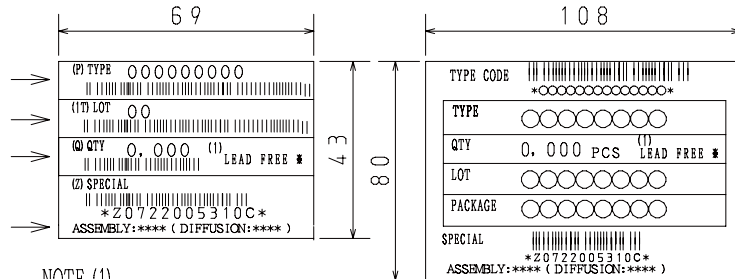
Type No.  
LOT No.  
Quantity  
Origin

Reel label

Reel label, Inner box label  
(unit:mm)

Outer box label

It is a label at the time of factory shipments.  
The form of a label may change in physical distribution process.



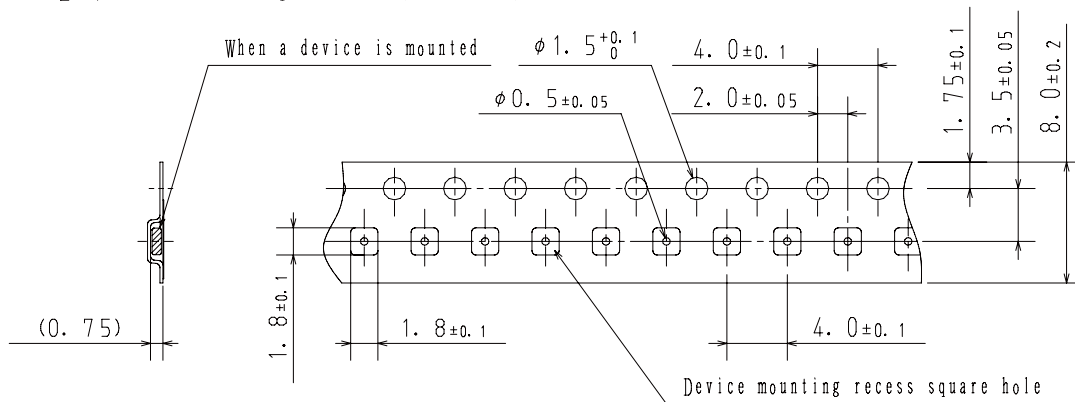
NOTE (1)

The LEAD FREE \* description shows that the surface treatment of the terminal is lead free.

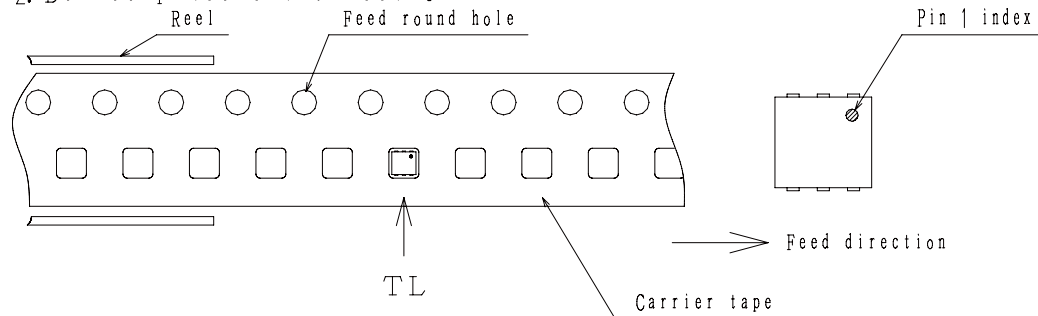
Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

2. Taping configuration

2-1. Carrier tape size (unit:mm)



2-2. Device placement direction

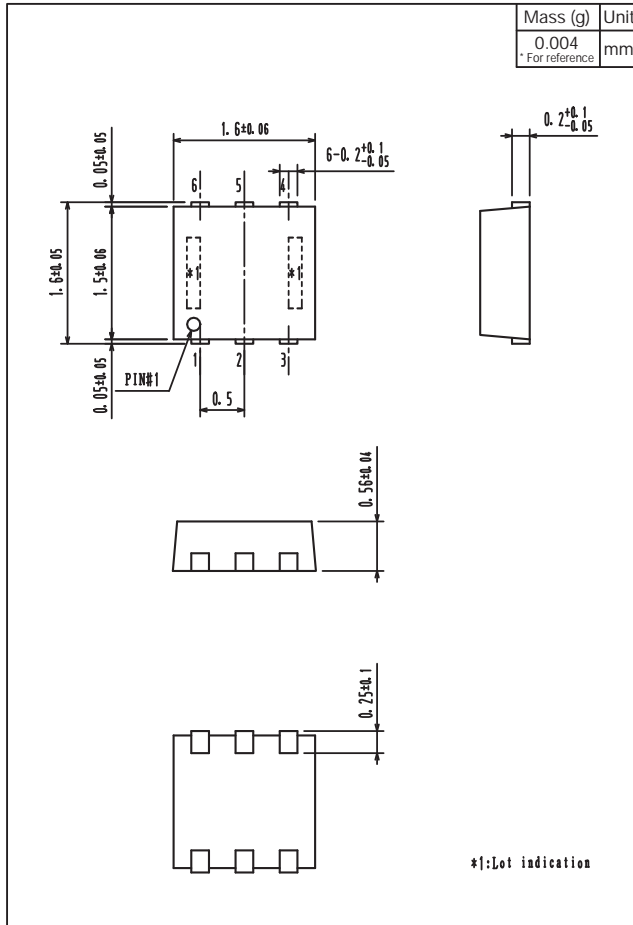


Those with pin 1 index on the feed hole side.....TL

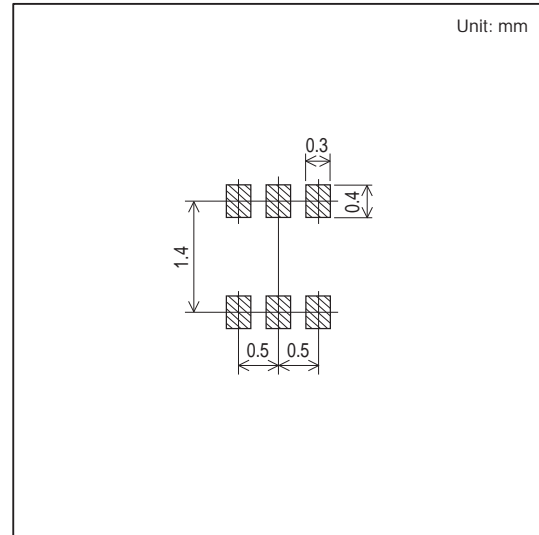
# SCH1333

## Outline Drawing

SCH1333-TL-H



## Land Pattern Example



Note on usage : Since the SCH1333 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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