



SANYO Semiconductors

DATA SHEET

An ON Semiconductor Company

ECH8659 — N-Channel Silicon MOSFET

General-Purpose Switching Device Applications

Features

- 4V drive
- Composite type, facilitating high-density mounting
- Halogen free compliance
- Protection diode in

Specifications

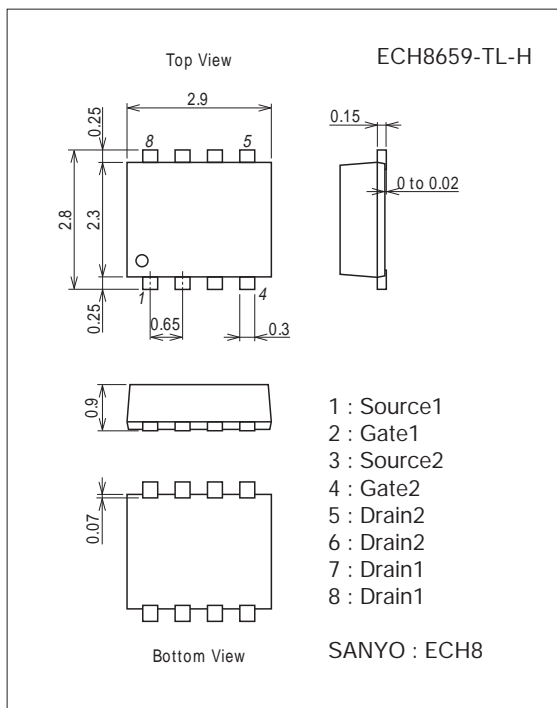
Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{DSS}		30	V
Gate-to-Source Voltage	V _{GSS}		±20	V
Drain Current (DC)	I _D		7	A
Drain Current (Pulse)	I _{DP}	PW≤10μs, duty cycle≤1%	40	A
Allowable Power Dissipation	PD	When mounted on ceramic substrate (900mm ² ×0.8mm) 1unit	1.3	W
Total Dissipation	P _T	When mounted on ceramic substrate (900mm ² ×0.8mm)	1.5	W
Channel Temperature	T _{ch}		150	°C
Storage Temperature	T _{stg}		-55 to +150	°C

Package Dimensions

unit : mm (typ)

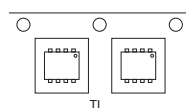
7011A-001



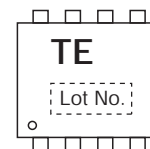
Product & Package Information

- Package : ECH8
- JEITA, JEDEC : -
- Minimum Packing Quantity : 3,000 pcs./reel

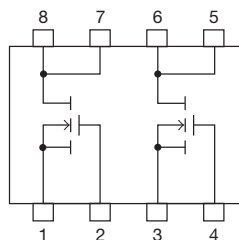
Packing Type : TL



Marking



Electrical Connection

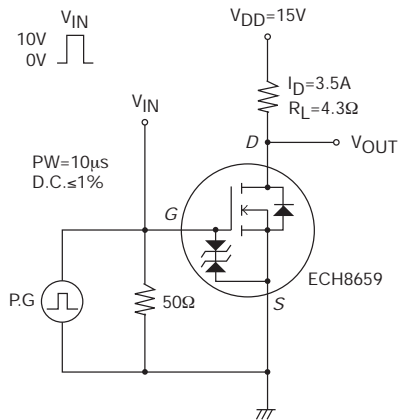


ECH8659

Electrical Characteristics at Ta=25°C

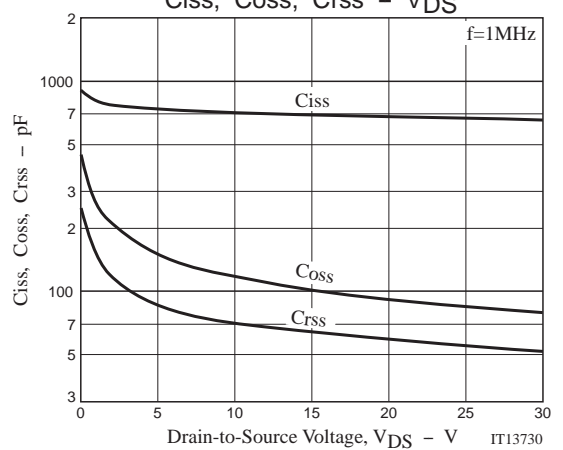
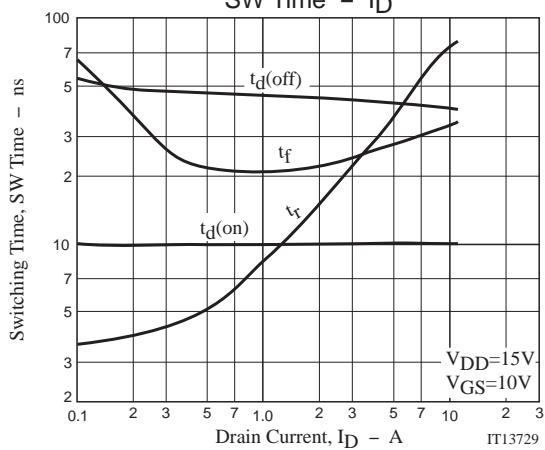
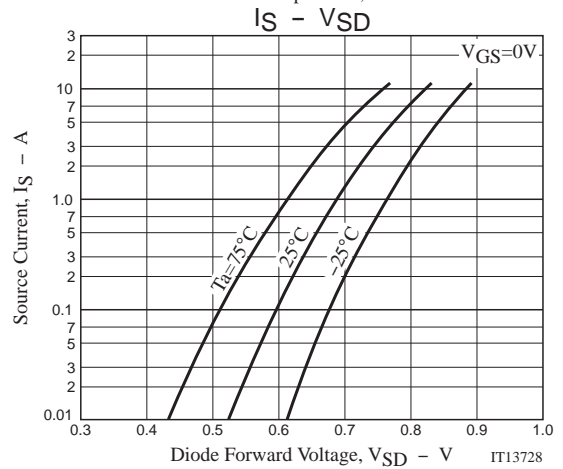
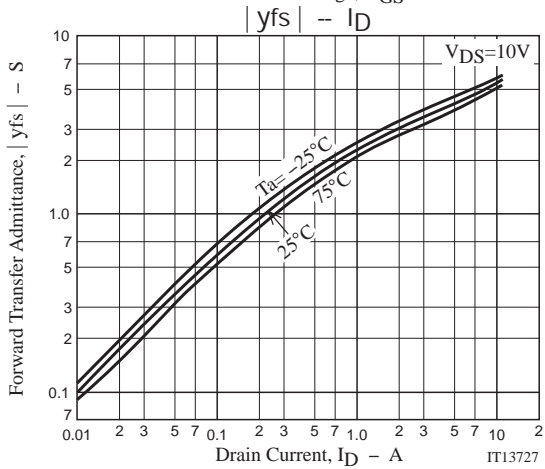
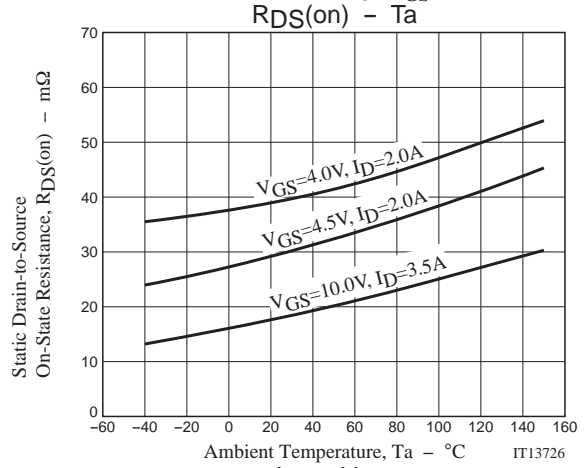
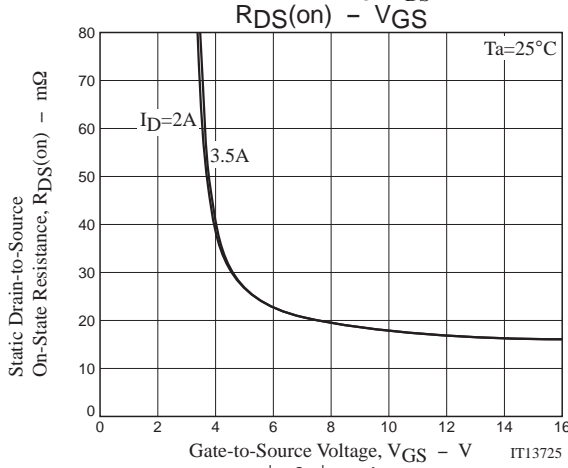
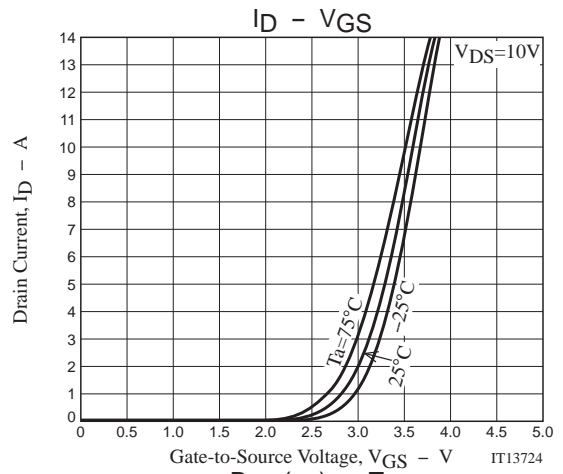
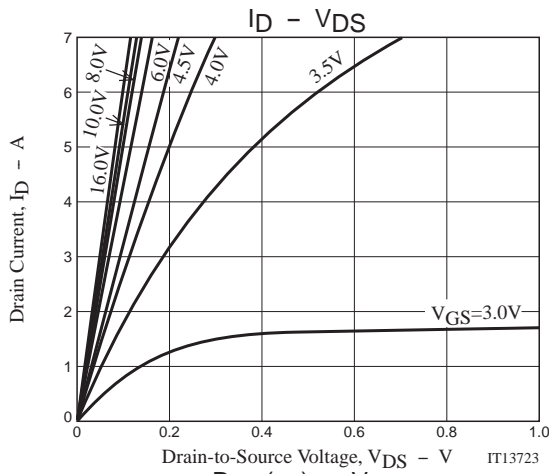
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D=1mA, V_{GS}=0V$	30			V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS}=30V, V_{GS}=0V$			1	μA
Gate-to-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 16V, V_{DS}=0V$			± 10	μA
Cutoff Voltage	$V_{GS(off)}$	$V_{DS}=10V, I_D=1mA$	1.2		2.6	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS}=10V, I_D=3.5A$	2.2	3.7		S
Static Drain-to-Source On-State Resistance	$R_{DS(on)1}$	$I_D=3.5A, V_{GS}=10V$		18	24	m Ω
	$R_{DS(on)2}$	$I_D=2A, V_{GS}=4.5V$		29	41	m Ω
	$R_{DS(on)3}$	$I_D=2A, V_{GS}=4V$		39	55	m Ω
Input Capacitance	C_{iss}	$V_{DS}=10V, f=1MHz$		710		pF
Output Capacitance	C_{oss}	$V_{DS}=10V, f=1MHz$		120		pF
Reverse Transfer Capacitance	C_{rss}	$V_{DS}=10V, f=1MHz$		72		pF
Turn-ON Delay Time	$t_{d(on)}$	See specified Test Circuit.		10		ns
Rise Time	t_r			25		ns
Turn-OFF Delay Time	$t_{d(off)}$			43		ns
Fall Time	t_f			25		ns
Total Gate Charge	Q_g				11.8	
Gate-to-Source Charge	Q_{gs}	$V_{DS}=15V, V_{GS}=10V, I_D=3.5A$		2.4		nC
Gate-to-Drain "Miller" Charge	Q_{gd}			2.0		nC
Diode Forward Voltage	V_{SD}		$I_S=7A, V_{GS}=0V$		0.79	1.2

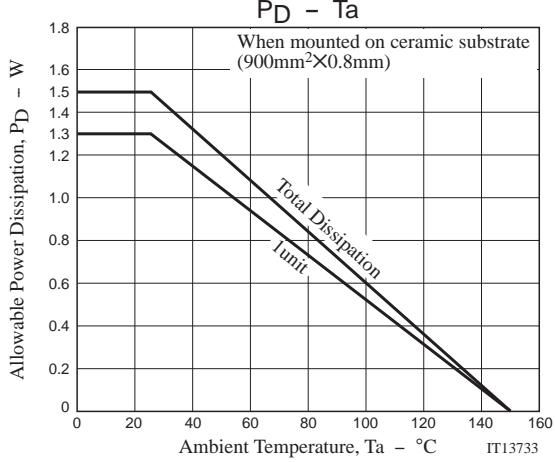
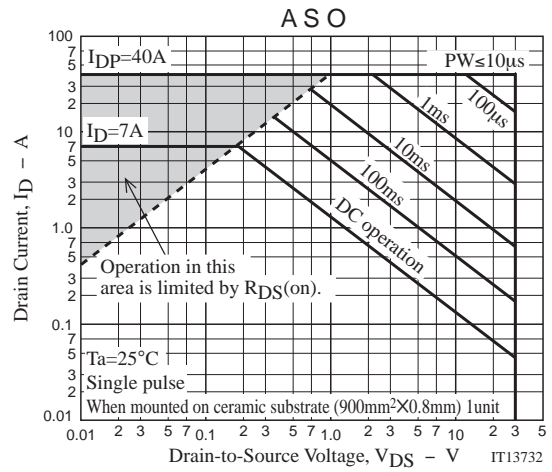
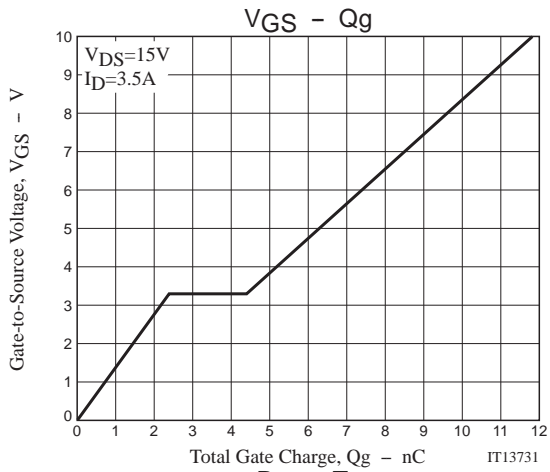
Switching Time Test Circuit



Ordering Information

Device	Package	Shipping	memo
ECH8659-TL-H	ECH8	3,000pcs./reel	Pb Free and Halogen Free





Embossed Taping Specification

ECH8659-TL-H

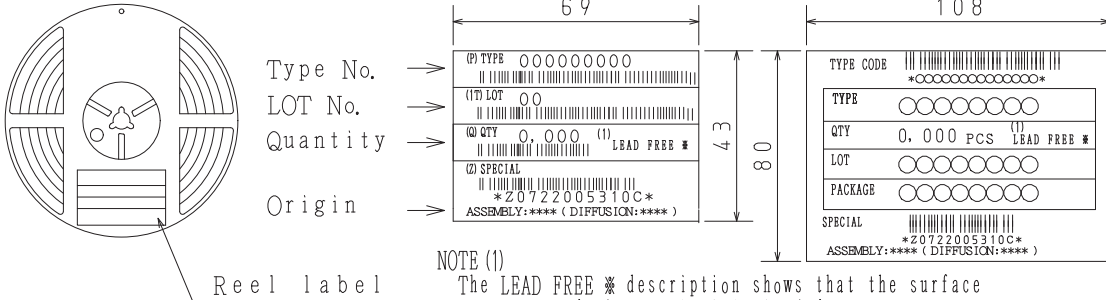
1. Packing Format

Package Name	Carrier Tape Type	Maximum Number of devices contained (pcs)			Packing format	
		Reel	Inner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)
ECH8	CPH6	3,000	15,000	90,000	5 reels contained Dimensions:mm (external) 183×72×185	6 inner boxes contained Dimensions:mm (external) 440×195×210

Packing method

Reel label, Inner box label (unit:mm) Outer box label

It is a label at the time of factory shipments. The form of a label may change in physical distribution process.

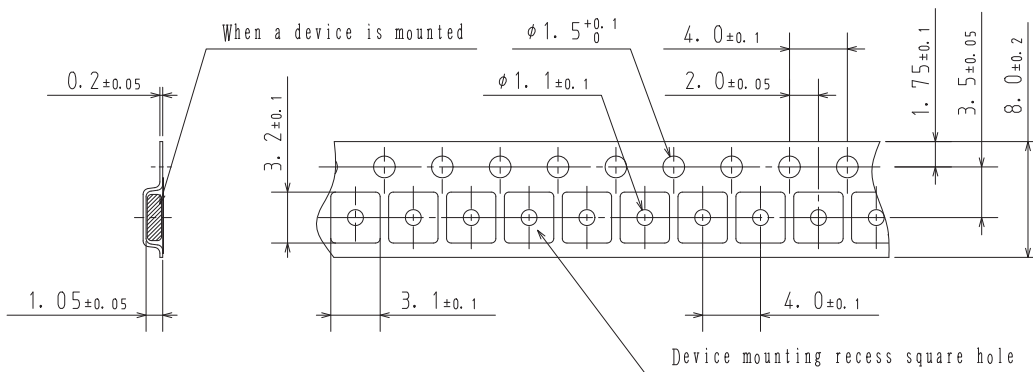


NOTE (1)
The LEAD FREE # description shows that the surface treatment of the terminal is lead free.

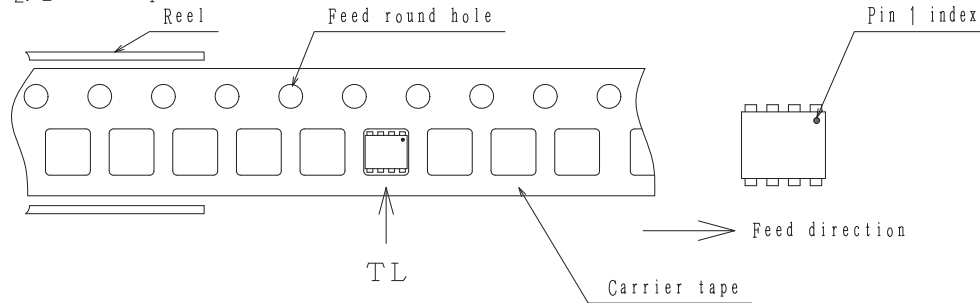
Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

2. Taping configuration

2-1. Carrier tape size (unit:mm)



2-2. Device placement direction



Those with pin 1 index on the feed hole side.....TL

Note on usage : Since the ECH8659 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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