



SANYO Semiconductors

DATA SHEET

An ON Semiconductor Company

SCH1343 — P-Channel Silicon MOSFET General-Purpose Switching Device Applications

Features

- ON-resistance $R_{DS(on)} = 55m\Omega$ (typ.)
- 1.8V drive
- Halogen free compliance
- Protection diode in

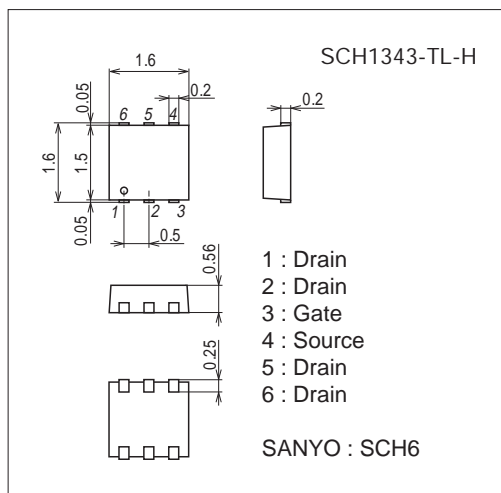
Specifications

Absolute Maximum Ratings at $T_a = 25^\circ C$

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DSS}		-20	V
Gate-to-Source Voltage	V_{GSS}		± 10	V
Drain Current (DC)	I_D		-3.5	A
Drain Current (Pulse)	I_{DP}	$PW \leq 10\mu s$, duty cycle $\leq 1\%$	-14	A
Allowable Power Dissipation	PD	When mounted on ceramic substrate (900mm ² x 0.8mm)	1	W
Channel Temperature	Tch		150	$^\circ C$
Storage Temperature	Tstg		-55 to +150	$^\circ C$

Package Dimensions

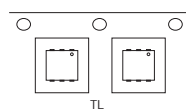
unit : mm (typ)
7028-002



Product & Package Information

- Package : SCH6
- JEITA, JEDEC : SOT-563
- Minimum Packing Quantity : 5,000 pcs./reel

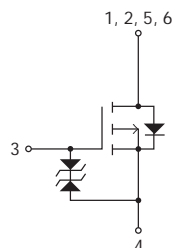
Packing Type : TL



Marking



Electrical Connection

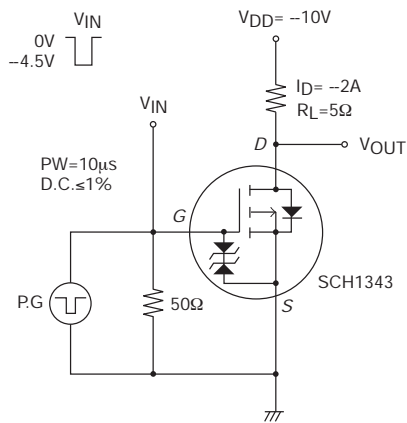


SCH1343

Electrical Characteristics at Ta=25°C

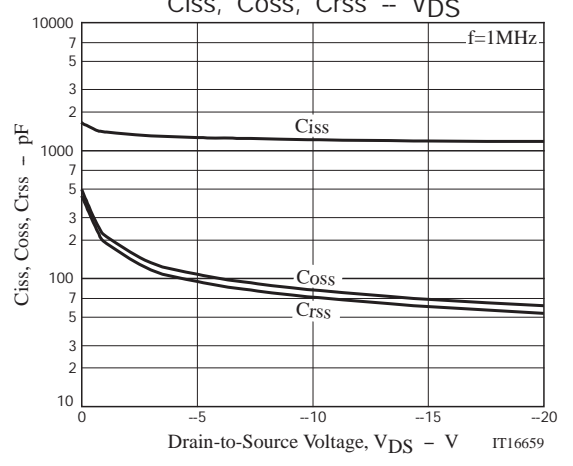
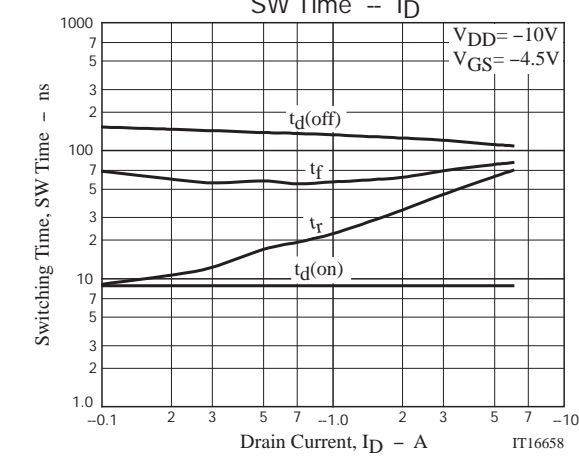
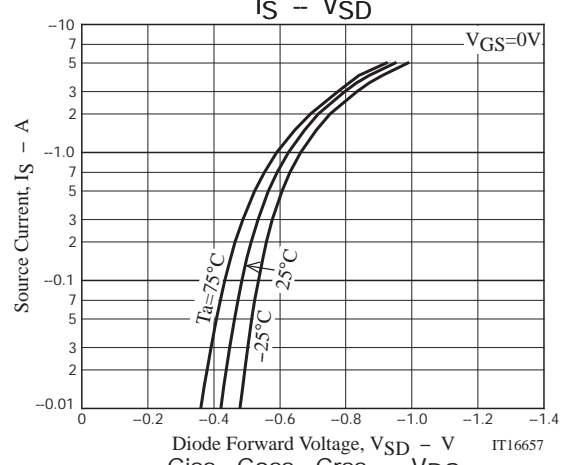
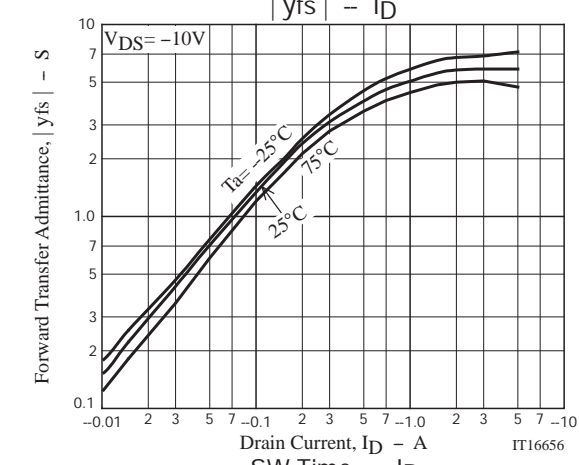
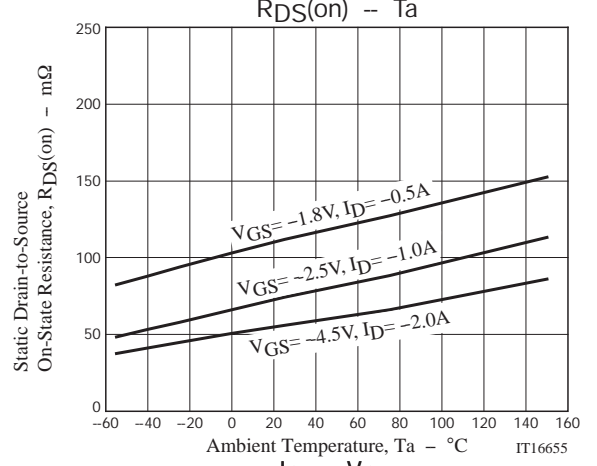
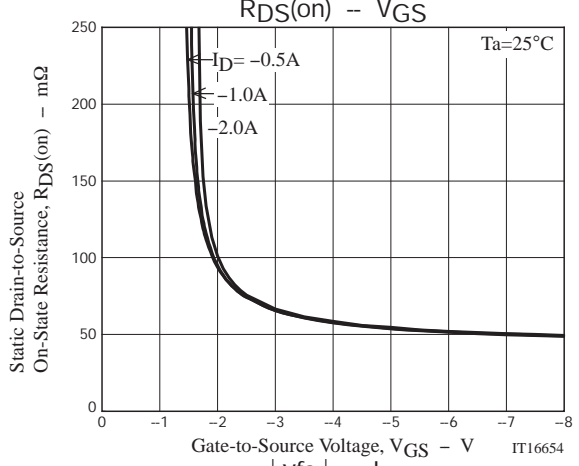
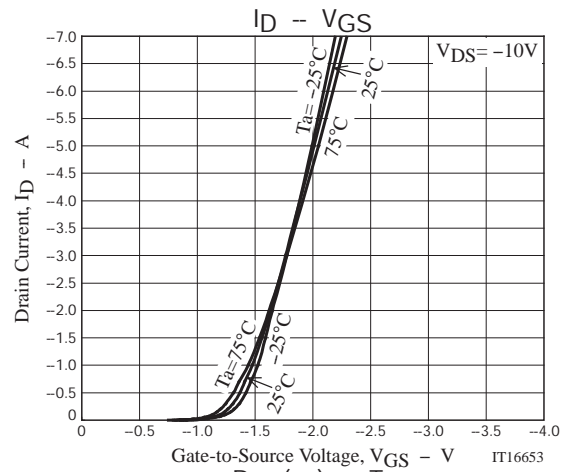
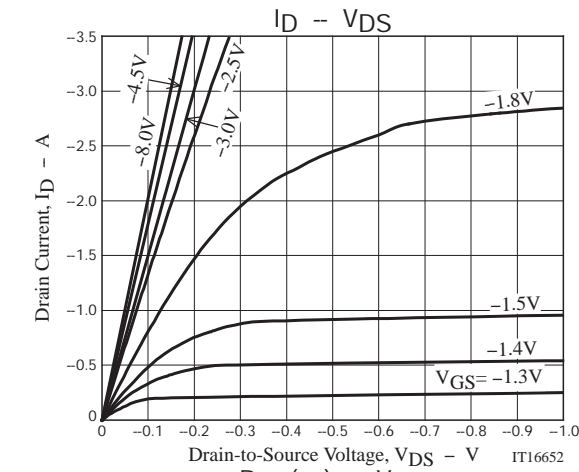
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D = -1\text{mA}, V_{GS} = 0\text{V}$	-20			V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -20\text{V}, V_{GS} = 0\text{V}$			-1	μA
Gate-to-Source Leakage Current	I_{GSS}	$V_{GS} = \pm 8\text{V}, V_{DS} = 0\text{V}$			± 10	μA
Cutoff Voltage	$V_{GS(off)}$	$V_{DS} = -10\text{V}, I_D = -1\text{mA}$	-0.4		-1.3	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS} = -10\text{V}, I_D = -2\text{A}$		6		S
Static Drain-to-Source On-State Resistance	$R_{DS(on)1}$	$I_D = -2\text{A}, V_{GS} = -4.5\text{V}$		55	72	$\text{m}\Omega$
	$R_{DS(on)2}$	$I_D = -1\text{A}, V_{GS} = -2.5\text{V}$		78	110	$\text{m}\Omega$
	$R_{DS(on)3}$	$I_D = -0.5\text{A}, V_{GS} = -1.8\text{V}$		115	173	$\text{m}\Omega$
Input Capacitance	C_{iss}	$V_{DS} = -10\text{V}, f = 1\text{MHz}$		1220		pF
Output Capacitance	C_{oss}			82		pF
Reverse Transfer Capacitance	C_{rss}			72		pF
Turn-ON Delay Time	$t_{d(on)}$			8.8		ns
Rise Time	t_r	See specified Test Circuit.		35		ns
Turn-OFF Delay Time	$t_{d(off)}$			123		ns
Fall Time	t_f			61		ns
Total Gate Charge	Q_g	$V_{DS} = -10\text{V}, V_{GS} = -4.5\text{V}, I_D = -3.5\text{A}$		11		nC
Gate-to-Source Charge	Q_{gs}			1.9		nC
Gate-to-Drain "Miller" Charge	Q_{gd}			1.9		nC
Diode Forward Voltage	V_{SD}		$I_S = -3.5\text{A}, V_{GS} = 0\text{V}$	-0.83	-1.2	

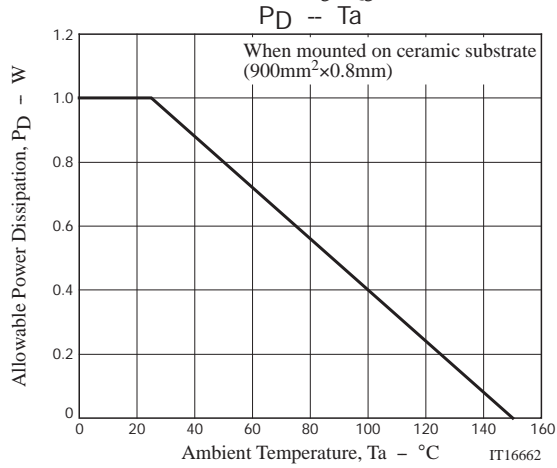
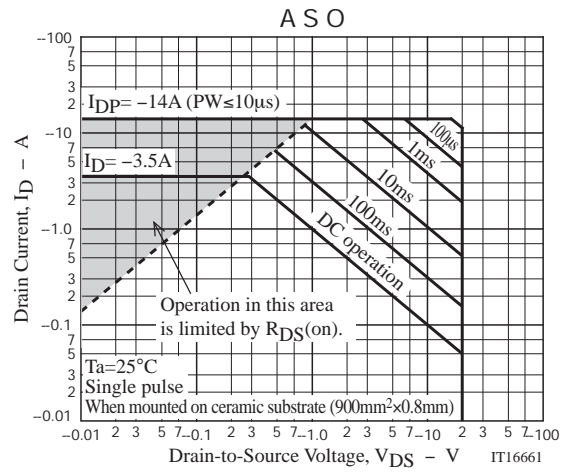
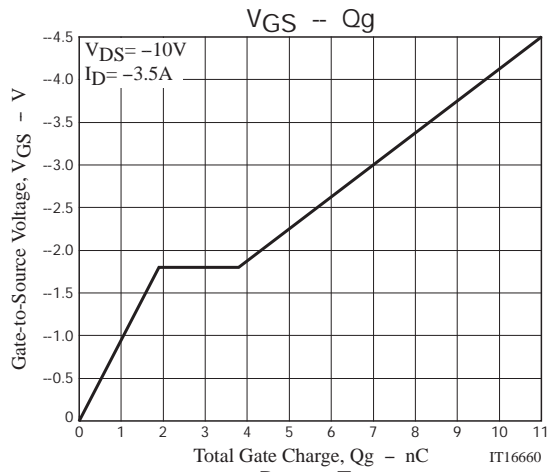
Switching Time Test Circuit



Ordering Information

Device	Package	Shipping	memo
SCH1343-TL-H	SCH6	5,000pcs./reel	Pb Free and Halogen Free





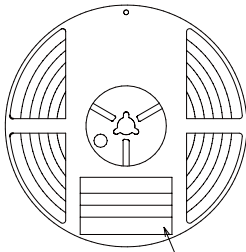
Taping Specification

SCH1343-TL-H

1. Packing Format

Package Name	Carrier Tape Type	Maximum Number of devices contained (pcs)			Packing format	
		Reel	Inner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)
SCH6	SCH6	5,000	25,000	150,000	5 reels contained Dimensions:mm (external) 183×72×185	6 inner boxes contained Dimensions:mm (external) 440×195×210

Packing method



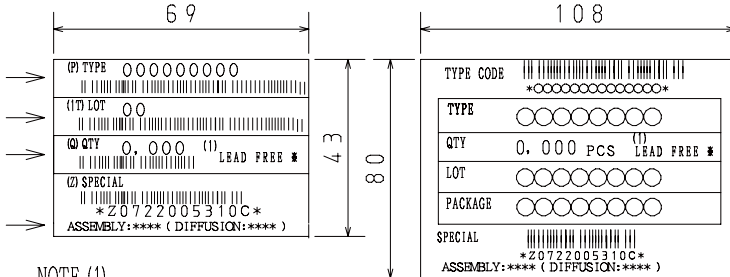
Type No.
LOT No.
Quantity
Origin

Reel label

Reel label, Inner box label
(unit:mm)

Outer box label

It is a label at the time of factory shipments.
The form of a label may change in physical distribution process.



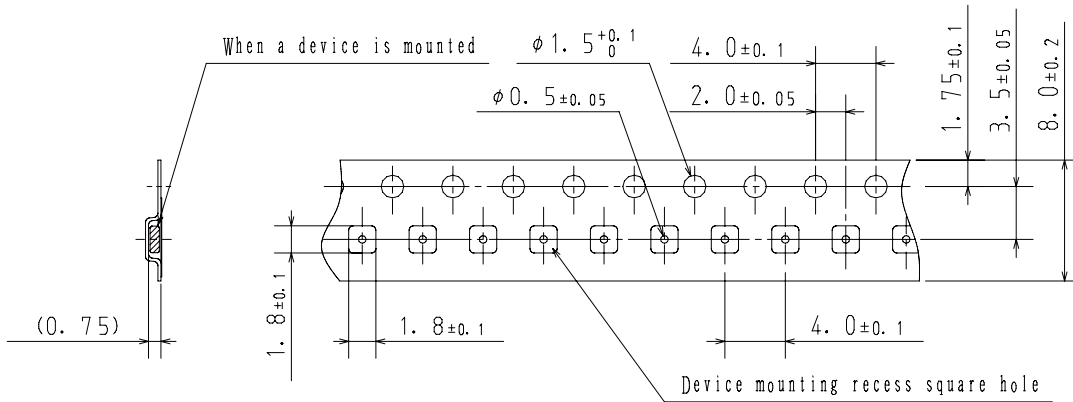
NOTE (1)

The LEAD FREE * description shows that the surface treatment of the terminal is lead free.

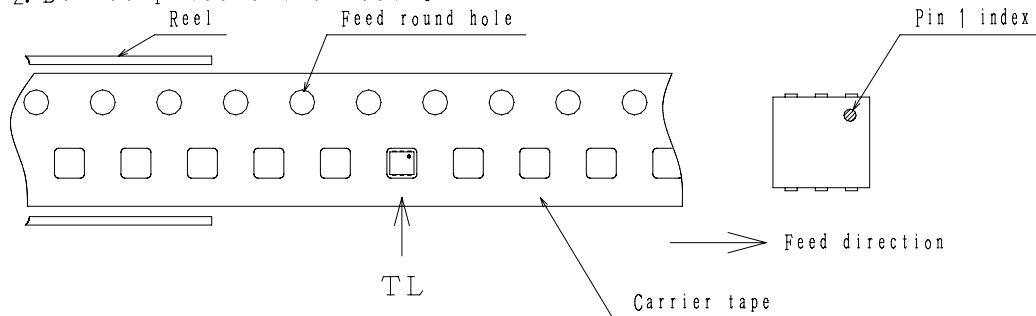
Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

2. Taping configuration

2-1. Carrier tape size (unit:mm)



2-2. Device placement direction

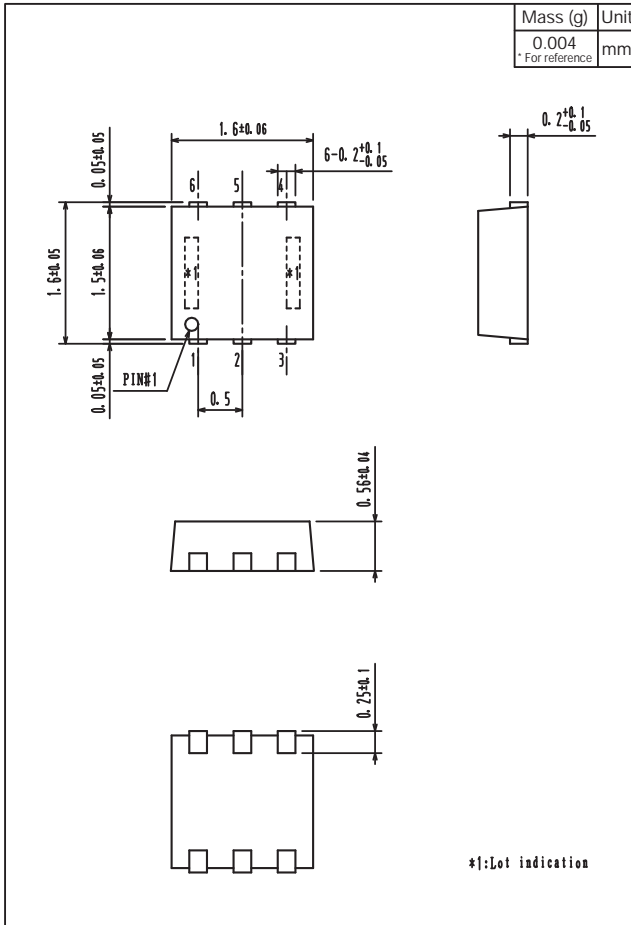


Those with pin 1 index on the feed hole side.....TL

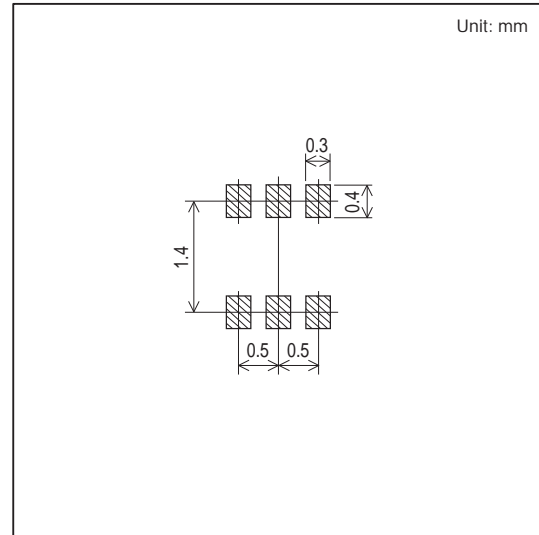
SCH1343

Outline Drawing

SCH1343-TL-H



Land Pattern Example



Note on usage : Since the SCH1343 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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