Vishay Semiconductors

HEXFRED[®] Ultrafast Soft Recovery Diode, 60 A



- · Fast recovery time characteristic
- · Electrically isolated base plate
- Large creepage distance between terminal
- · Simplified mechanical designs, rapid assembly
- UL approved file E78996
- Compliant to RoHS directive 2002/95/EC
- Designed for industrial level

DESCRIPTION

This SOT-227 modules with HEXFRED® rectifier are available in two basic configurations. They are the antiparallel and the parallel configurations. The antiparallel configuration (HFA120EA60) is used for simple series rectifier and high voltage application. The parallel configuration (HFA120FA60) is used for simple parallel rectifier and high current application. The semiconductor in the SOT-227 package is isolated from the copper base plate, allowing for common heatsinks and compact assemblies to be built. These modules are intended for general applications such as power supplies, battery chargers, electronic welders, motor control, DC chopper, and inverters.

| ABSOLUTE MAXIMUM RATINGS PER LEG | | | | | |
|--|---|--|-------------|-------|--|
| PARAMETER | SYMBOL | _ TEST CONDITIONS VALUES | | UNITS | |
| Cathode to anode voltage | V _R | | 600 | V | |
| Continuous forward current | I _F | T _C = 25 °C | 75 | | |
| | | T _C = 100 °C | 40 | | |
| Single pulse forward current | I _{FSM} T _J = 25 °C | | 800 | A | |
| Maximum repetitive forward current | I _{FRM} | Rated V_R , square wave, 20 kHz, T_C = 60 °C | 180 | | |
| RMS isolation voltage, any terminal to case | VISOL | t = 1 minute | 2500 | V | |
| Maximum power dissipation | P _D | T _C = 25 °C | 180 | w | |
| | | T _C = 100 °C | 71 | | |
| Operating junction and storage temperature range | TJ, T _{Stg} | | - 55 to 150 | °C | |

| ELECTRICAL SPECIFICATIONS PER LEG ($T_J = 25$ °C unless otherwise specified) | | | | | | | |
|--|--|---|------------|------|------|------|-------|
| PARAMETER | SYMBOL | TEST CONDITIONS | | MIN. | TYP. | MAX. | UNITS |
| Cathode to anode breakdown voltage | V _{BR} | I _R = 100 μA | | 600 | - | - | |
| Maximum forward voltage V _{FM} | V _{FM} | I _F = 60 A | See fig. 1 | - | 1.5 | 1.7 | v |
| | | I _F = 120 A | | - | 1.9 | 2.1 | |
| | I _F = 60 A, T _J = 125 °C | | - | 1.4 | 1.6 | | |
| Maximum reverse leakage current | | $V_{R} = V_{R}$ rated | See fig. 0 | - | 2.5 | 20 | μA |
| | IRM | T_J = 125 °C, V_R = 0.8 x V_R rated | See fig. 2 | - | 130 | 2000 | |
| Junction capacitance | CT | V _R = 200 V | See fig. 3 | - | 120 | 170 | pF |

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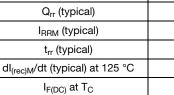
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www.vishay.com



COMPLIANT





PRODUCT SUMMARY

 V_{R}

V_F (typical) at 125 °C

SOT-227

600 V

1.4 V

270 nC

7.0 A

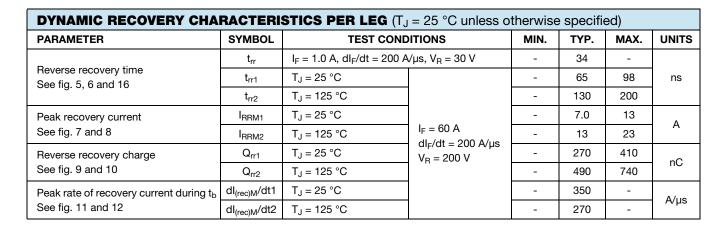
65 ns

270 A/µs

40 A at 100 °C



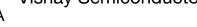
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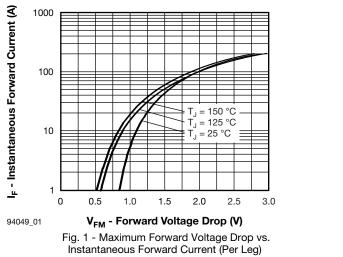


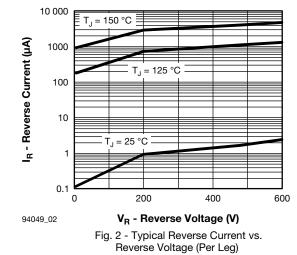
| THERMAL - MECHANICAL SPECIFICATIONS | | | | | | |
|---|---------------------|------|------|------|-------------|--|
| PARAMETER | SYMBOL | MIN. | TYP. | MAX. | UNITS | |
| Junction to case, single leg conducting | D | - | - | 0.70 | | |
| Junction to case, both legs conducting | – R _{thJC} | - | - | 0.35 | °C/W K/W | |
| Case to sink, flat, greased surface | R _{thCS} | - | 0.05 | - | 10.00 | |
| Weight | | - | 30 | - | g | |
| Mounting torque | | - | 1.3 | - | Nm | |

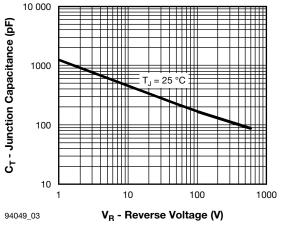


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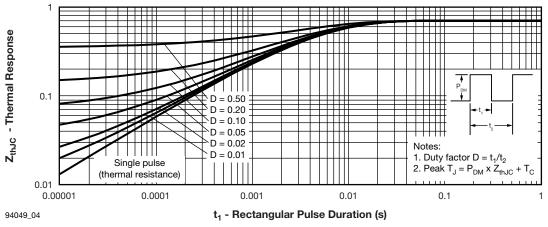


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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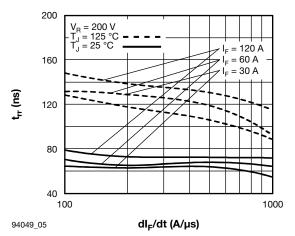


Fig. 5 - Typical Reverse Recovery Time vs. dI_F/dt (Per Leg)

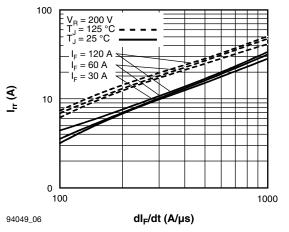
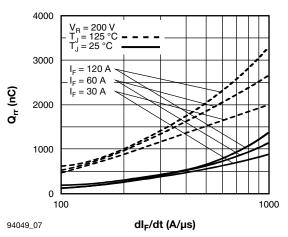
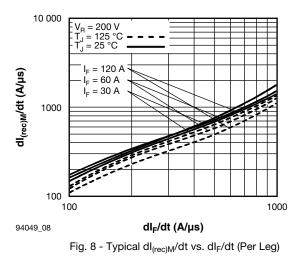


Fig. 6 - Typical Recovery Current vs. dl_F/dt (Per Leg)



SHA

Fig. 7 - Typical Stored Charge vs. dl_F/dt (Per Leg)





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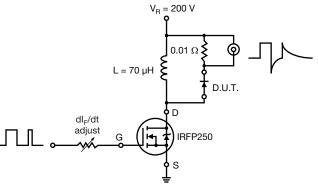
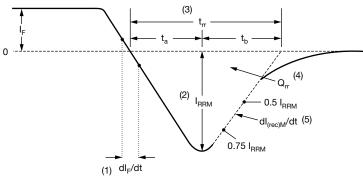
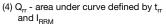


Fig. 9 - Reverse Recovery Parameter Test Circuit



(1) dl_F/dt - rate of change of current through zero crossing

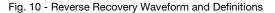


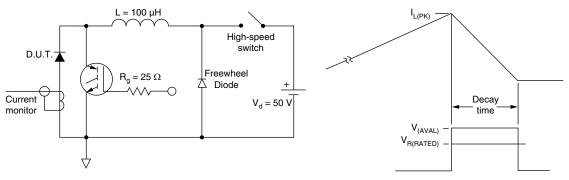
 $Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$

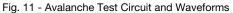
(2) I_{RRM} - peak reverse recovery current

(3) t_{rr} - reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.

(5) $dl_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}







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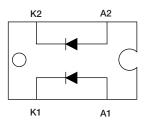
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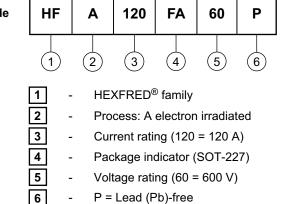


CIRCUIT CONFIGURATION



ORDERING INFORMATION TABLE

Device code



| LINKS TO RELATED DOCUMENTS | | |
|----------------------------|--------------------------|--|
| Dimensions | www.vishay.com/doc?95036 | |
| Packaging information | www.vishay.com/doc?95037 | |

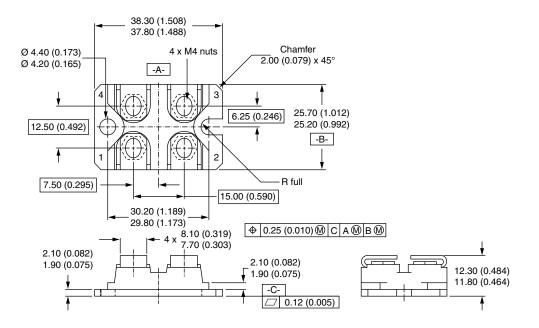


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SOT-227

DIMENSIONS in millimeters (inches)



Notes

- Dimensioning and tolerancing per ANSI Y14.5M-1982
- Controlling dimension: millimeter



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