



DMG2307L

P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(ON)} max	I _D max T _A = 25°C
	90mΩ @ V _{GS} = -10V	-3.8A
-30V	134mΩ @ V _{GS} = -4.5V	-3.1A

Description and Applications

This MOSFET has been designed to minimize the on-state resistance (R_{DS(on)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- General Purpose Interfacing Switch
- **Power Management Functions**
- Load Switch for Portable Devices

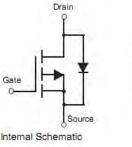
Features and Benefits

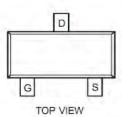
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green" Device (Note 2)
- Qualified to AEC-Q101 standards for High Reliability

Mechanical Data

- Case: SOT-23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See diagram
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Weight: 0.08 grams (approximate)







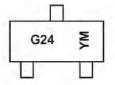
Ordering Information (Note 3)

Part Number	Case	Packaging
DMG2307L-7	SOT-23	3000Tape & Reel

1. No purposefully added lead. Notes:

Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com.
For packaging details, go to our website at http://www.diodes.com.

Marking Information



G24 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: W = 2009) M = Month (ex: 9 = September)

Year	200	9	2010		2011	20	12	2013	1	2014	2	2015
Code	W		Х		Y	Z		А		В	1972	C
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1 1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings $@T_A = 25^{\circ}C$ unless otherwise specified

Characteristic		Symbol	Value	Units	
Drain-Source Voltage		V _{DSS}	-30	V	
Gate-Source Voltage	V _{GSS}	±20	V		
Continuous Drain Current (Note 4) V_{GS} = -10V	Steady State	T _A = 25°C T _A = 70°C	I _D	-2.5 -2.0	А
Continuous Drain Current (Note 5) V_{GS} = -10V	Steady State	T _A = 25°C T _A = 70°C	ID	-3.8 -3.0	А
Continuous Drain Current (Note 5) V_{GS} = -10V	t≦10sec	T _A = 25°C T _A = 70°C	ID	-4.6 -3.6	А
Continuous Drain Current (Note 5) V_{GS} = -4.5V	Steady State	T _A = 25°C T _A = 70°C	ID	-3.1 -2.5	А
Pulsed Drain Current (Note 5)		IDM	-20	А	

Thermal Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 4)	PD	0.76	W
Thermal Resistance, Junction to Ambient (Note 4)	$R_{ ext{ heta}JA}$	159	°C/W
Total Power Dissipation (Note 5)	PD	1.36	W
Thermal Resistance, Junction to Ambient (Note 5)	$R_{ ext{ heta}JA}$	94	°C/W
Total Power Dissipation (Note 5) $t \leq 10 \text{sec}$	PD	1.9	W
Thermal Resistance, Junction to Ambient (Note 5) $t \leq 10sec$	$R_{\theta JA}$	65.8	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics @T_A = 25°C unless otherwise specified

1						-	-
Characteristic		Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)							
Drain-Source Breakdown Voltage		BV _{DSS}	-30	-	-	V	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current	$@T_c = 25^{\circ}C$	IDSS	-	-	-1.0	μA	$V_{DS} = -30V, V_{GS} = 0V$
Gate-Source Leakage		I _{GSS}	-	-	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 6)							
Gate Threshold Voltage		V _{GS(th)}	-1.0	-	-3.0	V	$V_{DS} = V_{GS}, I_D = -250 \mu A$
Static Drain-Source On-Resistance			-	70	90	mΩ	$V_{GS} = -10V, I_D = -2.5A$
		R _{DS} (ON)	-	105	134	111.5.2	$V_{GS} = -4.5V, I_D = -2.5A$
Forward Transfer Admittance		Y _{fs}	-	4.8	-	S	$V_{DS} = -10V, I_D = -2.5A$
Diode Forward Voltage (Note 6)		V_{SD}	-	-0.75	-1.0	V	$V_{GS} = 0V, I_{S} = -1A$
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance		Ciss	-	371.3	-	рF	
Output Capacitance		Coss	-	51.3	-	pF	[−] V _{DS} = -15V, V _{GS} = 0V, −f = 1.0MHz
Reverse Transfer Capacitance		C _{rss}	-	45.9	-	рF	1 = 1.000012
Gate Resistance		Rg	-	17	-	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$
Total Gate Charge (V _{GS} = -4.5V)		Qg	-	4.0	-	nC	
Total Gate Charge (V _{GS} = -10V)		Qg		8.2	-	nC	$V_{GS} = -10V, V_{DS} = -15V,$
Gate-Source Charge		Q _{gs}	-	0.9	-	nC	$I_D = -3A$
Gate-Drain Charge		Q _{gd}	-	1.2	-	nC	
Turn-On Delay Time		t _{D(on)}	-	4.8	-	ns	101
Turn-On Rise Time		tr	-	7.3	-	ns	$V_{DS} = -15V, V_{GS} = -10V,$
Turn-Off Delay Time		t _{D(off)}	-	22.4	-	ns	$R_L = 15\Omega, R_G = 6\Omega,$ $I_D = -1A$
Turn-Off Fall Time	t _f	-	13.4	-	ns	או טי	

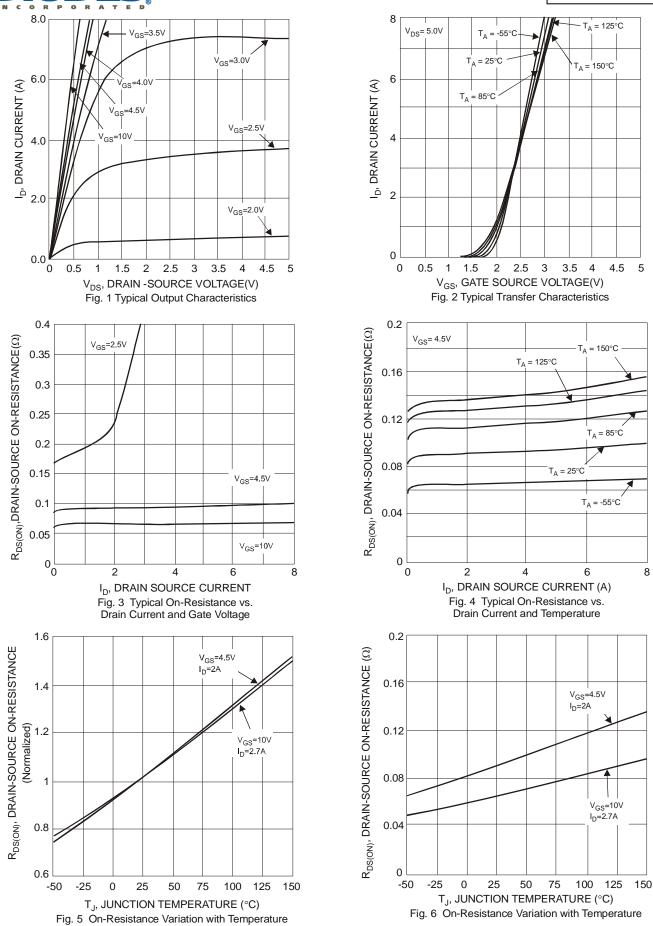
Notes:

Device mounted on FR-4 PCB, with minimum recommended pad layout.
Device mounted on FR-4 substrate PC board, 2oz copper, with thermal vias to bottom layer 1inch square copper plate.

Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to product testing.



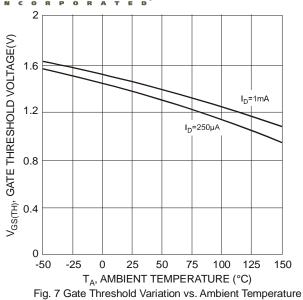
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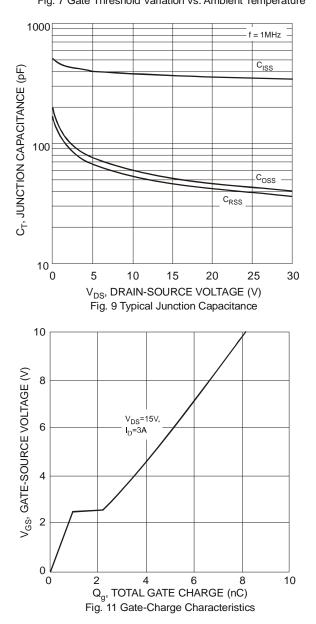


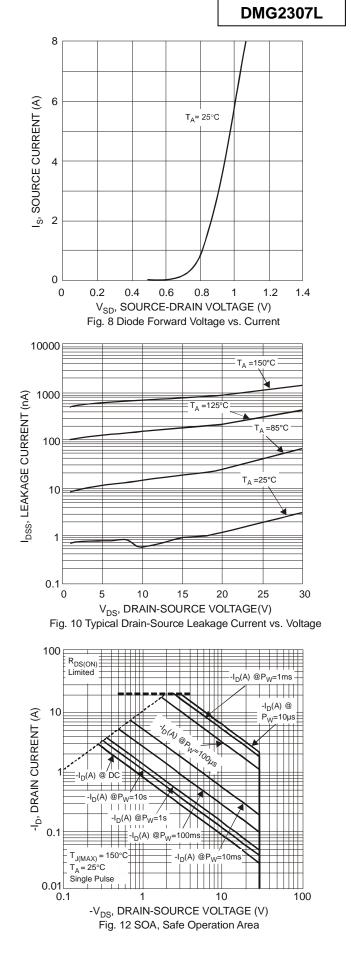
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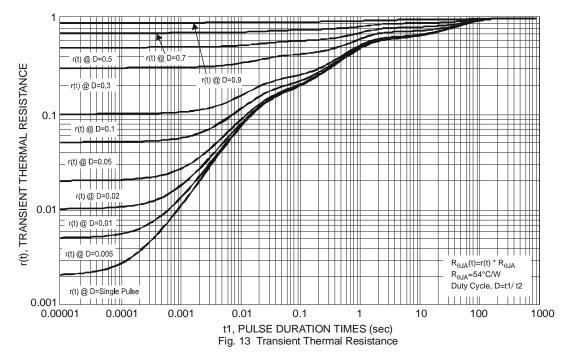






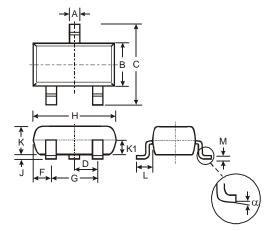
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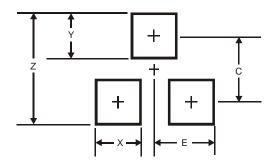


Package Outline Dimensions



SOT23								
Dim	Min	Max	Тур					
Α	0.37	0.51	0.40					
В	1.20	1.40	1.30					
С	2.30	2.50	2.40					
D	0.89	1.03	0.915					
F	0.45	0.60	0.535					
G	1.78	2.05	1.83					
Η	2.80	3.00	2.90					
J	0.013	0.10	0.05					
κ	0.903	1.10	1.00					
K1	-	-	0.400					
L	0.45	0.61	0.55					
Μ	0.085	0.18	0.11					
α	0°	8°	-					
All	Dimens	ions in	mm					

Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
С	2.0
E	1.35



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