

Vishay Semiconductors

Fast Sinterglass Diode

Features

- High temperature metallurgically bonded construction
- · Hermetically sealed package
- · Cavity-free glass passivated junction
- 1.0 ampere operation at T_{amb} = 55 °C with no thermal runaway
- · Fast switching for high efficiency



Mechanical Data

Case: Sintered glass case, JEDEC DO-204AP

Terminals: Solder plated axial leads, solderable per Mounting Position: Any

MILSTD- 750, Method 2026 Weight: 560 mg

Polarity: Color band denotes cathode end

Parts Table

Part	Type differentiation	Package
RG1A	V _{RRM} = 50 V	DO-204AP (G1)
RG1B	V _{RRM} = 100 V	DO-204AP (G1)
RG1D	V _{RRM} = 200 V	DO-204AP (G1)
RG1G	V _{RRM} = 400 V	DO-204AP (G1)
RG1J	V _{RRM} = 600 V	DO-204AP (G1)
RG1K	V _{RRM} = 800 V	DO-204AP (G1)
RG1M	V _{RRM} = 1000 V	DO-204AP (G1)

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RG1A to RG1M

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Absolute Maximum Ratings

 T_{amb} = 25 °C, unless otherwise specified

Parameter	Test condition	Part	Symbol	Value	Unit
Reverse voltage = Repetitive peak reverse	see electrical characteristics	RG1A	V _R =	50	V
voltage			V_{RRM}		
	see electrical characteristics	RG1B	V _R =	100	V
			V_{RRM}		
	see electrical characteristics	RG1D	V _R =	200	V
			V_{RRM}		
	see electrical characteristics	RG1G	V _R =	400	V
			V_{RRM}		
	see electrical characteristics	RG1J	V _R =	600	V
			V_{RRM}		
	see electrical characteristics	RG1K	V _R =	800	V
			V_{RRM}		
	see electrical characteristics	RG1M	V _R =	1000	V
			V_{RRM}		
Maximum average forward rectified current	0.375 " (9.5 mm) lead length at T _{amb} = 55 °C		I _{F(AV)}	1.0	Α
Peak forward surge current	8.3 ms single half sine-wave superimposed on rated load (JEDEC Method)		I _{FSM}	30	Α
Maximum full load reverse current	full cycle average 0.375 " (9.5 mm) lead length at T_{amb} = 25 °C		I _{R(AV)}	1.0	μΑ
	full cycle average 0.375 $^{\rm H}$ (9.5 mm) lead length at $\rm T_{amb} = 100~^{\circ}\rm C$		I _{R(AV)}	100	μА
Operating junction and storage temperature			T _J ,	- 55 to +	°C
range			T _{STG}	175	

Maximum Thermal Resistance

 T_{amb} = 25 °C, unless otherwise specified

Parameter		Symbol Value		Unit	
Typical thermal resistance 1)		$R_{\theta JA}$	55	K/W	

¹⁾ Thermal resistance from junction to ambient at 0.375 " (9.5 mm) lead length, P.C.B. mounted

Electrical Characteristics

 T_{amb} = 25 °C, unless otherwise specified

Parameter	Test condition	Part	Symbol	Тур.	Max	Unit
Maximum instantaneous forward voltage	I _F = 1 A		V _F		1.3	V
Reverse current	$V_R = V_{RRM}$		I _R		2.0	μΑ
Maximum reverse recovery time	$I_F = 0.5 \text{ A}, I_R = 1.0 \text{ A}, I_{rr} = 0.25 \text{ A}$	RG1A	t _{rr}		150	ns
	I _F = 0.5 A, I _R = 1.0 A, I _{rr} = 0.25 A	RG1B	t _{rr}		150	ns
	I _F = 0.5 A, I _R = 1.0 A, I _{rr} = 0.25 A	RG1D	t _{rr}		150	ns
	I _F = 0.5 A, I _R = 1.0 A, I _{rr} = 0.25 A	RG1G	t _{rr}		150	ns
	$I_F = 0.5 \text{ A}, I_R = 1.0 \text{ A}, I_{rr} = 0.25 \text{ A}$	RG1J	t _{rr}		200	ns
	$I_F = 0.5 \text{ A}, I_R = 1.0 \text{ A}, I_{rr} = 0.25 \text{ A}$	RG1K	t _{rr}		250	ns
	I _F = 0.5 A, I _R = 1.0 A, I _{rr} = 0.25 A	RG1M	t _{rr}		500	ns
Typical junction capacitance	V _R = 4.0 V, f = 1 MHz		CJ	15		pF

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Typical Characteristics ($T_{amb} = 25 \, ^{\circ}\text{C}$ unless otherwise specified)

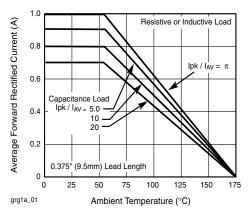


Figure 1. Forward Current Derating Curve

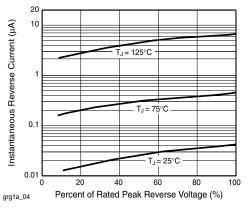


Figure 4. Typical Reverse Characteristics

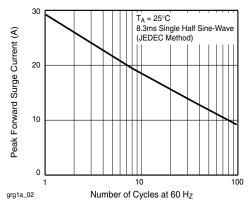


Figure 2. Maximum Non-Repetitive Peak Forward Surge Current

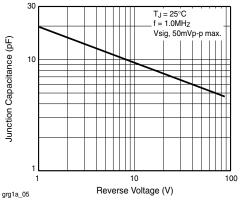


Figure 5. Typical Junction Capacitance

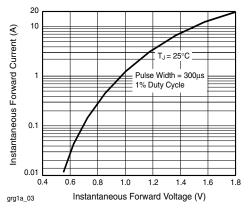
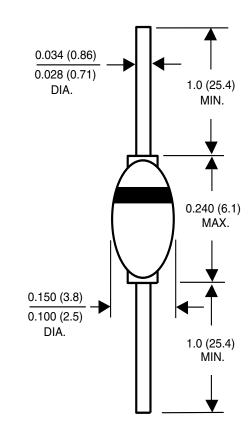


Figure 3. Typical Instantaneous Forward Characteristics

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Package Dimensions in Inches (mm)



17030

RG1A to RG1M



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Ozone Depleting Substances Policy Statement

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

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