

## BYW52, BYW53, BYW54, BYW55, BYW56

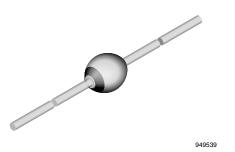
**Vishay Semiconductors** 

ROHS COMPLIANT

HALOGEN

FREE

### **Standard Avalanche Sinterglass Diode**



#### **MECHANICAL DATA**

#### Case: SOD-57

**Terminals:** plated axial leads, solderable per MIL-STD-750, method 2026

Polarity: color band denotes cathode end

Mounting position: any

Weight: approx. 369 mg

#### FEATURES

- Controlled avalanche characteristics
- Glass passivated junction
- Hermetically sealed package
- Low reverse current
- High surge current loading
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### **APPLICATIONS**

• Rectification, general purpose

ORDERING INFORMATION (Example)					
DEVICE NAME	ORDERING CODE TAPED UNITS MINIMUM ORDER QUA				
BYW56	BYW56-TR	5000 per 10" tape and reel	25 000		
BYW56	BYW56-TAP	5000 per ammopack	25 000		

PARTS TABLE				
PART	TYPE DIFFERENTIATION	PACKAGE		
BYW52	$V_{R} = 200 \text{ V}; \text{ I}_{F(AV)} = 2 \text{ A}$	SOD-57		
BYW53	V <sub>R</sub> = 400 V; I <sub>F(AV)</sub> = 2 A	SOD-57		
BYW54	$V_{R} = 600 \text{ V}; \text{ I}_{F(AV)} = 2 \text{ A}$	SOD-57		
BYW55	$V_{R} = 800 \text{ V}; \text{ I}_{F(AV)} = 2 \text{ A}$	SOD-57		
BYW56	V <sub>R</sub> = 1000 V; I <sub>F(AV)</sub> = 2 A	SOD-57		

<b>ABSOLUTE MAXIMUM RATINGS</b> ( $T_{amb} = 25 \text{ °C}$ , unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT		
		BYW52	$V_{R} = V_{RRM}$	200	V		
	See electrical characteristics	BYW53	$V_{R} = V_{RRM}$	400	V		
Reverse voltage = repetitive peak reverse voltage		BYW54	$V_{R} = V_{RRM}$	600	V		
levelse voltage		BYW55	$V_{R} = V_{RRM}$	800	V		
		BYW56	$V_{R} = V_{RRM}$	1000	V		
Peak forward surge current	t <sub>p</sub> = 10 ms, half sine wave		I <sub>FSM</sub>	50	А		
Repetitive peak forward current			I <sub>FRM</sub>	12	А		
Average forward current	φ = 180 °		I <sub>F(AV)</sub>	2	А		
Pulse avalanche peak power	$t_p$ = 20 $\mu s$ half sine wave, $T_j$ = 175 °C		P <sub>R</sub>	1000	W		
Pulse energy in avalanche mode, non repetitive (inductive load switch off)	I <sub>(BR)R</sub> = 1 Α, Τ <sub>j</sub> = 175 °C		E <sub>R</sub>	20	mJ		
i <sup>2</sup> t-rating			i <sup>2</sup> t	8	A <sup>2</sup> s		
Junction and storage temperature range			$T_j = T_{stg}$	- 55 to + 175	°C		

Rev. 1.8, 11-Sep-12

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Document Number: 86049

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<b>MAXIMUM THERMAL RESISTANCE</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Junction ambient	Lead length I = 10 mm, $T_L$ = constant	R <sub>thJA</sub>	45	K/W	
Sunction ambient	On PC board with spacing 25 mm	R <sub>thJA</sub>	100	K/W	

<b>ELECTRICAL CHARACTERISTICS</b> ( $T_{amb}$ = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I <sub>F</sub> = 1 A	V <sub>F</sub>	-	0.9	1	V
Reverse current	$V_{R} = V_{RRM}$	I <sub>R</sub>	-	0.1	1	μA
	$V_{R} = V_{RRM}, T_{j} = 100 \ ^{\circ}C$	I <sub>R</sub>	-	5	10	μA
Breakdown voltage	$I_R = 100 \ \mu A, t_p/T = 0.01, t_p = 0.3 \ ms$	V <sub>(BR)</sub>	-	-	1600	V
Diode capacitance	$V_R = 4 V, f = 1 MHz$	CD	-	18	-	pF
Reverse recovery time	I <sub>F</sub> = 0.5 A, I <sub>R</sub> = 1 A, i <sub>R</sub> = 0.25 A	t <sub>rr</sub>	-	-	4	μs
	$I_F = 1 \text{ A}, \text{ dI/dt} = 5 \text{ A/}\mu\text{s}, V_R = 50 \text{ V}$	t <sub>rr</sub>	-	-	4	μs
Reverse recovery charge	$I_F = 1 \text{ A}, \text{ dI/dt} = 5 \text{ A/}\mu\text{s}$	Q <sub>rr</sub>	-	-	200	nC

### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

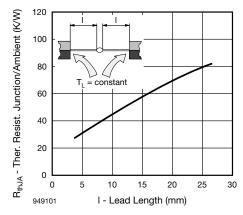


Fig. 1 - Typ. Thermal Resistance vs. Lead Length

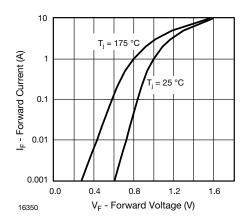


Fig. 2 - Forward Current vs. Forward Voltage

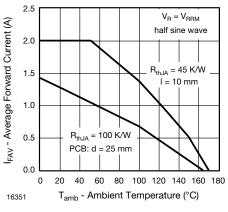


Fig. 3 - Max. Average Forward Current vs. Ambient Temperature

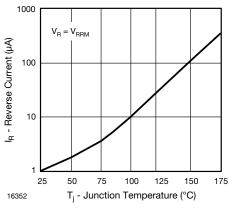


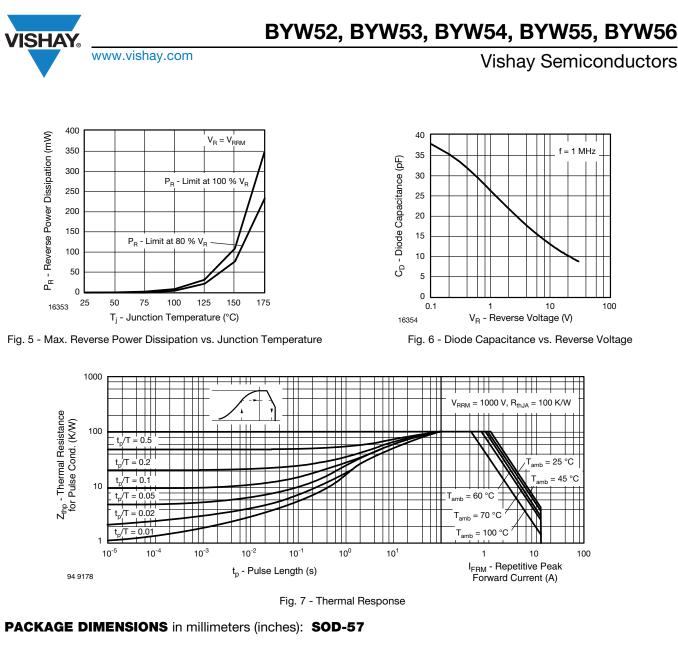
Fig. 4 - Reverse Current vs. Junction Temperature

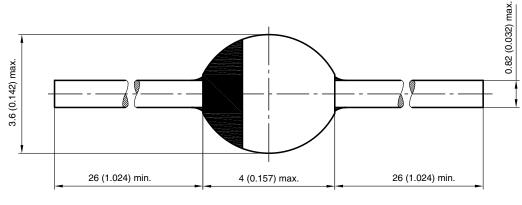
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20543 Rev. 3 - Date: 09.February 2005 Document no.:6.563-5006.3-4

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