

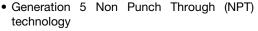
INT-A-PAK "Half-Bridge" (Ultrafast Speed IGBT), 209 A



INT-A-PAK

PRODUCT SUMMARY				
V _{CES}	600 V			
I _C DC	209 A			
V _{CE(on)} at 200 A, 25 °C	2.6 V			

FEATURES





· Ultrafast: Optimized for hard switching speed 8 kHz to 60 kHz

- Low V_{CE(on)}
- 10 µs short circuit capability
- Square RBSOA
- Positive V_{CE(on)} temperature coefficient
- HEXFRED® antiparallel diode with ultrasoft reverse recovery characteristics
- Industry standard package
- Al₂O₃ DBC
- UL approved file E78996



- · Designed for industrial level
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

BENEFITS

- Benchmark efficiency for UPS and welding application
- · Rugged transient performance
- Direct mounting on heatsink
- Very low junction to case thermal resistance

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Collector to emitter voltage	V _{CES}		600	V	
Continuous collector current	1	T _C = 25 °C	209		
	Ic	T _C = 80 °C	142		
Pulsed collector current	I _{CM}		400	٨	
Clamped inductive load current	I _{LM}		400	A	
Diode continuous forward current	I _F	T _C = 25 °C	178		
		T _C = 80 °C	121		
Gate to emitter voltage	V _{GE}		± 20	V	
Maximum power dissipation	Б.	T _C = 25 °C	781	10/	
	P _D	T _C = 80 °C	438	W	
Isolation voltage	V _{ISOL}	Any terminal to case, t = 1 minute	2500	V	



ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Collector to emitter breakdown voltage	V _{BR(CES)}	$V_{GE} = 0 \text{ V}, I_{C} = 500 \mu\text{A}$	600	-	-	
	V _{CE(on)}	V _{GE} = 15 V, I _C = 100 A	-	1.95	2.1	V
Collector to emitter valtage		V _{GE} = 15 V, I _C = 200 A	-	2.6	2.84	
Collector to emitter voltage		V _{GE} = 15 V, I _C = 100 A, T _J = 125 °C	-	2.28	2.5	
		V _{GE} = 15 V, I _C = 200 A, T _J = 125 °C	-	3.14	3.48	
Gate threshold voltage	V _{GE(th)}	$V_{CE} = V_{GE}$, $I_C = 500 \mu A$	3	4.2	6	
Collector to emitter leakage current	I _{CES}	$V_{GE} = 0 \text{ V}, V_{CE} = 600 \text{ V}$	-	0.005	0.2	mA
		$V_{GE} = 0 \text{ V}, V_{CE} = 600 \text{ V}, T_{J} = 150 ^{\circ}\text{C}$	-	0.01	15	IIIA
Diode forward voltage drop	V _{FM}	I _C = 100 A	-	1.39	1.78	
		I _C = 200 A	-	1.64	2.2	V
		I _C = 100 A, T _J = 125 °C	-	1.32	1.69	
		I _C = 200 A, T _J = 125 °C	-	1.67	2.30	
Gate to emitter leakage current	I _{GES}	V _{GE} = ± 20 V	-	-	± 200	nA

	SYMBOL	= 25 °C unless otherwise specified		TVD	MAN	LINUTO	
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Turn-on switching loss	E _{on}		-	3.65	=		
Turn-off switching loss	E _{off}	$I_C = 200$ A, $V_{CC} = 360$ V, $V_{GE} = 15$ V, $R_0 = 10$ Ω, $L = 200$ μH, $T_1 = 25$ °C	-	6.9	-		
Total switching loss	E _{tot}		-	10.55	-		
Turn-on switching loss	E _{on}		-	3.8	-	- mJ	
Turn-off switching loss	E _{off}		-	7.8	-]	
Total switching loss	E _{tot}		-	11.6	-		
Turn-on delay time	t _{d(on)}	$I_C = 200$ A, $V_{CC} = 360$ V, $V_{GE} = 15$ V, $R_0 = 10$ Ω, $L = 200$ μH, $T_{L} = 125$ °C	-	507	-		
Rise time	t _r	· · · · · · · · · · · · · · · · · · ·	-	133	-		
Turn-off delay time	t _{d(off)}		-	538	-	ns	
Fall time	t _f		-	92	-		
Reverse bias safe operating area	RBSOA	$T_J = 150 ^{\circ}\text{C}, I_C = 400 \text{A}, \ R_g = 27 \Omega, V_{GE} = 15 \text{V to } 0$	Fullsquare				
Short circuit safe operating area	SCSOA	$\begin{split} T_J &= 150~^{\circ}\text{C}, \ V_{CC} = 400~\text{V}, \ V_P = 600~\text{V}, \\ R_g &= 27~\Omega, \ V_{GE} = 15~\text{V to } 0 \end{split}$	10	-	-		
Diode reverse recovery time	t _{rr}		-	226	260	ns	
Diode peak reverse current	I _{rr}	I _F = 50 A, dI _F /dt = 200 A/μs, V _{CC} = 400 V, T _J = 25 °C	-	17	20	Α	
Diode recovery charge	Q _{rr}	00 25 1, 10 =1 5	-	1900	2600	nC	
Diode reverse recovery time	t _{rr}		-	290	330	ns	
Diode peak reverse current	I _{rr}	$I_F = 50 \text{ A}, dI_F/dt = 200 \text{ A/}\mu\text{s},$ $V_{CC} = 400 \text{ V}, T_{cl} = 125 \text{ °C}$	-	25	30	Α	
Diode recovery charge	Q _{rr}	130 130 1, 10 120 0	=	3600	5000	nC	



THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNITS	
Operating junction and storage temperature range		T _J , T _{Stg}	- 40	-	150	°C	
Junction to case per leg	IGBT	- R _{thJC}	-	0.13	0.16	°C/W	
	Diode		-	0.19	0.32		
Case to sink per module		R _{thCS}	-	0.1	-		
Mounting torque	case to heatsink		-	-	4	Nm	
	case to terminal 1, 2, 3		-	-	3	INIII	
Weight			-	185	-	g	

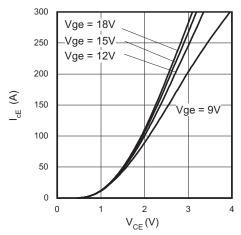


Fig. 1 - Typical IGBT Output Characteristics $T_J = 25~^{\circ}\text{C}, \, t_p = 500~\mu\text{s}$

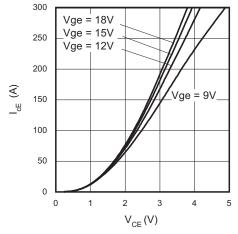


Fig. 2 - Typical IGBT Output Characteristics $T_J = 125~^{\circ}C,\, t_p = 500~\mu s$

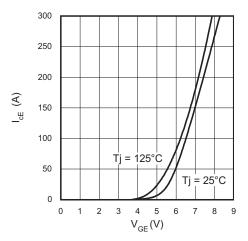


Fig. 3 - Typical Transfer Characteristics $V_{CE} = 20 \text{ V}, \, t_p = 500 \, \mu \text{s}$

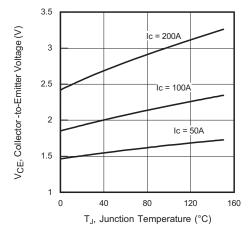


Fig. 4 - Typical Collector to Emitter Voltage vs. Junction Temperature

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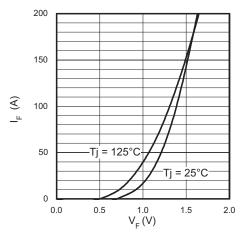


Fig. 5 - Diode Forward Characteristics, $t_p = 500 \mu s$

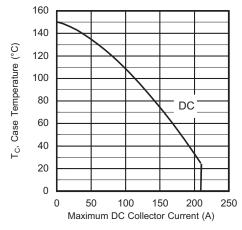


Fig. 6 - Maximum Collector Current vs. Case Temperature

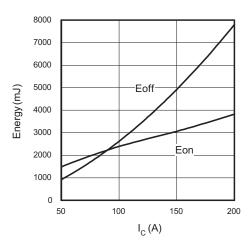


Fig. 7 - Typical Energy Loss vs. I_C T_J = 125 °C, L = 200 μ H, V_{CC} = 360 V, R_q = 10 Ω , V_{GE} = 15 V

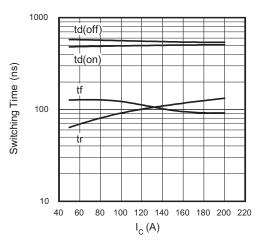
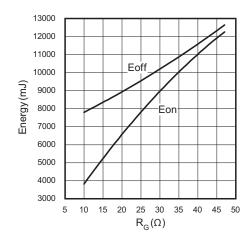


Fig. 8 - Typical Switching Time vs. I_C T_J = 125 °C, L = 200 μ H, V_{CC} = 360 V, R_g = 10 Ω , V_{GE} = 15 V



 $\begin{aligned} &\text{Fig. 9 - Typical Energy Loss vs. } R_g \\ T_J &= 125 \text{ ^{\circ}C}, \text{ L} = 200 \text{ } \mu\text{H, V}_{CC} = 360 \text{ V}, \\ I_{CE} &= 200 \text{ A, V}_{GE} = 15 \text{ V} \end{aligned}$

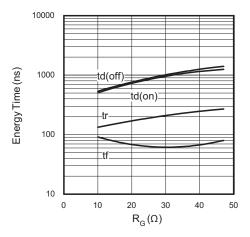


Fig. 10 - Typical Switching Time vs. R_g T_J = 125 °C, L = 200 μ H, V_{CC} = 360 V, I_{CE} = 200 A, V_{GE} = 15 V

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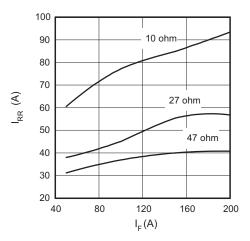


Fig. 11 - Typical Diode I_{rr} vs. I_F $T_J = 125 \, ^{\circ}C$

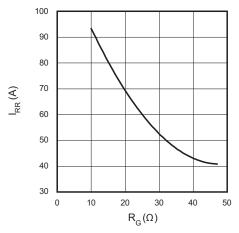


Fig. 12 - Typical Diode I_{rr} vs. R_g $T_J = 125$ °C, $I_F = 200$ A

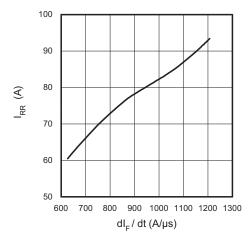


Fig. 13 - Typical Diode I_{rr} vs. dI_F/dt T_J = 125 °C, V_{CC} = 360 V, I_F = 200 A, V_{GE} = 15 V

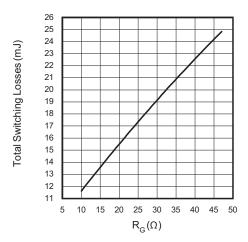


Fig. 14 - Typical Switching Losses vs. Gate Resistance T_J = 125 °C, L = 200 μ H, R_g = 10 Ω , V_{CC} = 360 V, V_{GE} = 15 V

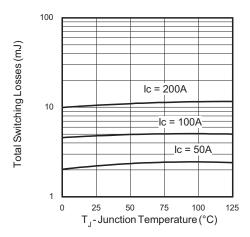
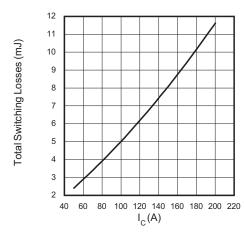


Fig. 15 - Typical Switching Losses vs. Junction Temperature; L = 200 μ H, R_q = 10 Ω , V_{CC} = 360 V, V_{GE} = 15 V



 $\label{eq:fig:sigma} Fig.~16 - Typical Switching Losses vs. \\ Collector to Emitter Current; \\ T_J = 125 \ ^{\circ}C, R_{g1} = 10 \ \Omega, \ R_{g2} = 0 \ \Omega, \ V_{CC} = 360 \ V, \ V_{GE} = 15 \ V \\ \end{array}$



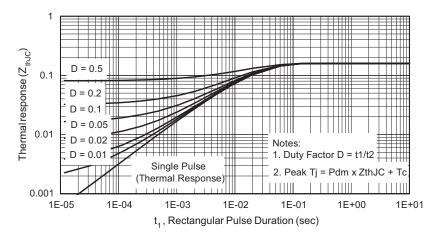


Fig. 17 - Maximum Transient Thermal Impedance, Junction to Case (IGBT)

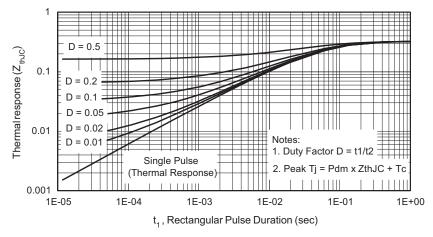
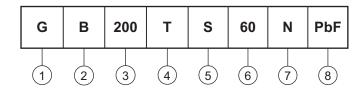


Fig. 18 - Maximum Transient Thermal Impedance, Junction to Case (HEXFRED®)

ORDERING INFORMATION TABLE

Device code



1 - Insulated Gate Bipolar Transistor (IGBT)

2 - B = IGBT Generation 5 NPT

3 - Current rating (200 = 200 A)

Circuit configuration (T = Half-bridge)

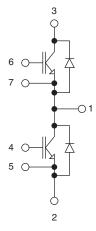
5 - Package indicator (S = INT-A-PAK)

6 - Voltage rating (60 = 600 V)

7 - Speed/type (N = Ultrafast IGBT)

8 - Lead (Pb)-free

CIRCUIT CONFIGURATION

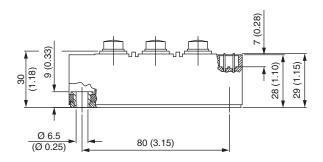


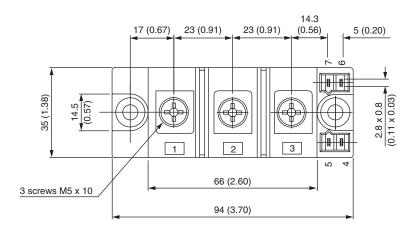
LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95543		

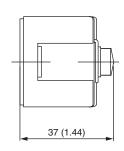


INT-A-PAK IGBT

DIMENSIONS in millimeters (inches)









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