

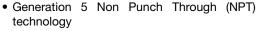
INT-A-PAK "Half-Bridge" (Ultrafast Speed IGBT), 138 A



INT-A-PAK

PRODUCT SUMMARY				
V _{CES}	600 V			
I _C DC	138 A			
V _{CE(on)} at 150 A, 25 °C	2.64 V			

FEATURES





 Ultrafast: Optimized for hard switching speed 8 kHz to 60 kHz

RoHS COMPLIANT

- Low V_{CE(on)}
- 10 µs short circuit capability
- Square RBSOA
- Positive V_{CE(on)} temperature coefficient
- HEXFRED® antiparallel diode with ultrasoft reverse recovery characteristics
- · Industry standard package
- Al₂O₃ DBC
- UL approved file E78996



- · Designed for industrial level
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

BENEFITS

- Benchmark efficiency for UPS and welding application
- Rugged transient performance
- · Direct mounting on heatsink
- Very low junction to case thermal resistance

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Collector to emitter voltage	V _{CES}		600	V	
Continuous collector current		T _C = 25 °C	138		
	I _C	T _C = 80 °C	93		
Pulsed collector current	I _{CM}		300	A	
Clamped inductive load current	I _{LM}		300	A	
Diode continuous forward current		T _C = 25 °C	178		
	I _F	T _C = 80 °C	121	1	
Gate to emitter voltage	V_{GE}		± 20	V	
Maximum power dissipation	В	T _C = 25 °C	500	W	
	P _D	T _C = 80 °C	280]	
Isolation voltage	V _{ISOL}	Any terminal to case, t = 1 min	2500	V	



ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Collector to emitter breakdown voltage	V _{BR(CES)}	$V_{GE} = 0 \text{ V}, I_{C} = 500 \mu\text{A}$	600	-	-		
Collector to emitter voltage	V _{CE(on)}	V _{GE} = 15 V, I _C = 100 A	-	2.2	2.7	V	
		V _{GE} = 15 V, I _C = 150 A	-	2.64	3		
		V _{GE} = 15 V, I _C = 100 A, T _J = 125 °C	-	2.68	3.11		
		V _{GE} = 15 V, I _C = 150 A, T _J = 125 °C	-	3.25	3.79		
Gate threshold voltage	V _{GE(th)}	$V_{CE} = V_{GE}, I_{C} = 500 \mu A$	3	4.2	6		
Collector to emitter leakage current	I _{CES}	V _{GE} = 0 V, V _{CE} = 600 V	-	0.01	0.2	mA	
		V _{GE} = 0 V, V _{CE} = 600 V, T _J = 150 °C	-	7.5	15	IIIA	
Diode forward voltage drop	V _{FM}	I _C = 100 A	-	1.39	1.78		
		I _C = 150 A	-	1.52	1.91	V	
		I _C = 100 A, T _J = 125 °C	-	1.31	1.72		
		I _C = 150 A, T _J = 125 °C	-	1.49	2.05		
Gate to emitter leakage current	I _{GES}	V _{GE} = ± 20 V	-	-	± 200	nA	

SWITCHING CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Turn-on switching loss	E _{on}		-	2.0	-		
Turn-off switching loss	E _{off}	$\begin{bmatrix} I_C = 150 \text{ A}, V_{CC} = 360 \text{ V}, V_{GE} = 15 \text{ V}, \\ R_g = 10 \Omega, L = 200 \mu H, T_{.I} = 25 °C \end{bmatrix}$	-	3.9	-		
Total switching loss	E _{tot}	9 , , , , , , , , , , , , , , , , , , ,	-	5.9	-		
Turn-on switching loss	E _{on}		-	2.42	-	mJ	
Turn-off switching loss	E _{off}		-	4.2	-	1	
Total switching loss	E _{tot}		-	6.62	-		
Turn-on delay time	t _{d(on)}	$\begin{matrix} I_{C} = 150 \text{ A}, V_{CC} = 360 \text{ V}, V_{GE} = 15 \text{ V}, \\ R_{g} = 10 \Omega, L = 200 \mu H, T_{J} = 125 °C \end{matrix}$	-	390	-		
Rise time	t _r	9 , , , , , , , , , , , , , , , , , , ,	-	100	-]	
Turn-off delay time	t _{d(off)}		-	402	-	ns	
Fall time	t _f		-	80	-		
Reverse bias safe operating area	RBSOA	$T_J = 150 ^{\circ}\text{C}, I_C = 300 \text{A}, \ R_g = 10 \Omega, V_{GE} = 15 \text{V to } 0$	Fullsquare				
Short circuit safe operating area	SCSOA	$T_J = 150 ^{\circ}\text{C}, V_{CC} = 400 \text{V}, V_P = 600 \text{V}, R_g = 10 \Omega, V_{GE} = 15 \text{V} \text{ to } 0$	10	-	-		
Diode reverse recovery time	t _{rr}		-	226	260	ns	
Diode peak reverse current	I _{rr}	$I_F = 50 \text{ A}, dI_F/dt = 200 \text{ A/}\mu\text{s},$ $V_{CC} = 400 \text{ V}, T_{LI} = 25 \text{ °C}$	-	17	20	Α	
Diode recovery charge	Q _{rr}		-	1900	2600	nC	
Diode reverse recovery time	t _{rr}		-	290	330	ns	
Diode peak reverse current	I _{rr}	I _F = 50 A, dI _F /dt = 200 A/μs, V _{CC} = 400 V, T _{LI} = 125 °C	-	25	30	Α	
Diode recovery charge	Q _{rr}	1 35, -0	-	3600	5000	nC	



THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNITS	
Operating junction and storage temperature range		T _J , T _{Stg}	- 40	-	150	°C	
Junction to case per leg	IGBT	R_{thJC}	-	0.17	0.25	°C/W	
	Diode		-	0.19	0.32		
Case to sink per module		R _{thCS}	-	0.1	-		
Mounting torque	case to heatsink		-	-	4	- Nm	
	case to terminal 1, 2, 3		-	-	3		
Weight			-	185	-	g	

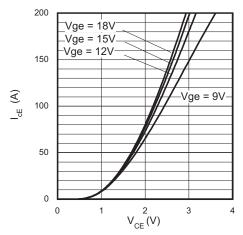


Fig. 1 - Typical IGBT Output Characteristics $T_J = 25~^{\circ}\text{C}, \, t_p = 500~\mu\text{s}$

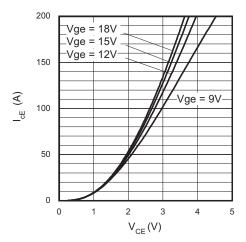


Fig. 2 - Typical IGBT Output Characteristics $T_J = 125~^{\circ}C,\, t_p = 500~\mu s$

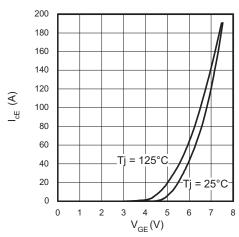


Fig. 3 - Typical Transfer Characteristics V_{CE} = 20 V, t_p = 500 μs

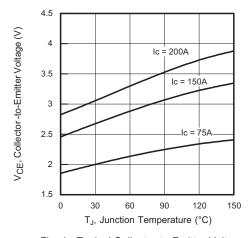


Fig. 4 - Typical Collector to Emitter Voltage vs. Junction Temperature $V_{GE} = 15 \text{ V}, 500 \text{ } \mu \text{s} \text{ pulse width}$

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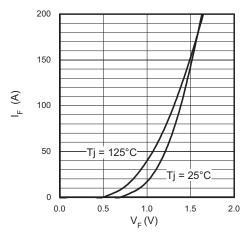


Fig. 5 - Diode Forward Characteristics, $t_p = 500 \mu s$

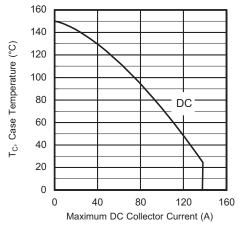


Fig. 6 - Maximum Collector Current vs. Case Temperature

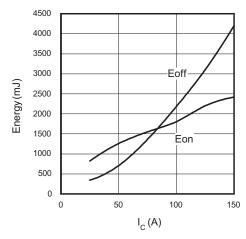


Fig. 7 - Typical Energy Loss vs. I_C T_J = 125 °C, L = 200 μ H, V_{CC} = 360 V, R_q = 10 Ω , V_{GE} = 15 V

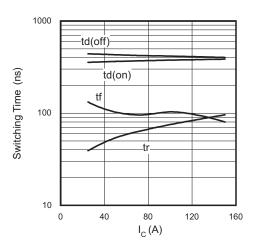
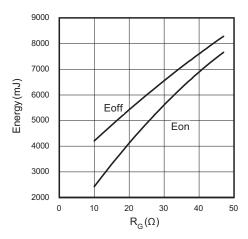


Fig. 8 - Typical Switching Time vs. I_C T_J = 125 °C, L = 200 μH , V_{CC} = 360 V, R_g = 10 Ω , V_{GE} = 15 V



 $\begin{aligned} &\text{Fig. 9 - Typical Energy Loss vs. R}_g \\ &\text{T}_J = 125 \text{ °C, L} = 200 \ \mu\text{H, V}_{CC} = 360 \ \text{V,} \\ &\text{I}_{CE} = 150 \ \text{A, V}_{GE} = 15 \ \text{V} \end{aligned}$

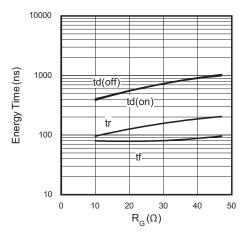


Fig. 10 - Typical Switching Time vs. R_g , $T_J = 125$ °C, $L = 200~\mu H,~V_{CC} = 360~V,~I_{CE} = 150~A,~V_{GE} = 15~V$

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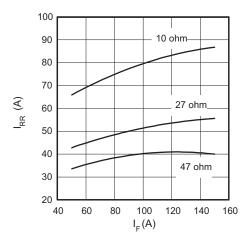


Fig. 11 - Typical Diode I_{rr} vs. I_F $T_J = 125 \, ^{\circ}C$

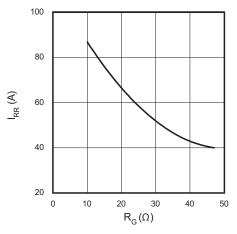


Fig. 12 - Typical Diode I_{rr} vs. R_g $T_J = 125$ °C, $I_F = 150$ A

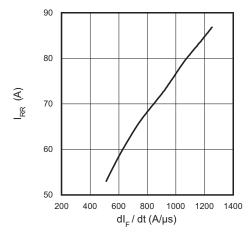


Fig. 13 - Typical Diode I_{rr} vs. dI_F/dt T_J = 125 °C, V_{CC} = 360 V, I_F = 150 A, V_{GE} = 15 V

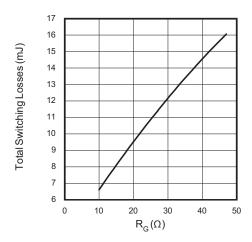


Fig. 14 - Typical Switching Losses vs. Gate Resistance, T_J = 125 °C, L = 200 $\mu H,~R_g$ = 10 $\Omega,~V_{CC}$ = 360 V, V_{GE} = 15 V

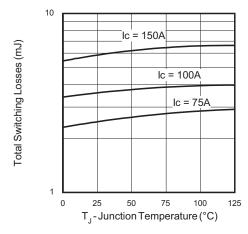


Fig. 15 - Typical Switching Losses vs. Junction Temperature; L = 200 μ H, R $_g$ = 10 Ω , V $_{CC}$ = 360 V, V $_{GE}$ = 15 V

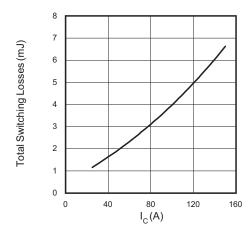
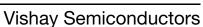


Fig. 16 - Typical Switching Losses vs. Collector to Emitter Current; T_J = 125 °C, R_{g1} = 10 Ω , R_{g2} = 0 Ω , V_{CC} = 360 V, V_{GE} = 15 V





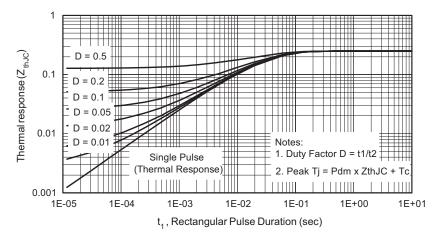


Fig. 17 - Maximum Transient Thermal Impedance, Junction to Case (IGBT)

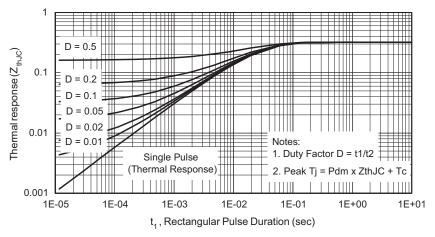
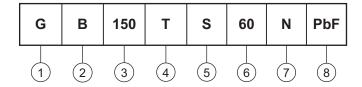


Fig. 18 - Maximum Transient Thermal Impedance, Junction to Case (HEXFRED®)

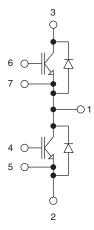
ORDERING INFORMATION TABLE

Device code



- Insulated Gate Bipolar Transistor (IGBT)
- 2 B = IGBT Generation 5 NPT
- Current rating (150 = 150 A)
- 4 Circuit configuration (T = Half-bridge)
- 5 Package indicator (S = INT-A-PAK)
- 6 Voltage rating (60 = 600 V)
- 7 Speed/type (N = Ultrafast IGBT)
- 8 Lead (Pb)-free

CIRCUIT CONFIGURATION

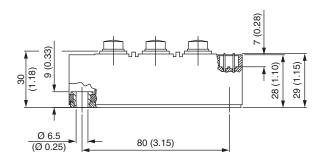


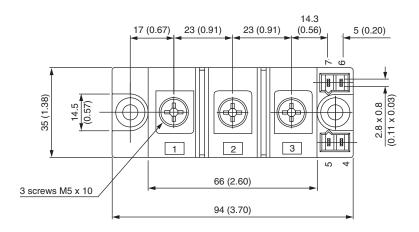
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95543			

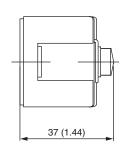


INT-A-PAK IGBT

DIMENSIONS in millimeters (inches)









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