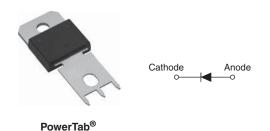


E<sub>AS</sub>

### Vishay Semiconductors

# Schottky Rectifier, 100 A



PRODUCT SUMMARY				
Package	PowerTab <sup>®</sup>			
I <sub>F(AV)</sub> 100 A				
$V_{R}$	45 V			
V <sub>F</sub> at I <sub>F</sub>	0.71 V			
I <sub>RM</sub>	320 mA at 125 °C			
T <sub>J</sub> max.	150 °C			
Diode variation	Single die			

40 mJ

#### **FEATURES**







· Continuous high current operation

Guard ring for enhanced ruggedness and long term reliability



COMPLIANT

- Screw mounting only
- Designed and qualified according to JEDEC-JESD47
- PowerTab<sup>®</sup> package
- Compliant to RoHS Directive 2002/95/EC

### **DESCRIPTION**

The VS-100BGQ045 Schottky rectifier has been optimized for ultralow forward voltage drop specifically for low voltage output in high current AC/DC power supplies.

The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
	Rectangular waveform	100	A	
I <sub>F(AV)</sub>	T <sub>C</sub>	97	°C	
V <sub>RRM</sub>		45	V	
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	4400	A	
V	100 A <sub>pk</sub> (typical)	0.65	V	
$V_{F}$	T <sub>J</sub>	150	°C	
TJ	Range	- 55 to 150	°C	

VOLTAGE RATINGS				
PARAMETER	SYMBOL	100BGQ045	UNITS	
Maximum DC reverse voltage	$V_{R}$	45	V	
Maximum working peak reverse voltage	$V_{RWM}$	45	V	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 97 °C, rectangular waveform		100	Α
Maximum peak one cycle	I	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	4400	Α
non-repetitive surge current	I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse		830	A
Non-repetitive avalanche energy	E <sub>AS</sub>	$T_J = 25  ^{\circ}\text{C},  I_{AS} = 6  \text{A},  L = 2  \text{mH}$		40	mJ
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s  Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		6	Α



ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
	V <sub>FM</sub> <sup>(1)</sup>	50 A	T <sub>J</sub> = 25 °C	0.54	0.58	
Forward voltage drop		100 A		0.69	0.77	V
Torward voitage drop		50 A	T <sub>J</sub> = 150 °C	0.48	0.52	
		100 A		0.65	0.71	
		T <sub>J</sub> = 150 °C, V <sub>R</sub> = 45 V		600	1000	
Reverse leakage current	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	0.3	1	mA
		T <sub>J</sub> = 125 °C		180	320	
Maximum junction capacitance	C <sub>T</sub>	$V_R = 5 V_{DC}$ , (test signal range 100 kHz to 1 MHz) 25 °C		27	00	pF
Typical series inductance	L <sub>S</sub>	Measured from tab to mounting plane 3.5		.5	nH	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000		V/µs		

#### Note

 $<sup>^{(1)}</sup>$  Pulse width < 300  $\mu$ s, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and temperature range	storage	T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 150	°C	
Maximum thermal resis	stance,	R <sub>thJC</sub>	DC operation	0.50	°C/W	
Typical thermal resistar case to heatsink	nce,	R <sub>thCS</sub>	Mounting surface, smooth and greased	0.30		
Approximate weight				5	g	
Approximate weight				0.18	OZ.	
Mounting torque ————	minimum			1.2 (10)	N·m	
	maximum			2.4 (20)	(lbf $\cdot$ in)	
Marking device			Case style PowerTab®	100BGQ045		



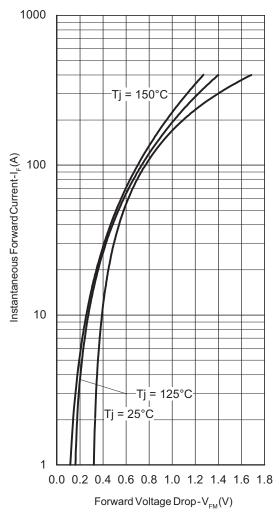


Fig. 1 - Maximum Forward Voltage Drop Characteristics

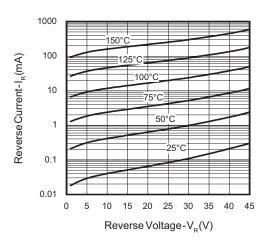


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

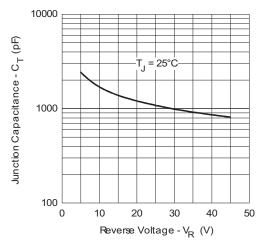


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

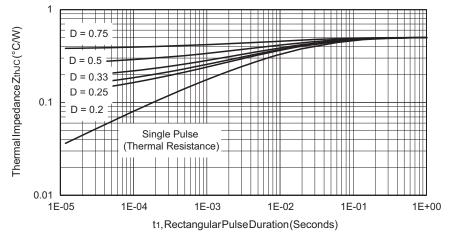


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

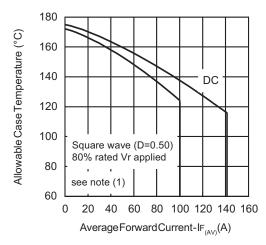


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

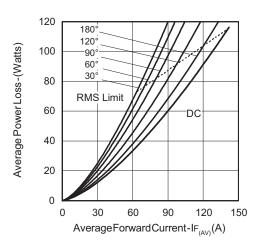


Fig. 6 - Forward Power Loss Characteristics

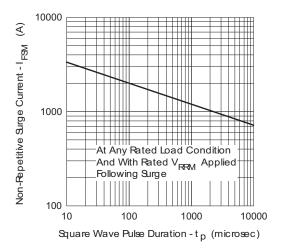


Fig. 7 - Maximum Non-Repetitive Surge Current

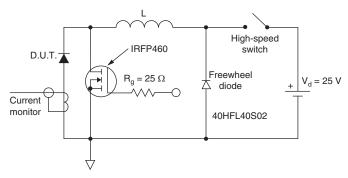


Fig. 8 - Unclamped Inductive Test Circuit

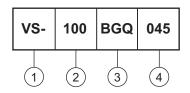
#### Note

 $^{(1)}$  Formula used: T<sub>C</sub> = T<sub>J</sub> - (Pd + Pd<sub>REV</sub>) x R<sub>thJC</sub>; Pd = Forward power loss = I<sub>F(AV)</sub> x V<sub>FM</sub> at (I<sub>F(AV)</sub>/D) (see fig. 6); Pd<sub>REV</sub> = Inverse power loss = V<sub>R1</sub> x I<sub>R</sub> (1 - D); I<sub>R</sub> at V<sub>R1</sub> = 80 % rated V<sub>R</sub>



#### **ORDERING INFORMATION TABLE**

**Device code** 



1 - Vishay Semiconductors product

2 - Current rating

3 - Essential part number

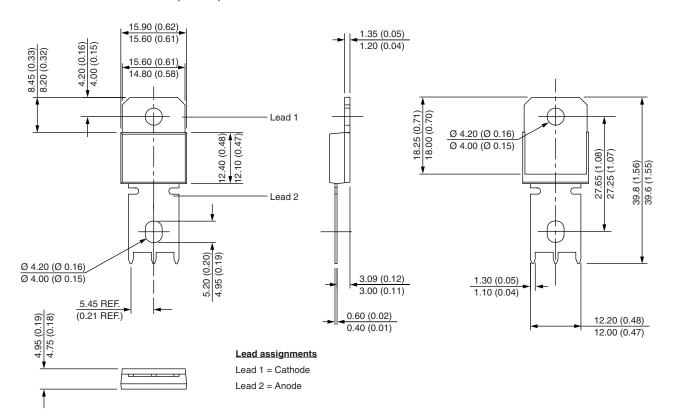
Voltage code = V<sub>RRM</sub>

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95240</u>				
Part marking information	www.vishay.com/doc?95370			
Application note <u>www.vishay.com/doc?95179</u>				



## PowerTab<sup>®</sup>

### **DIMENSIONS** in millimeters (inches)





## **Legal Disclaimer Notice**

Vishay

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