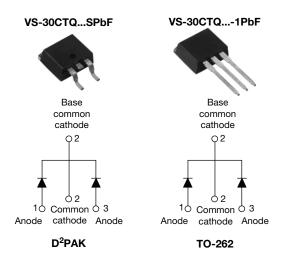




Vishay High Power Products

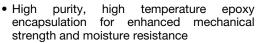
# Schottky Rectifier, 2 x 15 A



PRODUCT SUMMARY				
I <sub>F(AV)</sub>	2 x 15 A			
$V_{R}$	80 V/100 V			

### **FEATURES**

- 175 °C T<sub>J</sub> operation
- Center tap configuration
- Low forward voltage drop
- High frequency operation





- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Halogen-free according to IEC 61249-2-21 definition
- Compliant to RoHS directive 2002/95/EC
- AEC-Q101 qualified

### **DESCRIPTION**

This center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATING	MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL CHARACTERISTICS VALUES UNITS							
I <sub>F(AV)</sub>	Rectangular waveform	30	A				
V <sub>RRM</sub>		80/100	V				
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	850	A				
V <sub>F</sub>	15 Apk, T <sub>J</sub> = 125 °C (per leg)	0.67	V				
T <sub>J</sub>	Range	- 55 to 175	°C				

VOLTAGE RATINGS						
PARAMETER	SYMBOL	VS-30CTQ080SPbF VS-30CTQ080-1PbF	VS-30CTQ100SPbF VS-30CTQ100-1PbF	UNITS		
Maximum DC reverse voltage	$V_{R}$	80	100	V		
Maximum working peak reverse voltage	$V_{RWM}$	00	100	V		

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL TEST CONDITIONS						
Maximum average per device forward current	'		30				
See fig. 5 per leg	I <sub>F(AV)</sub>			15	A		
Maximum peak one cycle non-repetitive	I <sub>FSM</sub>	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	850			
surge current per leg See fig. 7		10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	275			
Non-repetitive avalanche energy per leg	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 0.50 A, L = 60 mH		7.50	mJ		
Repetitive avalanche current per leg	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		0.50	А		

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# VS-30CTQ...SPbF, VS-30CTQ...-1PbF Series

# Vishay High Power Products Schottky Rectifier, 2 x 15 A



ELECTRICAL SPECIFICATIONS							
PARAMETER SYMBOL TEST CONDITIONS VALUES UNI							
Maximum forward voltage drop per leg See fig. 1		15 A	T <sub>.1</sub> = 25 °C	0.86	V		
	V <sub>FM</sub> <sup>(1)</sup>	30 A	1j=25 C	1.05			
	V <sub>FM</sub> ···/	15 A	T 105 °C	0.67			
		30 A	T <sub>J</sub> = 125 °C	0.82			
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>B</sub> = Rated V <sub>B</sub>	0.55	mA		
See fig. 2		T <sub>J</sub> = 125 °C	v <sub>R</sub> = nateu v <sub>R</sub>	7.0			
Maximum junction capacitance per leg	C <sub>T</sub>	$V_R$ = 5 $V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		500	pF		
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 mm from package body		8.0	nΗ		
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000			V/µs		

### Note

 $<sup>^{(1)}\,</sup>$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANI	THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	PARAMETER SYMBOL TEST CONDITIONS VALUES UN								
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 175	°C				
Maximum thermal resistance, junction to case per leg		D	DC operation	3.25	°C/W				
Maximum thermal resistance, junction to case per package		$R_{thJC}$	DC operation	1.63					
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.50					
Approximate weight				2	g				
Approximate weight				0.07	OZ.				
Mounting toyour	minimum			6 (5)	kgf · cm				
Mounting torque -	maximum			12 (10)	(lbf · in)				
Maddina dadina			Case style D <sup>2</sup> PAK	30CTC	100S				
Marking device			Case style TO-262	30CTQ	100-1				



# Schottky Rectifier, 2 x 15 A Vishay High Power Products

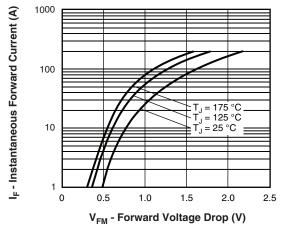


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

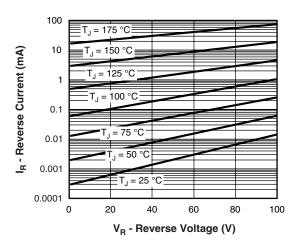


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

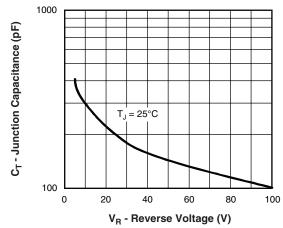


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

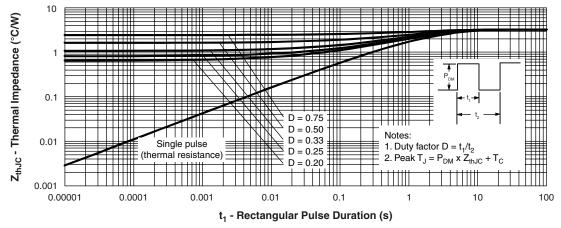


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics (Per Leg)

# VS-30CTQ...SPbF, VS-30CTQ...-1PbF Series

# Vishay High Power Products Schottky Rectifier, 2 x 15 A



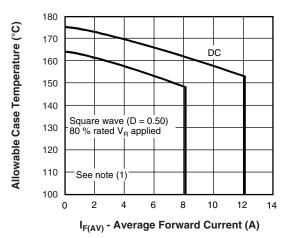


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

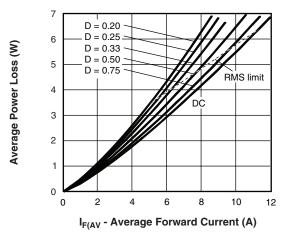


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

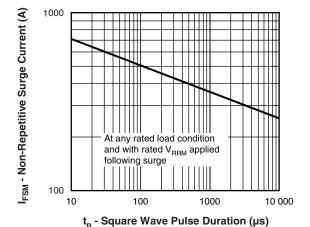


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

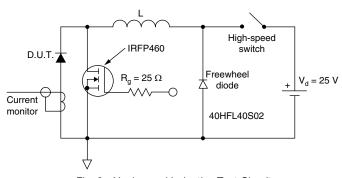


Fig. 8 - Unclamped Inductive Test Circuit

### Note

 $^{(1)}$  Formula used:  $T_C = T_J$  - (Pd + Pd\_{REV}) x R<sub>thJC</sub>; Pd = Forward power loss =  $I_{F(AV)}$  x V<sub>FM</sub> at (I<sub>F(AV)</sub>/D) (see fig. 6); Pd\_{REV} = Inverse power loss = V\_{R1} x I<sub>R</sub> (1 - D); I<sub>R</sub> at V<sub>R1</sub> = 10 V

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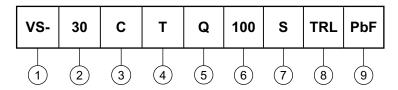


# VS-30CTQ...SPbF, VS-30CTQ...-1PbF Series

Schottky Rectifier, 2 x 15 A Vishay High Power Products

## **ORDERING INFORMATION TABLE**

**Device code** 



- 1 HPP product suffix
- 2 Current rating (30 A)
- 3 Circuit configuration: C = Common cathode
- **4** T = TO-220
- 5 Schottky "Q" series
- 7 • S = D<sup>2</sup>PAK
  - -1 = TO-262
- 8 • None = Tube (50 pieces)
  - TRL = Tape and reel (left oriented for D<sup>2</sup>PAK only)
  - TRR = Tape and reel (right oriented for D<sup>2</sup>PAK only)
- 9 PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?95014</u>					
Part marking information	www.vishay.com/doc?95008				
Packaging information	www.vishay.com/doc?95032				

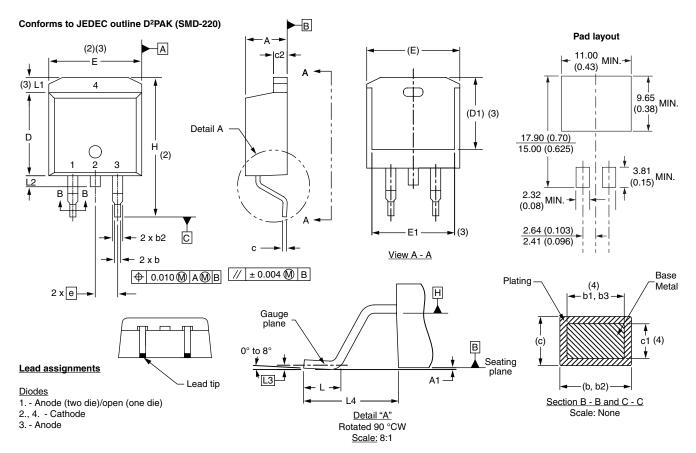
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# Vishay High Power Products

# D<sup>2</sup>PAK, TO-262

# **DIMENSIONS FOR D<sup>2</sup>PAK** in millimeters and inches



SYMBOL	MILLIM	MILLIMETERS		INCHES		
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	
Α	4.06	4.83	0.160	0.190		
A1	0.00	0.254	0.000	0.010		
b	0.51	0.99	0.020	0.039		
b1	0.51	0.89	0.020	0.035	4	
b2	1.14	1.78	0.045	0.070		
b3	1.14	1.73	0.045	0.068	4	
С	0.38	0.74	0.015	0.029		
c1	0.38	0.58	0.015	0.023	4	
c2	1.14	1.65	0.045	0.065		
D	8.51	9.65	0.335	0.380	2	

SYMBOL	MILLIM	ETERS	INC	NOTES		
	STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
	D1	6.86	8.00	0.270	0.315	3
	Е	9.65	10.67	0.380	0.420	2, 3
	E1	7.90	8.80	0.311	0.346	3
	е	2.54 BSC		0.100 BSC		
	Н	14.61	15.88	0.575	0.625	
	L	1.78	2.79	0.070	0.110	
	L1	-	1.65	-	0.066	3
	L2	1.27	1.78	0.050	0.070	
	L3	0.25 BSC		0.010	BSC	
	L4	4.78	5.28	0.188	0.208	

### Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- $^{(3)}\,$  Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch

(7) Outline conforms to JEDEC outline TO-263AB

Document Number: 95014 Revision: 31-Mar-09 For technical questions concerning discrete products, contact: <a href="mailto:diodes-tech@vishay.com">diodes-tech@vishay.com</a>
For technical questions concerning module products, contact: <a href="mailto:ind-modules@vishay.com">ind-modules@vishay.com</a>

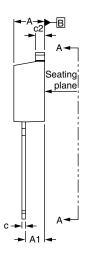
Vishay High Power Products

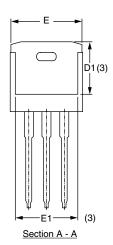
D<sup>2</sup>PAK, TO-262



### **DIMENSIONS FOR TO-262** in millimeters and inches

# Modified JEDEC outline TO-262 (Datum A) (2) (3) (3) L1 D L2 B B B L (2) 3 x b2 -3 x b





**⊕** 0.010**⋒**|A**⋒**|B

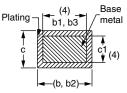
### Lead assignments



**Diodes** 

1. - Anode (two die)/open (one die) 2., 4. - Cathode

3. - Anode



Section B - B and C - C Scale: None

CVMDOL	MILLIMETERS		INC	HES	NOTEC
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190	
A1	2.03	3.02	0.080	0.119	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2
D1	6.86	8.00	0.270	0.315	3
Е	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54 BSC		0.100	BSC	
L	13.46	14.10	0.530	0.555	
L1	-	1.65	-	0.065	3
L2	3.56	3.71	0.140	0.146	

### Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Controlling dimension: inches

(6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline



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Vishay

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