VSUD360CW40

Vishay Semiconductors





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PRODUCT SUMMARY						
I _{F(AV)}	360 A					
V _R	400 V					
Q _{rr} (typical)	1250 nC					
t _{rr}	40 ns					
Туре	Modules - Diode, FRED Pt®					

FEATURES

- Very low Q_{rr} and t_{rr}
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified for industrial level

BENEFITS

- Reduced RFI and EMI
- Higher frequency operation
- Reduced snubbing

DESCRIPTION

FRED Pt[®] diodes are optimized to reduce losses and EMI/RFI in high frequency power conditioning systems. The softness of the recovery eliminates the need for a snubber in most applications. These devices are ideally suited for HF welding, power converters and other applications where switching losses are a significant portion of the total losses.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL TEST CONDITIONS		MAX.	UNITS		
Cathode to anode voltage	V _R		400	V		
		T _C = 25 °C	510			
Continuous forward current per diode	I _{F(AV)}	T _C = 85 °C	305	^		
		T _C = 116 °C	180	A		
Single pulse forward current per diode	I _{FSM}		1200			
Maximum power dissipation	P _D	T _C = 25 °C	570	w		
Maximum power dissipation		T _C = 110 °C	180	vv		
Operating junction and storage temperatures	T _J , T _{Stg}		- 40 to 150	°C		

ELECTRICAL SPECIFICATIONS PER LEG ($T_J = 25 \text{ °C}$ unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage	V_{BR}	I _R = 100 μA		-	-		
	V _{FM}	I _F = 180 A	-	1.09	1.27		
Forward voltage		I _F = 360 A	-	1.23	1.50	V	
		I _F = 180 A, T _J = 150 °C	-	0.88	0.96		
		I _F = 360 A, T _J = 150 °C	-	1.04	1.18		
Reverse leakage current	I _{RM}	$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	0.26	1.28	mA	
Series inductance	L _S	From top of terminal hole to mounting plane	-	5	-	nH	

Revision: 10-Nov-11

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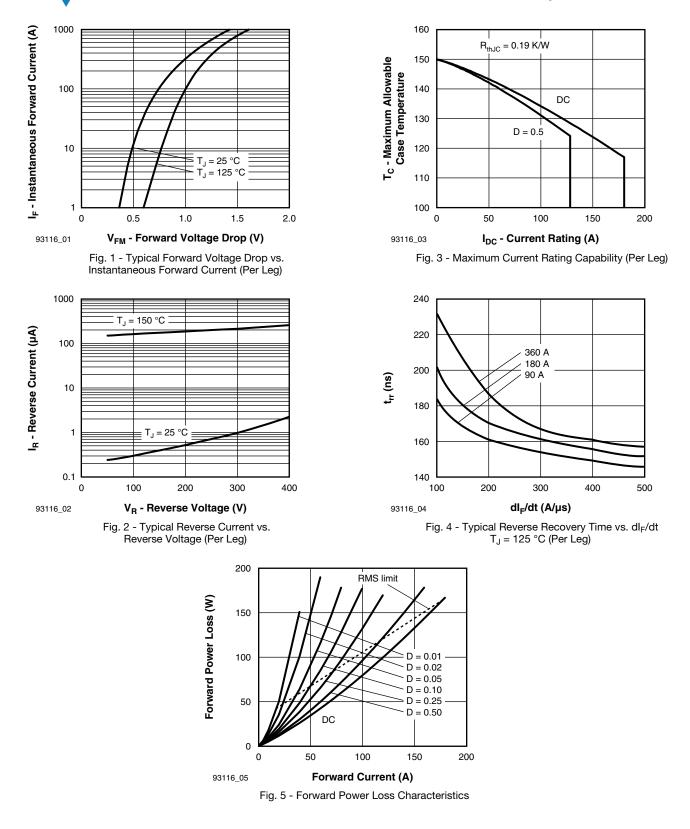
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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS			TYP.	MAX.	UNITS
		I _F = 1.0 A, dI _F /dt = 200 A/	-	40	69		
Reverse recovery time	t _{rr}	T _J = 25 °C	l _F = 180 A, dl _F /dt = 200 A/µs,	-	74	-	ns
		T _J = 150 °C	$V_{\rm R} = 200 \text{ V}$	-	171	-	
	I _{RRM}	$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 200 \text{ A}/\mu\text{s}, \text{ V}_R = 30 \text{ V}$			5.1	-	
Peak recovery current		I _F = 180 A, dI _F /dt = 200 A	-	6.6	-	А	
		$I_F = 180 \text{ A}, \text{ d}I_F/\text{d}t = 200 \text{ A}/1000 \text{ A}$	-	15.2	-		
	Q _{rr}	I _F = 1.0 A, dI _F /dt = 200 A/	-	125	-		
Reverse recovery charge		I _F = 180 A, dI _F /dt = 200 A	-	243	-	nC	
		$I_F = 180 \text{ A}, \text{ d}I_F/\text{d}t = 200 \text{ A}/1000 \text{ A}/10000 \text{ A}/100000\text{ A}/100000\text{ A}/100000\text{ A}/100000\text{ A}/1000000\text{ A}/1000000\text{ A}/1000000000000000000000000000000000000$	-	1295	-		

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNITS	
Thermal resistance,	per leg	Р	-	-	0.19		
junction to case	per module	R _{thJC}	-	-	0.095	°C/W	
Thermal resistance, case to heatsink (flag grease	nal resistance, to heatsink (flag greased surface)		-	0.10	-		
Waight			-	68	-	g	
Weight			-	2.4	-	oz.	
Mounting torque Mounting torque center hole Terminal torque			30 (3.4)	-	40 (4.6)		
			12 (1.4)	-	18 (2.1)	lbf ⋅ in (N ⋅ m)	
			30 (3.4)	-	40 (4.6)	(111)	
Vertical pull 2" lever pull			-	-	80	llaf in	
			-	-	35	- lbf ∙ in	
Case style				TO-244 (T	O-244AB)		



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Document Number: 93116

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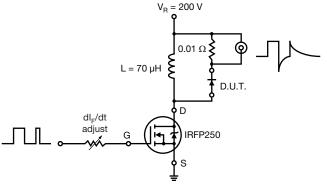
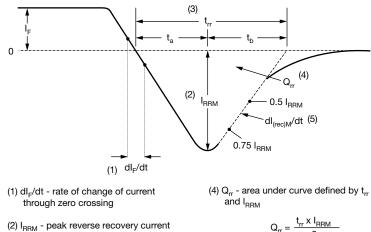


Fig. 6 - Reverse Recovery Parameter Test Circuit



$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(3) ${\rm t}_{\rm rr}$ - reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.

(5) $dI_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 7 - Reverse Recovery Waveform and Definitions





ORDERING INFORMATION TABLE

Device code	vs	UD	360	с	w	40
		(2)	(3)	(4)	(5)	(6)
	1 - Vishay Semiconductors product suffix					
	2 -	Тур	e of dev	vice: UD	= FRE	D Pt [®]
	3 -	Cur	rent rati	ng (360	= 360 A	۹)
	4 -	Circ	uit conf	iguratior	1:	
	C = Common cathode					
	5 -	Тур	e of dev	vice:		
	_	W =	= TO-24	4 wire b	ondable	e not iso
	6 -	· Volt	age rati	ng (40 =	400 V))

CIRCUIT CONFIGURATION						
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING				
Two diodes common cathodes	С	Lug anode 2 Lug terminal anode 1				

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95021				

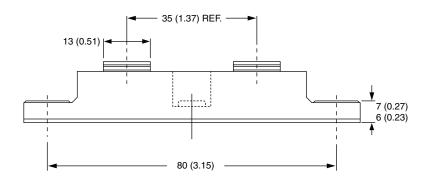


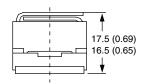
Outline Dimensions

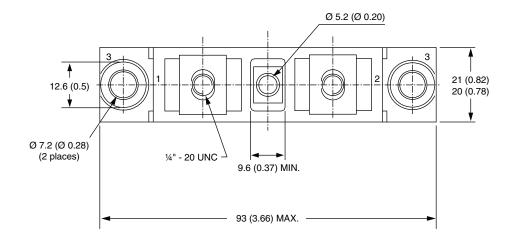
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TO-244

DIMENSIONS in millimeters (inches)









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