

**Vishay Semiconductors** 

# ADD-A-PAK Generation VII Power Modules Standard Diodes, 80 A



ADD-A-PAK

PRODUCT SUMMARY				
I <sub>F(AV)</sub>	80 A			
Туре	Modules - Diode, High Voltage			

#### **MECHANICAL DESCRIPTION**

The ADD-A-PAK generation VII, new generation of ADD-A-PAK module, combines the excellent thermal performances obtained by the usage of exposed direct bonded copper substrate, with advanced compact simple package solution and simplified internal structure with minimized number of interfaces.

### FEATURES

- High voltage
- Industrial standard package
- · Low thermal resistance
- UL approved file E78996
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified for industrial level

### BENEFITS

- Excellent thermal performances obtained by the usage of exposed direct bonded copper substrate
- Up to 1600 V
- High surge capability
- Easy mounting on heatsink

### **ELECTRICAL DESCRIPTION**

These modules are intended for general purpose high voltage applications such as high voltage regulated power supplies, lighting circuits, temperature and motor speed control circuits, UPS and battery charger.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS	VALUES	UNITS				
I <sub>F(AV)</sub>	110 °C	80					
I <sub>F(RMS)</sub>		126	A				
1	50 Hz	1500	A				
IFSM	60 Hz	1570					
l <sup>2</sup> t	50 Hz	11.25	kA <sup>2</sup> s				
1-1	60 Hz	10.26	KA-S				
l²√t		112.5	kA²√s				
V <sub>RRM</sub>	Range	400 to 1600	V				
TJ		- 40 to 150	°C				
T <sub>Stg</sub>		- 40 10 150	U				

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### **ELECTRICAL SPECIFICATIONS**

VOLTAGE RATINGS							
TYPE NUMBER	VOLTAGE CODE	V <sub>RRM</sub> , MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE V	V <sub>RSM</sub> , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I <sub>RRM</sub> MAXIMUM AT T <sub>J</sub> = 150 °C mA			
	04	400	500				
	06	600	700				
	08	800	900				
VSK.71	10	1000	1100	10			
	12	1200	1300				
	14	1400	1500				
	16	1600	1700				

FORWARD CONDUCTION							
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS		
Maximum average forward current at case temperature	I <sub>F(AV)</sub>	180° condu	ction, half sine	wave	80 110	A °C	
Maximum RMS forward current	I <sub>F(RMS)</sub>	DC at 90 °C	case temperat	ure	126	0	
	. (	t = 10 ms	No voltage		1500		
Maximum peak, one-cycle forward,		t = 8.3 ms	reapplied		1570	А	
non-repetitive surge current	I <sub>FSM</sub>	t = 10 ms	100 % V <sub>RRM</sub>		1260		
		t = 8.3 ms	reapplied	Sinusoidal half wave,	1320		
Martine and Plant street	l <sup>2</sup> t	t = 10 ms	No voltage	intitial T <sub>J</sub> = T <sub>J</sub> maximum	11.25	kA <sup>2</sup> s	
		t = 8.3 ms	reapplied		10.26		
Maximum I <sup>2</sup> t for fusing		t = 10 ms	100 % V <sub>RRM</sub>		7.95		
		t = 8.3 ms	reapplied		7.23		
Maximum I <sup>2</sup> $\sqrt{t}$ for fusing	l²√t	t = 0.1 ms to 10 ms, no voltage reapplied			112.5	kA²√s	
Low level value of threshold voltage	V <sub>F(TO)1</sub>	(16.7 % x π	$x I_{F(AV)} < I < \pi x$	(I <sub>F(AV)</sub> ), T <sub>J</sub> = T <sub>J</sub> maximum	0.73	v	
High level value of threshold voltage	V <sub>F(TO)2</sub>	$(I > \pi \times I_{F(AV)})$	), T <sub>J</sub> = T <sub>J</sub> maxir	num	0.83	v	
Low level value of forward slope resistance	r <sub>f1</sub>	(16.7 % x $\pi$ x I <sub>F(AV)</sub> < I < $\pi$ x I <sub>F(AV)</sub> ), T <sub>J</sub> = T <sub>J</sub> maximum			3.22	mΩ	
High level value of forward slope resistance	r <sub>f2</sub>	$(I > \pi \times I_{F(AV)}), T_J = T_J maximum$			2.89	1115.2	
Maximum forward voltage drop	V <sub>FM</sub>	$I_{FM} = \pi \times I_{F(a)}$	$AV$ , $T_J = 25 \circ C$ ,	t <sub>p</sub> = 400 μs square wave	1.6	V	

BLOCKING						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum peak reverse leakage current	I <sub>RRM</sub>	T <sub>J</sub> = 150 °C	10	mA		
Maximum RMS insulation voltage	V <sub>INS</sub>	50 Hz	3000 (1 min) 3600 (1 s)	V		

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THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	YMBOL TEST CONDITIONS		UNITS	
Junction and storage temp	erature range	T <sub>J</sub> , T <sub>Stg</sub>		- 40 to 150	°C	
Maximum internal thermal junction to case per leg	resistance,	R <sub>thJC</sub>	DC operation	0.28	20.00	
Typical thermal resistance, case to heatsink per module		R <sub>thCS</sub>	Mounting surface flat, smooth and greased	0.1	- °C/W	
to hea			A mounting compound is recommended and the	4	Nime	
Mounting torque ± 10 %	busbar		torque should be rechecked after a period of 3 hours to allow for the spread of the compound.	3	Nm	
Approvimete weight				75	g	
Approximate weight				2.7	oz.	
Case style			JEDEC	ADD-A-PAK Ger	n. VII (TO-240AA)	

DEVICES	S	SINE HALF	WAVE CO	NDUCTIO	Ν	RECTANGULAR WAVE CONDUCTION					
DEVICES	180°	120°	90°	60°	30°	180°	120°	<b>90</b> °	<b>60</b> °	30°	UNITS
VSK.71	0.075	0.088	0.113	0.155	0.228	0.06	0.094	0.12	0.158	0.23	°C/W

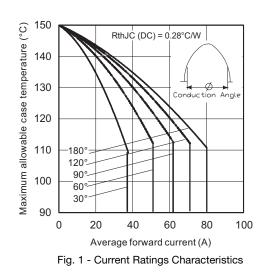
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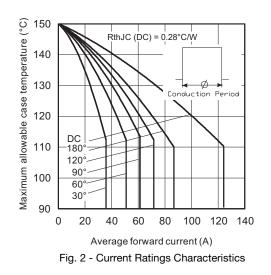
Table shows the increment of thermal resistance R<sub>thJC</sub> when devices operate at different conduction angles than DC

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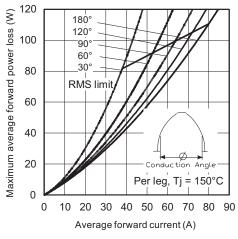
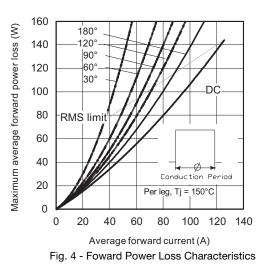
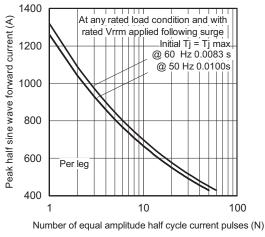


Fig. 3 - Forward Power Loss Characteristics







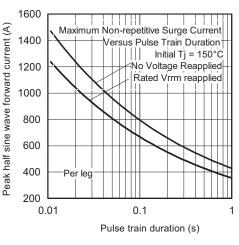


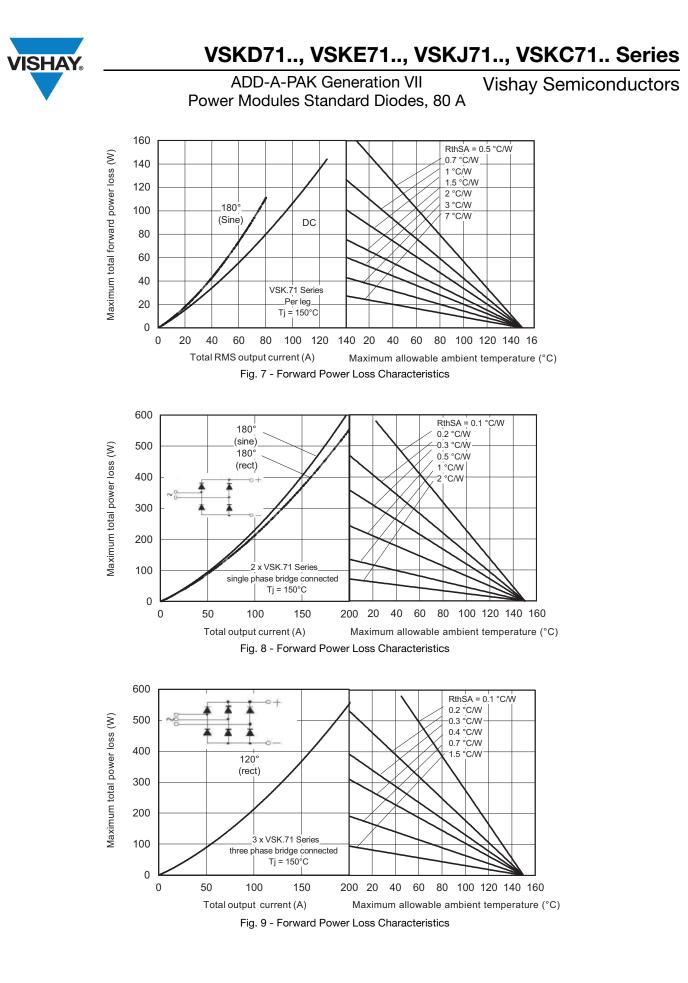
Fig. 6 - Maximum Non-Repetitive Surge Current

For technical questions, contact: indmodules@vishay.com

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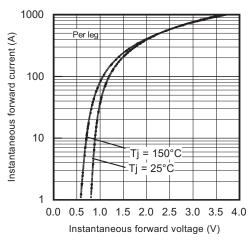


Fig. 10 - Forward Voltage Characteristics

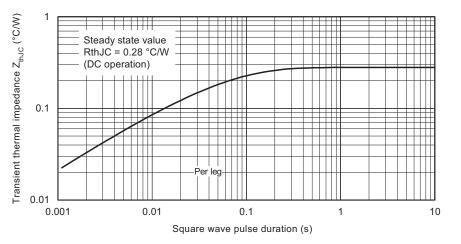


Fig. 11 - Thermal Impedance ZthJC Characteristics

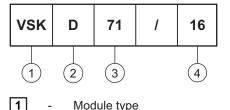
### **ORDERING INFORMATION TABLE**

Device code

2

3

4



- Module type

- Circuit configuration (see Circuit Configuration table)
- Current code (80 A)
- Voltage code (see Voltage Ratings table)

#### Note

• To order the optional hardware go to www.vishay.com/doc?95172

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CIRCUIT CONFIGURATION					
CIRCUIT DESCRIPTION	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING			
Two diodes doubler circuit	D				
Two diodes common cathodes	С				
Two diodes common anodes	J				
Single diode	E	VSKE (2) 0 (3)			

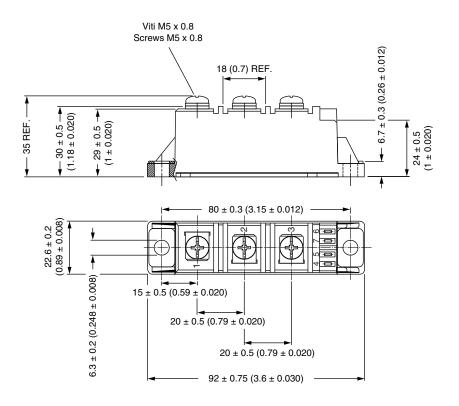
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95369			

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## **ADD-A-PAK Generation VII - Diode**

### **DIMENSIONS** in millimeters (inches)





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