

## Vishay High Power Products

# HEXFRED® Ultrafast Diodes, 100 A (New INT-A-PAK Power Modules)



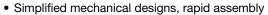
New INT-A-PAK

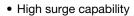
PRODUCT SUMMARY				
$V_{R}$	1200 V			
V <sub>F</sub> (typical)	2.5 V			
t <sub>rr</sub> (typical)	150 ns			
I <sub>F(DC)</sub> at T <sub>C</sub>	110 A at 100 °C			

#### **FEATURES**

• Electrically isolated: DBC base plate







- Large creepage distances
- UL approved file E78996 **71**
- Case style New INT-A-PAK
- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for industrial level

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Cathode to anode voltage	$V_{R}$		1200	V	
Continuous forward current		T <sub>C</sub> = 25 °C	205		
	I <sub>F</sub>	T <sub>C</sub> = 100 °C	110	Α	
Single pulse forward current	I <sub>FSM</sub>	Limited by junction temperature	800		
Maximum pawar dissination	P <sub>D</sub>	T <sub>C</sub> = 25 °C	695	w	
Maximum power dissipation		T <sub>C</sub> = 100 °C	280		
RMS isolation voltage	V <sub>ISOL</sub>	50 Hz, circuit to base, all terminal shorted, t = 1 s	3500	V	
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		- 40 to + 150	°C	

<b>ELECTRICAL SPECIFICATIONS PER LEG</b> (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	L TEST CONDITIONS		TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	$V_{BR}$	Ι <sub>R</sub> = 100 μΑ	1200	-	-	
Maximum forward voltage V <sub>FM</sub>	V	I <sub>F</sub> = 100 A	-	2.5	3.2	V
	I <sub>F</sub> = 160 A	-	2.9	3.9		
Maximum reverse leakage current	I <sub>RM</sub>	T <sub>J</sub> = 150 °C, V <sub>R</sub> = 1200 V	-	18	30	mA

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## VSKDU162/12PbF



# Vishay High Power Products HEXFRED® Ultrafast Diodes, 100 A (New INT-A-PAK Power Modules)

<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	150	200	ns
Reverse recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C	l <sub>F</sub> = 160 A dl <sub>F</sub> /dt = 200 A/μs	-	20	22	Α
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C	$V_{R} = 200 \text{ V}$	-	2000	2400	nC
Peak rate of recovery current	dl <sub>(rec)M</sub> /dt	T <sub>J</sub> = 25 °C	1.	-	-	300	A/µs

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL TEST CONDITIONS		VALUES	UNITS	
Junction operating and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		- 40 to 150	°C	
Maximum internal thermal resistance, junction to case per leg		R <sub>thJC</sub>	DC operation	0.18		
Typical thermal resistance, case to heatsink per modu		R <sub>thCS</sub>	Mounting surface flat, smooth and greased	°C/W ace flat, smooth and greased 0.05		
to heatsink			A mounting compound is recommended and the torque should be rechecked after a period of 3 hours	4	Nee	
Mounting torque ± 10 %	busbar		to allow for the spread of the compound.	6	Nm	
Approximate weight				200	g	
				7.1	oz.	
Case style New		New INT	-A-PAK			



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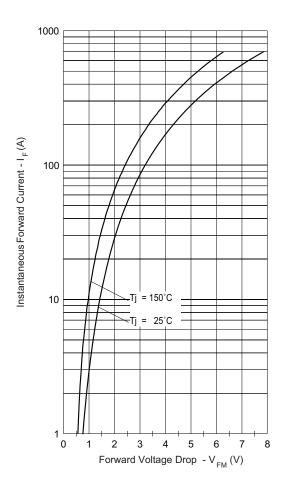


Fig. 1 - Maximum Forward Voltage Drop Characteristics

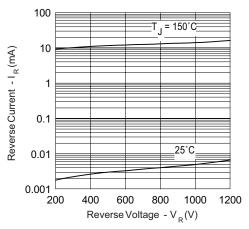


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

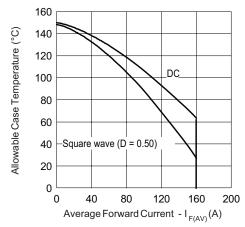


Fig. 3 - Maximum Allowable Case Temperature vs. Average Forward Current

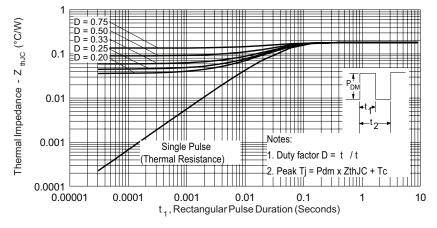


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

# VISHAY.

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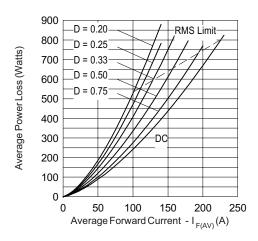


Fig. 5 - Forward Power Loss Characteristics

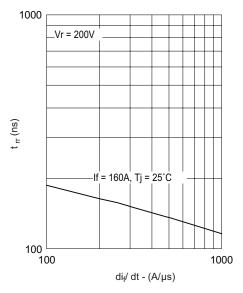


Fig. 6 - Typical Reverse Recovery Time vs.  $dI_F/dt$  (Per Leg)

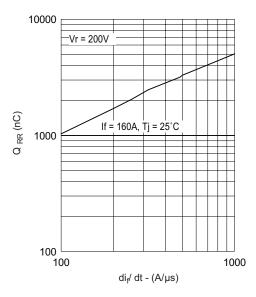


Fig. 7 - Typical Reverse Recovery Charge vs. dl<sub>F</sub>/dt (Per Leg)

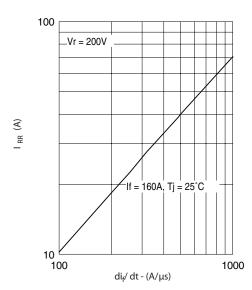


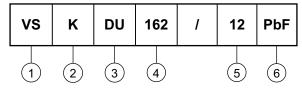
Fig. 8 - Typical Reverse Recovery Current vs.  $dI_F/dt$  (Per Leg)



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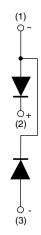
#### **ORDERING INFORMATION TABLE**

**Device code** 



- 1 Vishay HPP
- 2 K = New INT-A-PAK module
- 3 DU = HEXFRED® ultrafast diode
- 4 Current rating
  - Voltage rating (12 = 1200 V)
- 6 PbF = Lead (Pb)-free

#### **CIRCUIT CONFIGURATION**



LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95254			

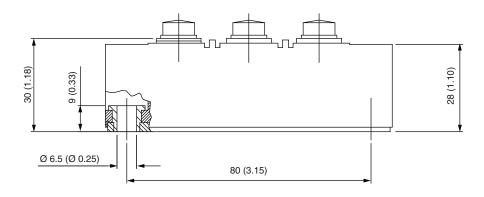
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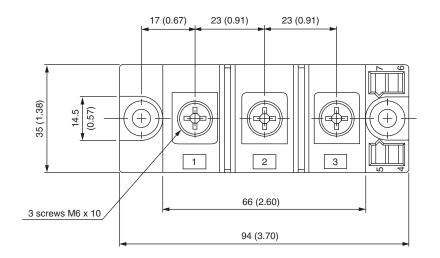


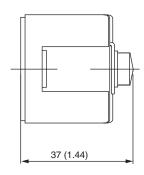
Vishay Semiconductors

## **INT-A-PAK DBC**

#### **DIMENSIONS** in millimeters (inches)









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Vishay

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