

Vishay High Power Products

Three Phase Controlled Bridge (Power Modules), 55 A to 110 A



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FEATURES

· Package fully compatible with the industry standard INT-A-PAK power modules series



• High thermal conductivity package, electrically insulated case

- Excellent power volume ratio
- 4000 V_{RMS} isolating voltage
- UL E78996 approved
- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for industrial level

PRODUCT SUMMARY	
I _O	55 A to 110 A

DESCRIPTION

A range of extremely compact, encapsulated three phase controlled bridge rectifiers offering efficient and reliable operation. They are intended for use in general purpose and heavy duty applications.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	5.MTK	9.MTK	11.MTK	UNITS				
1		55	90	110	А				
I _O	T _C	85	85	85	°C				
1	50 Hz	390	950	1130	^				
I _{FSM}	60 Hz	410	1000	1180	Α Α				
l ² t	50 Hz	770	4525	6380	A ² s				
1-1	60 Hz	700	4130	5830	A-s				
I ² √t		7700	45 250	63 800	A²√s				
V _{RRM}	Range	800 to 1600							
T _{Stg} , T _J	Range		- 40 to 125		°C				

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ELECTRICAL SPECIFICATIONS

VOLTAGE RAT	VOLTAGE RATINGS									
TYPE NUMBER	VOLTAGE CODE	V _{RRM} , MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	V _{DRM} , MAXIMUM REPETITIVE PEAK OFF-STATE VOLTAGE, GATE OPEN CIRCUIT V	I _{RRM} /I _{DRM} , MAXIMUM AT T _J = 125 °C mA					
	80	800	900	800						
	100	1000	1100	1000						
5.MTK	120	1200	1300	1200	10					
	140	1400	1500	1400						
	160	1600	1700	1600						
	80	800	900	800						
	100	1000	1100	1000						
9.MTK 11.MTK	120	1200	1300	1200	20					
11.1011	140	1400	1500	1400						
	160	1600	1700	1600						

FORWARD CONDUCTION								
PARAMETER	SYMBOL		TEST CONE	5.MTK	9.MTK	11.MTK	UNITS	
Maximum DC output current at case temperature	I _O	120° rect. c	conduction angle)	55	90	110	A
current at case temperature					85	85	85	°C
Marianian and and area		t = 10 ms	No voltage		390	950	1130	
Maximum peak, one-cycle forward, non-repetitive	I _{TSM}	t = 8.3 ms	reapplied		410	1000	1180	Α
on state surge current	13101	t = 10 ms	100 % V _{RRM}		330	800	950	,,
		t = 8.3 ms	reapplied	Initial $T_1 = T_1$ maximum	345	840	1000	
		t = 10 ms	No voltage		770	4525	6380	
Maximum I ² t for fusing	l ² t	t = 8.3 ms	reapplied		700	4130	5830	A ² s
Maximum i-t for fusing	1-1	t = 10 ms	100 % V _{RRM}		540	3200	4510	A ² S
		t = 8.3 ms	reapplied		500	2920	4120	
Maximum I ² √t for fusing	I ² √t	t = 0.1 ms t	o 10 ms, no volt	7700	45 250	63 800	A²√s	
Low level value of threshold voltage	V _{T(TO)1}	(16.7 % x π	$x \mid_{T(AV)} < I < \pi x$	1.17	1.09	1.04	V	
High level value of threshold voltage	V _{T(TO)2}	$(I > \pi \times I_{T(AV)})$), T _J maximum		1.45	1.27	1.27	V
Low level value on-state slope resistance	r _{t1}	(16.7 % x π	$x \mid_{T(AV)} < I < \pi x$	I _{T(AV)}), T _J maximum	12.40	4.10	3.93	~ C
High level value on-state slope resistance	r _{t2}	$(I > \pi \times I_{T(AV)})$), T _J maximum		11.04	3.59	3.37	mΩ
Maximum on-state voltage drop	V _{TM}	I _{pk} = 150 A,	T _J = 25 °C, t _p =	2.68	1.65	1.57	V	
Maximum non-repetitve rate of rise of turned on current	dl/dt	T_J = 25 °C, from 0.67 V_{DRM} , I_{TM} = π x $I_{T(AV)}$, I_g = 500 mA, t_r < 0.5 μ s, t_p > 6 μ s				150		A/μs
Maximum holding current	I _H	-	T _J = 25 °C, anode supply = 6 V, resistive load, gate open circuit			200		mA
Maximum latching current	ΙL	$T_J = 25$ °C,	anode supply =	6 V, resistive load		400		



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BLOCKING						
PARAMETER	SYMBOL	TEST CONDITIONS	5.MTK	9.MTK	11.MTK	UNITS
RMS isolation voltage	V_{ISOL}	$T_J = 25~^{\circ}\text{C}$ all terminal shorted, $f = 50~\text{Hz}$, $t = 1~\text{s}$		4000		V
Maximum critical rate of rise of off-state voltage	dV/dt ⁽¹⁾	$T_J = T_J$ maximum, linear to 0.67 V_{DRM} , gate open circuit		500		V/µs

Note

 $^{^{(1)}}$ Available with dV/dt = 1000 V/ μs , to complete code add S90 i. e. 113MT160KBS90

TRIGGERING								
PARAMETER	SYMBOL	TEST CO	NDITIONS	5.MTK	9.MTK	11.MTK	UNITS	
Maximum peak gate power	P _{GM}				10		w	
Maximum average gate power	P _{G(AV)}				2.5		\ \ \ \	
Maximum peak gate current	I _{GM}	$T_J = T_J$ maximum			2.5		Α	
Maximum peak negative gate voltage	- V _{GT}			10				
		T _J = - 40 °C		4.0			V	
Maximum required DC gate voltage to trigger	V_{GT}	T _J = 25 °C		2.5				
voltage to trigger		T _J = 125 °C	Anode supply = 6 V,	1.7				
		T _J = - 40 °C	resistive load	270				
Maximum required DC gate current to trigger	I _{GT}	T _J = 25 °C	150		mA			
current to trigger		T _J = 125 °C			80			
Maximum gate voltage that will not trigger	V _{GD}	T - T maximum rates	dV applied		0.25		V	
Maximum gate current that will not trigger	I _{GD}	$T_J = T_J$ maximum, rated	v _{DRM} applied		6		mA	

THERMAL AND MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	5.MTK	9.MTK	11.MTK	UNITS		
Maximum junction operating and storage temperature range	T _J , T _{Stg}			- 40 to 125		°C		
		DC operation per module	0.18	0.14	0.12			
Maximum thermal resistance, junction to case	R_{thJC}	DC operation per junction	1.07	0.86	0.70			
		120 °C rect. conduction angle per module	0.19	0.15	0.12	K/W		
		120 °C rect. conduction angle per junction	1.17	0.91	0.74	1000		
Maximum thermal resistance, case to heatsink per module	R _{thCS}	Mounting surface smooth, flat and grased	0.03					
Mounting to heatsink		A mounting compound is recommended and		4 to 6		Nies		
torque ± 10 % to terminal		the torque should be rechecked after a period of	3 to 4			Nm		
Approximate weight		3 hours to allow for the spread of the compound. Lubricated threads.				g		

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△R CONDUCTION PER JUNCTION											
DEVICES			DAL CONI T _J MAXIM					ULAR CON			UNITS
	180°	120°	90°	60°	30°	180°	120°	90°	60°	30°	
5.MTK	0.072	0.085	0.108	0.152	0.233	0.055	0.091	0.117	0.157	0.236	
9.MTK	0.033	0.039	0.051	0.069	0.099	0.027	0.044	0.055	0.071	0.100	K/W
11.MTK	0.027	0.033	0.042	0.057	0.081	0.023	0.037	0.046	0.059	0.082	

Note

[•] Table shows the increment of thermal resistance R_{thJC} when devices operate at different conduction angles than DC

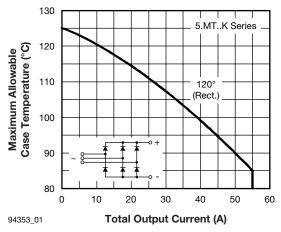


Fig. 1 - Current Ratings Characteristic

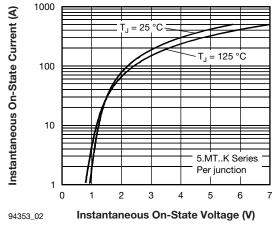
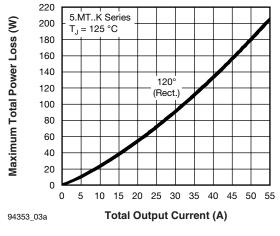


Fig. 2 - Forward Voltage Drop Characteristics



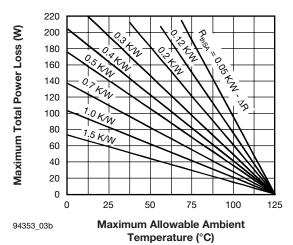


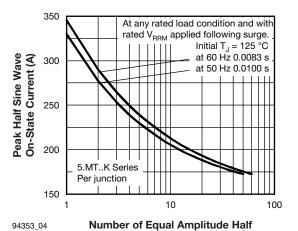
Fig. 3 - Total Power Loss Characteristics

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Cycle Current Pulses (N)
Fig. 4 - Maximum Non-Repetitive Surge Current

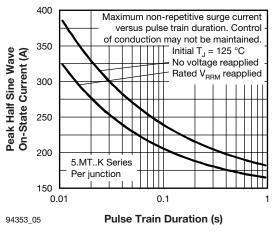
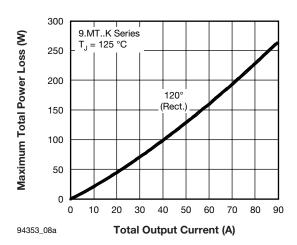


Fig. 5 - Maximum Non-Repetitive Surge Current



130 9.MT..K Series Maximum Allowable Case 120 Temperature (°C) 120° 110 (Rect.) 100 90 80 20 40 60 80 100 94353_06 **Total Output Current (A)**

Fig. 6 - Current Ratings Characteristic

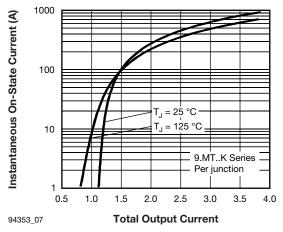


Fig. 7 - Forward Voltage Drop Characteristics

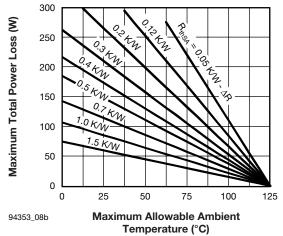


Fig. 8 - Total Power Loss Characteristics



(Power Modules), 55 A to 110 A



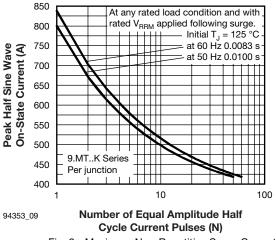


Fig. 9 - Maximum Non-Repetitive Surge Current

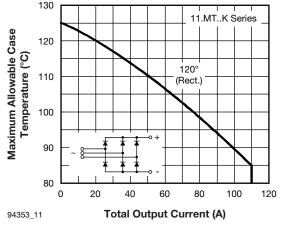


Fig. 11 - Current Ratings Characteristic

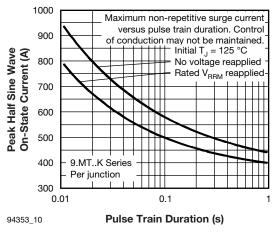


Fig. 10 - Maximum Non-Repetitive Surge Current

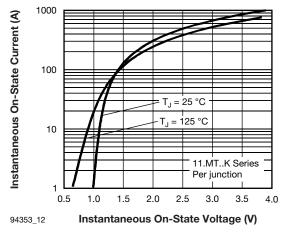
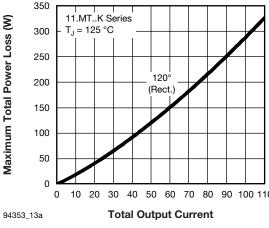


Fig. 12 - Forward Voltage Drop Characteristics



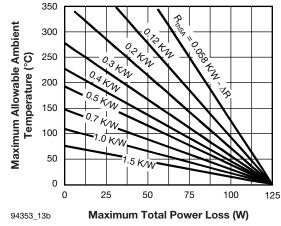


Fig. 13 - Total Power Loss Characteristics



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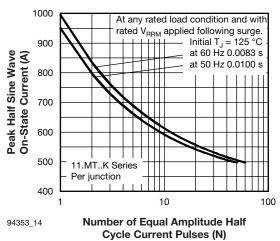


Fig. 14 - Maximum Non-Repetitive Surge Current

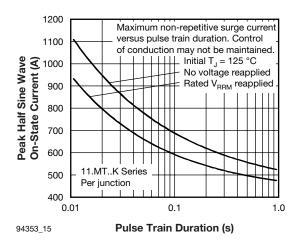


Fig. 15 - Maximum Non-Repetitive Surge Current

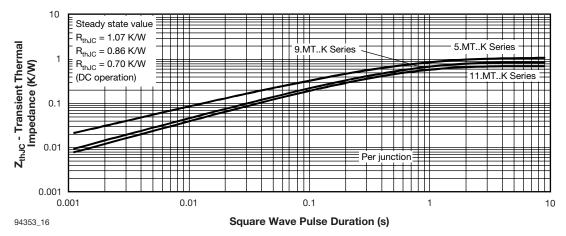


Fig. 16 - Thermal Impedance Z_{thJC} Characteristics

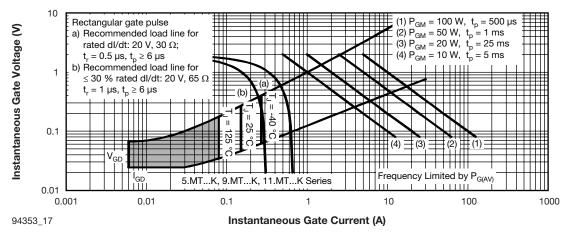


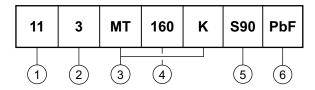
Fig. 17 - Gate Characteristics

Vishay High Power Products Three Phase Controlled Bridge (Power Modules), 55 A to 110 A



ORDERING INFORMATION TABLE

Device code

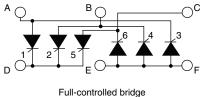


- 1 Current rating code:
 - 5 = 55 A (average)
 - 9 = 90 A (average)
 - 11 = 110 A (average)
- 2 Circuit configuration code:
 - 1 = Negative half-controlled bridge
 - 2 = Positive half-controlled bridge
 - 3 = Full-controlled bridge
- 3 Essential part number
- Voltage code x 10 = V_{RRM} (see Voltage Ratings table)
- 5 Critical dV/dt:
 - None = 500 V/µs (standard value)
 - S90 = 1000 V/µs (special selection)
- 6 PbF = Lead (Pb)-free

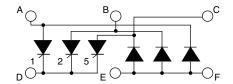
Note

• To order the optional hardware go to www.vishay.com/doc?95172

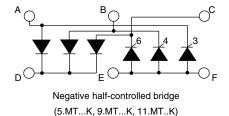
CIRCUIT CONFIGURATION



(5.MT...K, 9.MT...K, 11.MT..K)



Positive half-controlled bridge (5.MT...K, 9.MT...K, 11.MT..K)



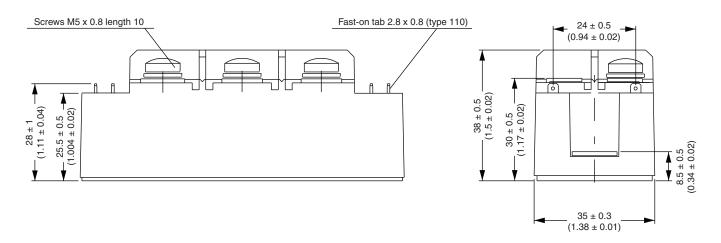
LINKS TO RELAT	ED DOCUMENTS
Dimensions	www.vishay.com/doc?95004

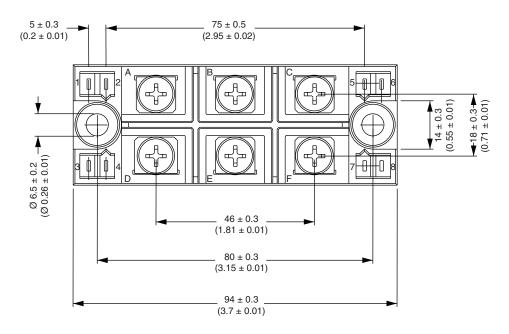


Vishay Semiconductors

MTK (with and without optional barrier)

DIMENSIONS WITH OPTIONAL BARRIERS in millimeters (inches)

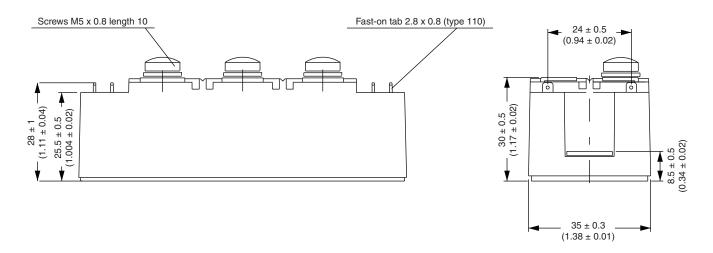


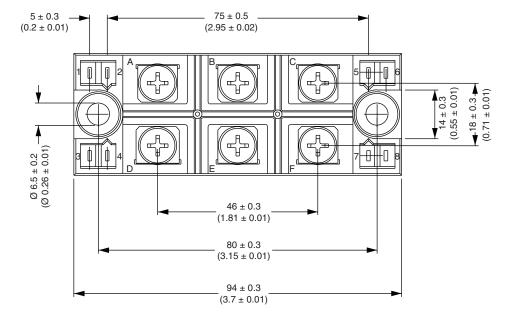


Vishay Semiconductors MTK (with and without optional barrier)



DIMENSIONS WITHOUT OPTIONAL BARRIERS in millimeters (inches)







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