Schottky Rectifier, 1.0 A

- Small foot print, surface mountable
- Very low forward voltage drop
- High frequency operation
- · Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for industrial level

DESCRIPTION

The VS-STPS1L30UPbF surface mount Schottky rectifie has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Rectangular waveform	1.0	A	
V _{RRM}		30	V	
I _{FSM}	t _p = 5 ms sine	360	А	
V _F	1.0 Apk, T _J = 125 °C	0.30	V	
TJ	Range	- 55 to 150	°C	

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-STPS1L30UPBF	UNITS	
Maximum DC reverse voltage	V _R	- 30	V	
Maximum working peak reverse voltage	V _{RWM}	30	v	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T_L = 106 °C, rectangular waveform		1.0	
Maximum peak one cycle non-repetitive surge current	I _{FSM}	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	360	А
		10 ms sine or 6 ms rect. pulse		75	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1 A, L = 6 mH		3.0	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1.0	А



PRODUCT SUMMARY

I_{F(AV)}

 V_{R}

 I_{RM}





Vishay High Power Products

Anode

1.0 A

30 V

15 mA at 125 °C

VS-STPS1L30UPbF

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop	V _{FM} ⁽¹⁾	1 A	T _J = 25 °C	0.420	V
		2 A		0.470	
		1 A	T _J = 125 °C	0.300	
		2 A		0.375	
	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	0.2	
Maximum reverse leakage current		T _J = 100 °C		5.0	mA
		T _J = 125 °C		15	
Maximum junction capacitance	CT	$V_{\rm R}$ = 5 $V_{\rm DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		200	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		2.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

⁽¹⁾ Pulse width < 300 μ s, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		- 55 to 150	°C
Maximum thermal resistance, junction to lead	R _{thJL} ⁽²⁾	DC operation	25	°C/W
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	80	
Approvimate weight			0.10	g
Approximate weight			0.003	oz.
Marking device		Case style SMB (similar to DO-214AA)	V13L	

Notes

(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

⁽²⁾ Mounted 1" square PCB



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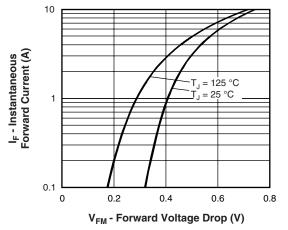


Fig. 1 - Maximum Forward Voltage Drop Characteristics

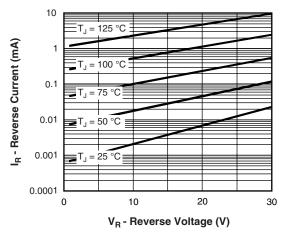


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

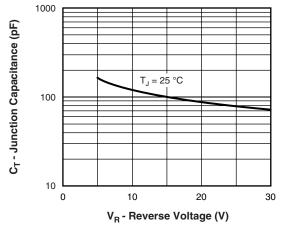
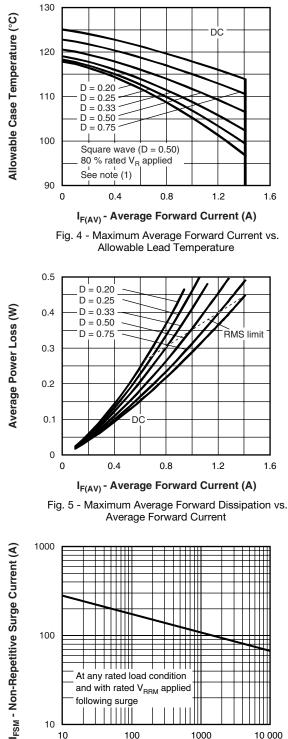
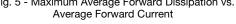
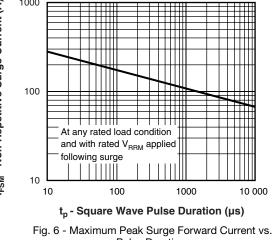


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage







Pulse Duration

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

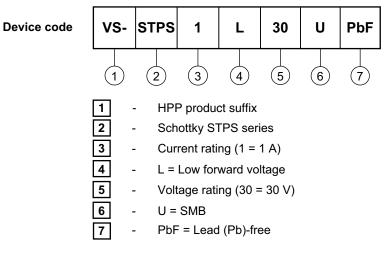
Pd = Forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R

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ORDERING INFORMATION TABLE



Tape and reel only (3000 pieces)

LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95017		
Part marking information	www.vishay.com/doc?95029		
Packaging information	www.vishay.com/doc?95034		

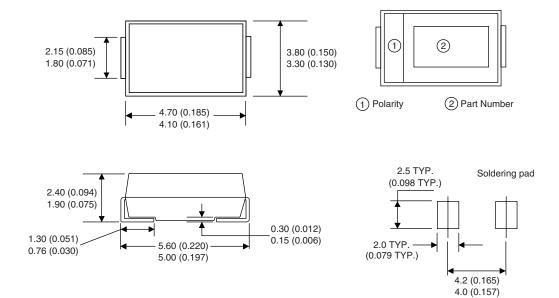


Outline Dimensions

Vishay High Power Products

SMB

DIMENSIONS in millimeters (inches)





Vishay

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