

Datasheet - production data

Single-line unidirectional ESD protection for high speed interface

Features

- Unidirectional device
- Ultralow diode capacitance: 0.8 pF
- Low leakage current
- 0201 SMD package size compatible
- Ultra small PCB area: 0.18 mm²
- RoHS compliant
- ECOPACK[®]2 compliant package

Complies with the following standards:

- IEC 61000-4-2 level 4
 - ±15 kV air discharge
 - ±8 kV contact discharge

Applications

Where transient overvoltage protection in ESD sensitive equipment is required, such as:

- Smartphone, mobile phone and accessories
- Tablet PC, netbook and notebook
- Portable multimedia device and accessories
- Digital camera and camcorder

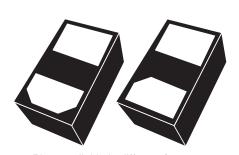
This is information on a product in full production.

Communication and highly integrated systems

Description

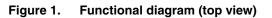
The ESDAULC6-1U2 is a unidirectional single line TVS diode designed to protect the data lines or other I/O ports against ESD transients.

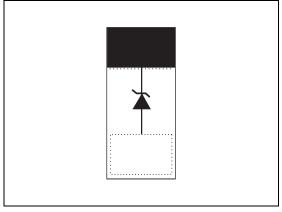
The device is ideal for applications where both reduced line capacitance and board space saving are required.



Pin 1 available in different forms

ST0201 package





TM: Transil is a trademark of STMicroelectronics

1 Characteristics

Second Second Second			
Symbol	Parameter	Value	Unit
V _{PP}	Peak pulse voltage: IEC 61000-4-2 contact discharge IEC 61000-4-2 air discharge	17 25	kV
P _{PP}	Peak pulse power dissipation (8/20 μ s) ⁽¹⁾	18	W
I _{PP}	Peak pulse current (8/20 μs)	1.4	А
Тj	Operating junction temperature range	-40 to 150	°C
T _{stg}	Storage temperature range	- 55 to +150	°C
Τ _L	Maximum lead temperature for soldering during 10 s	260	°C

Table 1.	Absolute maximum	ratings	(T _{amb} =	25 °C))
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1. For a surge greater than the maximum values, the diode will fail in short-circuit

Figure 2. Electrical characteristics (definitions)

Symb	lool	Parameter
V_{BR}	=	Breakdown voltage
V_{CL}	=	Clamping voltage
I _{RM}	=	Leakage current @ V _{RM}
V _{RM}	=	Stand-off voltage
I _F	=	Forward current
I _{PP}	=	Peak pulse current
I _B	=	Breakdown current
VF	=	Forward voltage drop
R _d	=	Dynamic impedance
αΤ	=	Voltage temperature

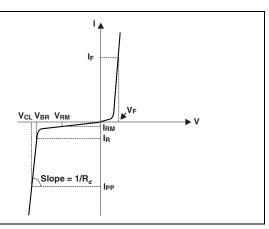


Table 2.	Electrical characteristics (values, T _{amb} = 25 °C)
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Symbol	Test conditions	Min.	Тур.	Max.	Unit
V_{BR}	I _R = 1 mA	6			V
I _{RM}	V _{RM} = 3 V			100	nA
V _{CL}	IPP = 1 A, 8/20 μs			13	V
C _{line}	F = [200 MHz - 3000 MHz], V _R = 0 V		0.8	1	pF

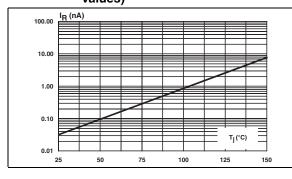


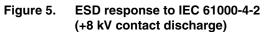
 $V_R(V)$

3000

2500

Figure 3. Variation of leakage current versus Figure 4. junction temperature (typical







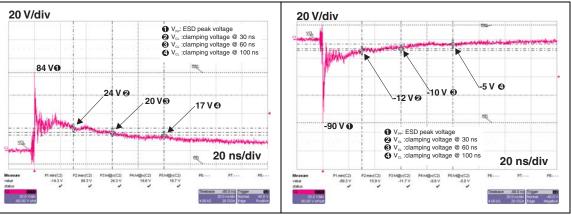
1500

2000

1000

Junction capacitance versus

applied voltage (typical values)



C(pF)

1200 1000

800

600 400

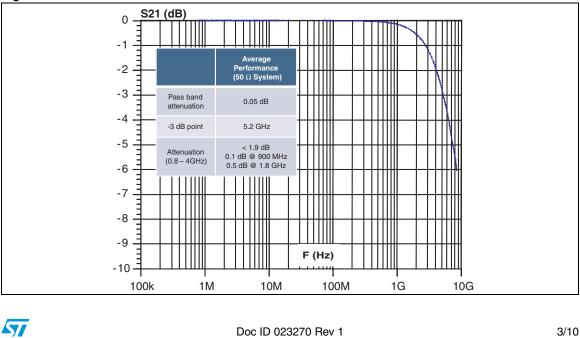
200

0

0

500

Figure 7. S21 attenuation measurement results



values)

2 Ordering information scheme

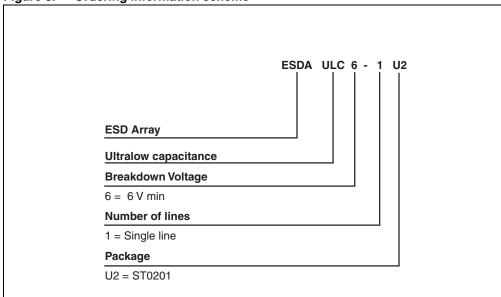


Figure 8. Ordering information scheme

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3 Package information

- Epoxy meets UL94, V0
- Lead-free packages

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: <u>www.st.com</u>. ECOPACK[®] is an ST trademark.

Table 3. ST0201 dimensions

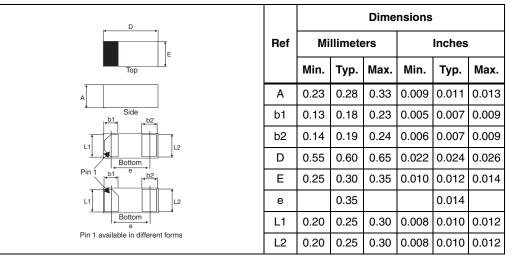
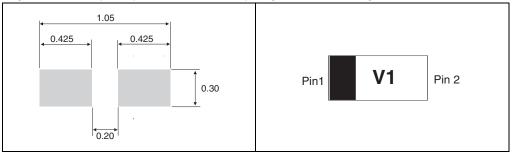


Figure 9. Footprint (dimensions in mm) Figure 10. Marking





Product marking may be rotated by multiples of 180° for assembly plant differentiation. In no case should this product marking be used to orient the component for its placement on a PCB. Only pin 1 mark is to be used for this purpose.



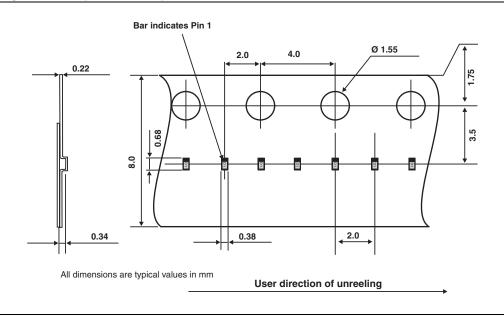


Figure 11. Tape and reel specifications

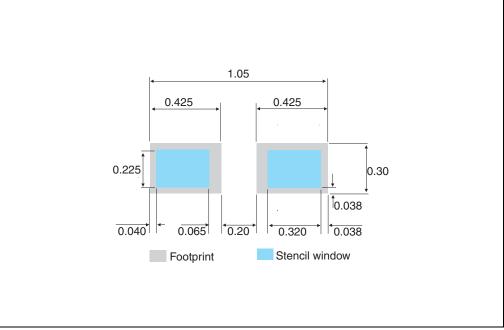
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4 Recommendation on PCB assembly

4.1 Stencil opening design





4.2 Solder paste

- 1. Halide-free flux qualification ROL0 according to ANSI/J-STD-004.
- 2. "No clean" solder paste is recommended.
- 3. Offers a high tack force to resist component movement during high speed
- 4. Solder paste with fine particles: powder particle size is 20-45 μ m.



4.3 Placement

- 1. Manual positioning is not recommended.
- 2. It is recommended to use the lead recognition capabilities of the placement system, not the outline centering
- 3. Standard tolerance of ± 0.05 mm is recommended.
- 4. 3.5 N placement force is recommended. Too much placement force can lead to squeezed out solder paste and cause solder joints to short. Too low placement force can lead to insufficient contact between package and solder paste that could cause open solder joints or badly centered packages.
- 5. To improve the package placement accuracy, a bottom side optical control should be performed with a high resolution tool.
- 6. For assembly, a perfect supporting of the PCB (all the more on flexible PCB) is recommended during solder paste printing, pick and place and reflow soldering by using optimized tools.

4.4 PCB design preference

- 1. To control the solder paste amount, the closed via is recommended instead of open vias.
- The position of tracks and open vias in the solder area should be well balanced. The symmetrical layout is recommended, in case any tilt phenomena caused by asymmetrical solder paste amount due to the solder flow away.

4.5 **Reflow profile**

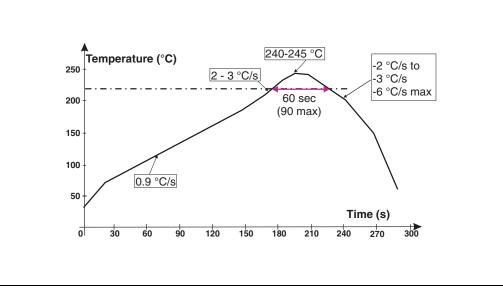


Figure 13. ST ECOPACK® recommended soldering reflow profile for PCB mounting

Note:

Minimize air convection currents in the reflow oven to avoid component movement.

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5 Ordering information

Table 4. Ordering information

Order code	Marking	Weight	Base qty	Delivery mode
ESDAULC6-1U2	V1 ⁽¹⁾	0.124 mg	15000	Tape and reel

1. The marking can be rotated by multiples of 180° to differentiate assembly location

6 Revision history

Table 5.Document revision history

Date	Revision	Changes
05-Jun-2012	1	Initial release.



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