MAXIM

Low-Voltage SIM/Smart Card Level Translators in µMAX

MAX1840/MAX1841

General Description

The MAX1840/MAX1841 subscriber identity module (SIM)/smart card level translators provide level shifting and electrostatic discharge (ESD) protection for SIM and smart card ports. These devices integrate two unidirectional level shifters for the reset and clock signals, a bidirectional level shifter for the serial data stream, and ±10kV ESD protection on all card contacts.

The MAX1840 includes a $\overline{SHDN}$ control input to aid insertion and removal of SIM and smart cards, while the MAX1841 includes a system-side data driver to support system controllers without open-drain outputs. The logic supply voltage range is +1.4V to +5.5V for the "controller side" and +1.7V to +5.5V for the "card side." Total supply current is 1.0µA. Both devices automatically shut down when either power supply is removed. For a complete SIM card interface, combine the MAX1840/MAX1841 with the MAX1686H 0V/3V/5V regulated charge pump.
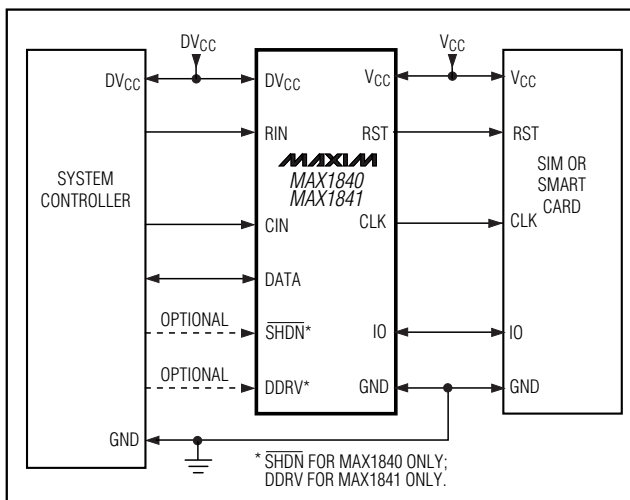
The MAX1840/MAX1841 are available in ultra-small 10-pin µMAX packages that are only 1.09mm high and half the area of an 8-pin SO.

The MAX1840/MAX1841 are compliant with GSM test specifications 11.11 and 11.12.

Applications

SIM Interface in GSM Cellular Telephones

Smart Card Readers

Logic Level Translation

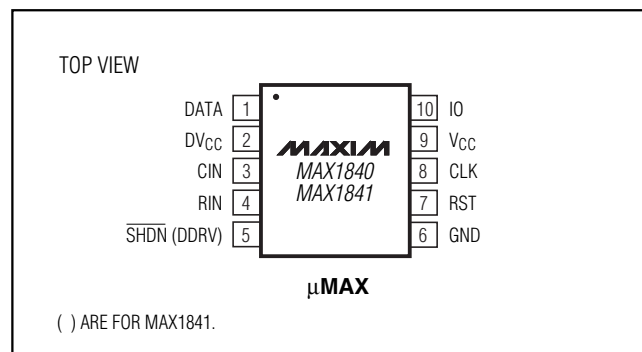SPI™/QSPI™/MICROWIRE™ Level Translation

Features

♦ **SIM/Smart Card Level Shifting**

♦ **+1.4V to +5.5V Controller Voltage Range**

♦ **+1.7V to +5.5V Card Voltage Range**

♦ **±10kV ESD Card Socket Protection**

♦ **Allows Level Translation with $DV_{CC} \geq V_{CC}$ or $DV_{CC} \leq V_{CC}$**

♦ **Automatically Shuts Down When Either Supply Is Removed**

♦ **Card Contacts Actively Pulled Low During Shutdown**

♦ **1µA Total Quiescent Supply Current**

♦ **0.01µA Total Shutdown Supply Current**

♦ **Ultra-Small 10-Pin µMAX Package**

♦ **Compliant with GSM Test Specifications 11.11 and 11.12**

Ordering Information

PART	TEMP. RANGE	PIN-PACKAGE
MAX1840EUB	-40°C to +85°C	10 µMAX
MAX1841EUB	-40°C to +85°C	10 µMAX

Typical Operating Circuit

* $\overline{SHDN}$ FOR MAX1840 ONLY; DDRV FOR MAX1841 ONLY.

Pin Configuration

TOP VIEW

DATA	1			10	IO
DV_{CC}	2	*MAX1840 MAX1841*		9	V_{CC}
CIN	3			8	CLK
RIN	4			7	RST
$\overline{SHDN}$ (DDRV)	5			6	GND

µMAX

( ) ARE FOR MAX1841.

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MICROWIRE is a trademark of National Semiconductor Corp.

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For small orders, phone 1-800-835-8769.

Low-Voltage SIM/Smart Card Level Translators in µMAX

ABSOLUTE MAXIMUM RATINGS

DV$_{CC}$, V$_{CC}$ to GND...-0.3V to +6.0V
RIN, CIN, DATA, DDRV,
$\overline{SHDN}$ to GND ....-0.3V to (DV$_{CC}$ + 0.3V)
RST, CLK, IO to GND ....-0.3V to (V$_{CC}$ + 0.3V)
Continuous Power Dissipation (T$_A$ = +70°C)
10-Pin µMAX (derate 5.6mW/°C above +70°C) 444mW

Operating Temperature Range ....-40°C to +85°C
Storage Temperature Range ....-65°C to +150°C
Junction Temperature ....+150°C
Lead Temperature (soldering, 10s) ....+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

(Figure 1, DV$_{CC}$ = +1.8V; V$_{CC}$ = +1.8V, +3.0V, or +5.0V; $\overline{SHDN}$ = DV$_{CC}$, CIN = RIN = GND or DV$_{CC}$, IO = V$_{CC}$, DATA = DDRV = DV$_{CC}$, C$_{IO}$ = C$_{CLK}$ = C$_{RST}$ = C$_{DATA}$ = 30pF, **T$_A$ = -40°C to +85°C**, unless otherwise noted. Typical values are at T$_A$ = +25°C.) (Note1)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
POWER SUPPLIES						
DV$_{CC}$ Operating Range	DV$_{CC}$		1.4		5.5	V
V$_{CC}$ Operating Range	V$_{CC}$		1.7		5.5	V
DV$_{CC}$ Operating Current	I$_{DVCC}$	CIN static		0.1	0.5	µA
		CIN clocked at 1.625MHz from GND to DV$_{CC}$ with 50% duty cycle		2.5		
		CIN clocked at 3.25MHz from GND to DV$_{CC}$ with 50% duty cycle		5		
V$_{CC}$ Operating Current	I$_{VCC}$	CIN static		0.9	3.0	µA
		CIN clocked at 1.625MHz from GND to DV$_{CC}$ with 50% duty cycle		0.4		mA
		CIN clocked at 3.25MHz from GND to DV$_{CC}$ with 50% duty cycle		0.8		
Total Shutdown Current	I$_{SHDN}$	I$_{OFF}$ = I$_{VCC}$ + I$_{DVCC}$, $\overline{SHDN}$ = GND (MAX1840 only), or DV$_{CC}$ = GND or V$_{CC}$ = GND		0.01	1	µA
CIN, RIN, $\overline{SHDN}$, DDRV LOGIC INPUTS						
Digital Input Low Threshold	V$_{IL}$		0.2 × DV$_{CC}$			V
Digital Input High Threshold	V$_{IH}$				0.7 × DV$_{CC}$	V
Input Leakage Current				0.01	1	µA
CLK, RST OUTPUTS						
Digital Output Low Level	V$_{OL}$	I$_{SINK}$ = 200µA			0.4	V
Digital Output High Level	V$_{OH}$	I$_{SOURCE}$ = 20µA	0.9 × V$_{CC}$			V
		I$_{SOURCE}$ = 200µA	0.8 × V$_{CC}$			
DATA INPUT/OUTPUT						
DATA Pullup Resistance	R$_{DATA}$	Between DATA and DV$_{CC}$	13	20	28	kΩ
Input Low Threshold	V$_{IL(DATA)}$	(Note 2)	0.3			V
Input High Threshold	V$_{IH(DATA)}$	(Note 3)			DV$_{CC}$ - 0.6	V
Input Low Current	I$_{IL}$	V$_{CC}$ = 5.0V			1	mA
Input High Current	I$_{IH}$				2	µA

MAXIM

MAX1840/MAX1841

ELECTRICAL CHARACTERISTICS (continued)

(Figure 1, DV_{CC} = +1.8V; V_{CC} = +1.8V, +3.0V, or +5.0V; $\overline{SHDN}$ = DV_{CC}, CIN = RIN = GND or DV_{CC}, IO = V_{CC}, DATA = DDRV = DV_{CC}, C_{IO} = C_{CLK} = C_{RST} = C_{DATA} = 30pF, **T_A = -40°C to +85°C**, unless otherwise noted. Typical values are at T_A = +25°C.) (Note1)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Output Low Level	$V_{OL(DATA)}$	IO = GND, I_{SINK} = 100µA			0.4	V
		DV_{CC} = 3.0V, IO = GND, I_{SINK} = 200µA			0.4	
Output High Level	$V_{OH(DATA)}$	I_{SOURCE} = 10µA	$0.7 \times DV_{CC}$			V
		DV_{CC} = 3.0V, I_{SOURCE} = 20µA	$0.7 \times DV_{CC}$			
IO (INPUT/OUTPUT)						
IO Pullup Resistance	R_{IO}	Between IO and V_{CC}	6.5	10	14	kΩ
Input Low Threshold	$V_{IL(IO)}$	$I_{IL(MAX)}$ = 1mA (Note 2)	0.3			V
Input High Threshold	$V_{IH(IO)}$	$I_{IH(MAX)}$ = ±20µA (Note 3)			$0.7 \times V_{CC}$	V
Input Low Current	I_{IL}				1	mA
Input High Current	I_{IH}				20	µA
Output Low Level	$V_{OL(IO)}$	DATA = GND or DDRV = GND, I_{SINK} = 200µA			0.4	V
Output High Level	$V_{OH(IO)}$	I_{SOURCE} = 20µA	$0.8 \times V_{CC}$			V
SHUTDOWN OUTPUT LEVELS						
Shutdown Output Levels (IO, CLK, RST)		I_{SINK} = 200µA, $\overline{SHDN}$ = GND, DATA = CIN = RIN = DV_{CC} (MAX1840 only)			0.4	V
		I_{SINK} = 200µA, DV_{CC} = GND, $\overline{SHDN}$ (MAX1840) = DDRV (MAX1841) = DATA = CIN = RIN = DV_{CC}			0.4	V
		I_{SINK} = 200µA, V_{CC} = GND, $\overline{SHDN}$ (MAX1840) = DDRV (MAX1841) = DATA = CIN = RIN = DV_{CC}			0.4	V
TIMING						
Maximum CLK Frequency (Notes 4, 5)	f_{CLK}	V_{CC} = 2.7V to 5.5V, DV_{CC} = 1.4V to 2.7V	5	20		MHz
		V_{CC} = 1.7V to 3.6V, DV_{CC} = 1.4V to 2.25V	5	15		

Note 1: Specifications to -40°C are guaranteed by design, not production tested.
Note 2: V_{IL} is defined as the voltage at which the output (DATA/IO) voltage equals 0.5V.
Note 3: V_{IH} is defined as the voltage at which the output (DATA/IO) voltage exceeds the input (IO/DATA) voltage by 100mV.
Note 4: Timing specifications are guaranteed by design, not production tested.
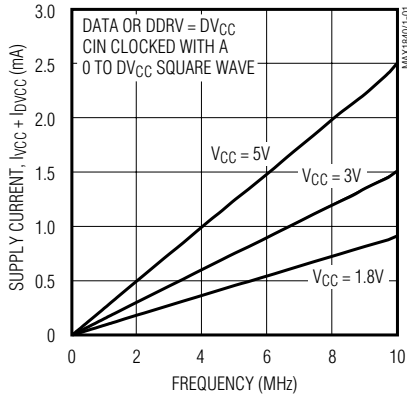**Note 5:** The maximum CLK frequency is defined as the output duty cycle remaining in the 40% to 60% range when the 50% CIN is applied. CIN has 5ns rise and fall times; levels are GND to DV_{CC}. Input and output levels are measured at 50% of the waveform.

MAXIM _____ **3**

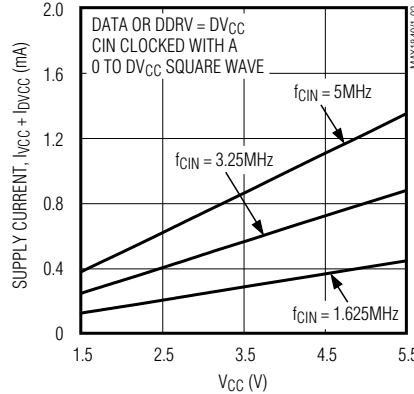# Low-Voltage SIM/Smart Card
Level Translators in µMAX

MAX1840/MAX1841

_____Typical Operating Characteristics_

(Circuit of Figure 1, DV_{CC} = 3.0V, V_{CC} = +5.0V, DDRV or DATA = DV_{CC}, RIN = CIN = GND, T_A = +25°C, unless otherwise noted.)

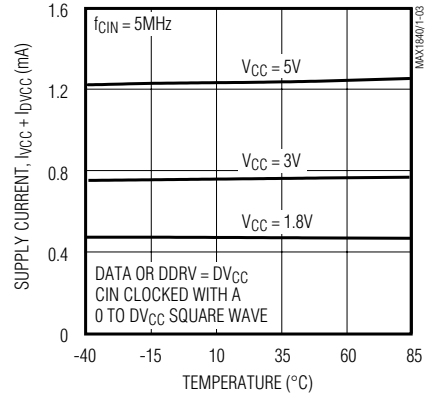**TOTAL OPERATING SUPPLY CURRENT vs. CLOCK FREQUENCY (DATA STATIC)**

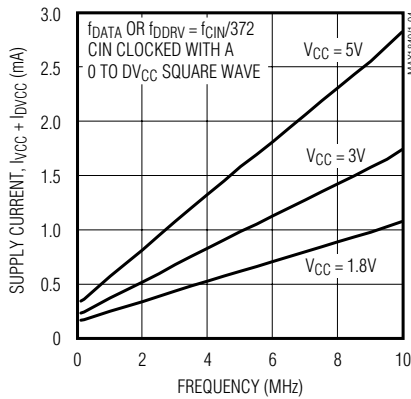DATA OR DDRV = DV_{CC}
CIN CLOCKED WITH A
0 TO DV_{CC} SQUARE WAVE

V_{CC} = 5V
V_{CC} = 3V
V_{CC} = 1.8V

MAX1840/1-01

TOTAL OPERATING SUPPLY CURRENT vs. CARD-SIDE SUPPLY VOLTAGE (DATA STATIC)

DATA OR DDRV = DV_{CC}
CIN CLOCKED WITH A
0 TO DV_{CC} SQUARE WAVE

f_{CIN} = 5MHz
f_{CIN} = 3.25MHz
f_{CIN} = 1.625MHz

MAX1840/1-02

TOTAL OPERATING SUPPLY CURRENT vs. TEMPERATURE (DATA STATIC)

f_{CIN} = 5MHz

V_{CC} = 5V
V_{CC} = 3V
V_{CC} = 1.8V

DATA OR DDRV = DV_{CC}
CIN CLOCKED WITH A
0 TO DV_{CC} SQUARE WAVE

MAX1840/1-03

TOTAL OPERATING SUPPLY CURRENT vs. CLOCK FREQUENCY (DATA ACTIVE)

f_{DATA} OR f_{DDRV} = f_{CIN}/372
CIN CLOCKED WITH A
0 TO DV_{CC} SQUARE WAVE

V_{CC} = 5V
V_{CC} = 3V
V_{CC} = 1.8V

MAX1840/1-04

TOTAL OPERATING SUPPLY CURRENT vs. CARD-SIDE SUPPLY VOLTAGE (DATA ACTIVE)

f_{DATA} OR f_{DDRV} = f_{CIN}/372
CIN CLOCKED WITH A
0 TO DV_{CC} SQUARE WAVE

f_{CIN} = 5MHz
f_{CIN} = 3.25MHz
f_{CIN} = 1.625MHz

MAX1840/1-05
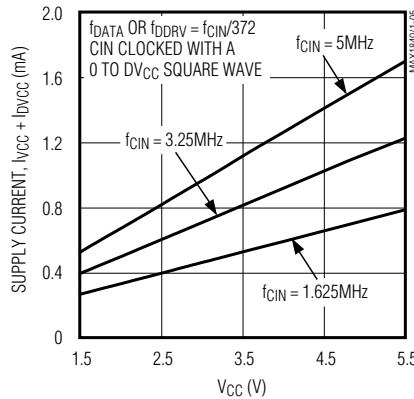
TOTAL OPERATING SUPPLY CURRENT vs. TEMPERATURE (DATA ACTIVE)
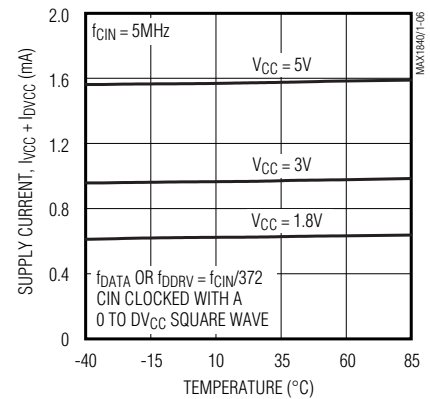
f_{CIN} = 5MHz

V_{CC} = 5V
V_{CC} = 3V
V_{CC} = 1.8V

f_{DATA} OR f_{DDRV} = f_{CIN}/372
CIN CLOCKED WITH A
0 TO DV_{CC} SQUARE WAVE

MAX1840/1-06

CIN TO CLK OR RIN TO RST WAVEFORM (3V TO 5V)

f_{CIN} = 5MHz

CIN OR RIN 2V/div
CLK OR RST 2V/div

20ns/div

MAX1840/1-07

CIN TO CLK OR RIN TO RST WAVEFORM (3V TO 1.8V)

f_{CIN} = 5MHz

CIN OR RIN 2V/div
CLK OR RST 1V/div

20ns/div

MAX1840/1-08
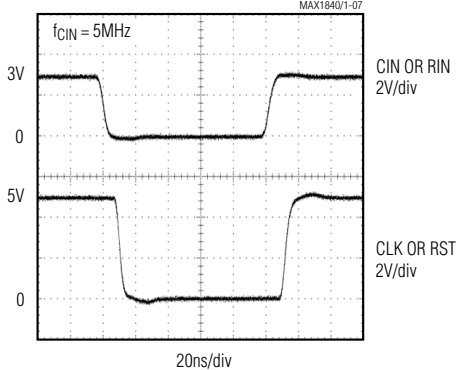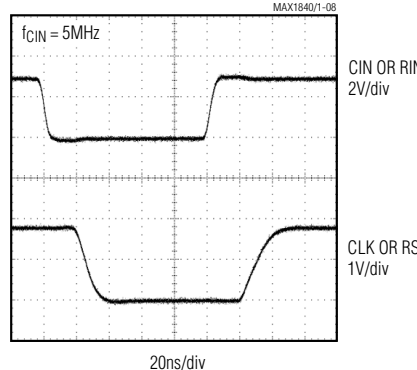
CIN TO CLK OR RIN TO RST WAVEFORM (1.8V TO 1.8V)
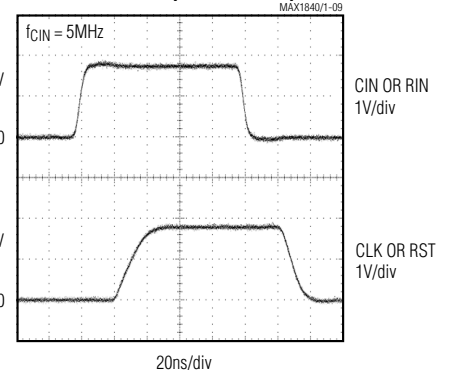
f_{CIN} = 5MHz

CIN OR RIN 1V/div
CLK OR RST 1V/div

20ns/div

MAX1840/1-09

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MAX1840/MAX1841

Typical Operating Characteristics (continued)

(Circuit of Figure 1, DV$_{CC}$ = 3.0V, V$_{CC}$ = +5.0V, DDRV or DATA = DV$_{CC}$, RIN = CIN = GND, T$_A$ = +25°C, unless otherwise noted.)

**UNDERVOLTAGE
SHUTDOWN WAVEFORM**

MAX1840/1-10

**DDRV TO IO AND DATA WAVEFORM
(MAX1841 ONLY)**
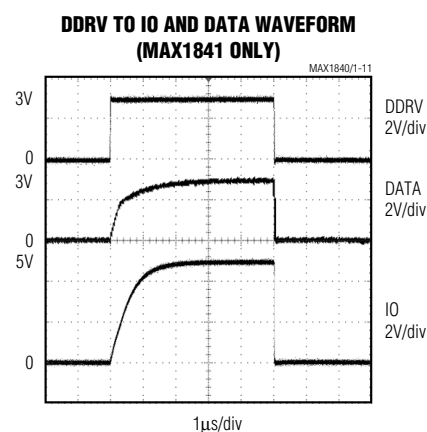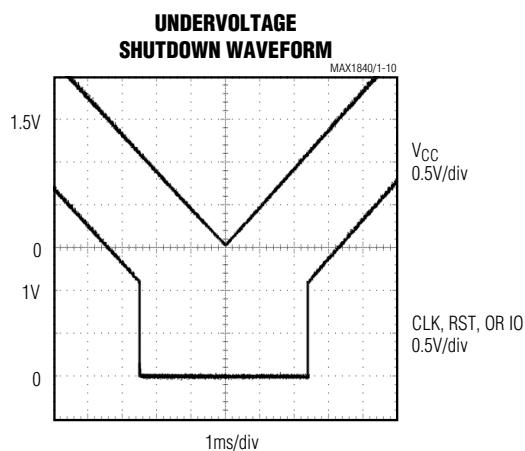
MAX1840/1-11

Pin Description

PIN		NAME	FUNCTION
MAX1840	MAX1841		
1	1	DATA	System Controller Data Input/Output. An open-drain IO with a 20kΩ pull-up resistor to DV$_{CC}$. For bidirectional data transfer, connect to an open-drain controller output capable of sinking 1mA while pulling DATA low. If the controller is not open-drain, use DDRV to send data and DATA to receive data.
2	2	DV$_{CC}$	Supply Voltage for System Controller Digital Pins. Set at +1.4V to +5.5V.
3	3	CIN	System Controller Clock Input
4	4	RIN	System Controller Reset Input
—	5	DDRV	Optional System Controller Data Input. Connect to controllers without an open-drain output. When not used, connect DDRV to DV$_{CC}$.
5	—	$\overline{SHDN}$	Shutdown Mode Input. Driving $\overline{SHDN}$ low reduces the total supply current to less than 1µA. In shutdown mode, RST, CLK, and IO are actively pulled low and the transfer gate between DATA and IO is disabled. When not used, connect $\overline{SHDN}$ to DV$_{CC}$.
6	6	GND	System Controller and Card Ground
7	7	RST	Reset Output to Card. Actively pulled low during shutdown.
8	8	CLK	Clock Output to Card. Actively pulled low during shutdown.
9	9	V$_{CC}$	Supply Voltage for Card-Side Digital Pins. Set at +1.7V to +5.5V. Proper supply bypassing is required to meet ±10kV ESD specifications.
10	10	IO	Card-Side Bidirectional Input/Output. An open-drain output with a 10kΩ pull-up resistor to V$_{CC}$. For bidirectional data transfer, connect to an open-drain card output capable of sinking 1mA while pulling IO low. Actively pulled low during shutdown.

MAXIM

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Low-Voltage SIM/Smart Card Level Translators in µMAX

Detailed Description

The MAX1840/MAX1841 provide the necessary level translation for interfacing with SIMs and smart cards in multivoltage systems. These devices operate with logic supply voltages between +1.4V and +5.5V on the controller side (DV$_{CC}$) and between +1.7V and +5.5V on the card side (V$_{CC}$). The total supply current (I$_{DVCC}$ + I$_{VCC}$) is 1µA while operating in an idle state (see *Electrical Characteristics*). Figure 1 shows the MAX1840/MAX1841 test circuit. The *Typical Application Circuit* appears at the end of this data sheet.

Level Translation

The MAX1840/MAX1841 provide level translators for a clock input, a reset input, and a bidirectional data IO. The clock and reset inputs (CIN and RIN) are level shifted from the controller-side supply rails (DV$_{CC}$ to GND) to the card-side supply rails (V$_{CC}$ to GND). When connected to an open-drain controller output, DATA and IO provide bidirectional level translation. All level translation is valid for DV$_{CC}$ ≥ V$_{CC}$ or DV$_{CC}$ ≤ V$_{CC}$. The MAX1840/MAX1841 contain internal pull up resistors from DATA to the controller-side supply (DV$_{CC}$) and from IO to the card-side supply (V$_{CC}$). For push-pull controller outputs, see the *Data Driver* section for bidirectional data translation.

Data Driver (MAX1841 Only)

When using a microcontroller (µC) without an open-drain output, use the data driver (DDRV) input to send data to the SIM/smart card, while DATA provides the controller-side output for bidirectional data transfer. When not used, connect DDRV to DV$_{CC}$ to reduce total supply current.

Shutdown Mode

For the MAX1840, drive $\overline{SHDN}$ low to activate shutdown. Connect $\overline{SHDN}$ to DV$_{CC}$ or drive high for normal operation. To allow for card insertion and removal, shutdown mode actively pulls CLK, RST, and IO low; it also disconnects the internal 10kΩ pull up resistor from V$_{CC}$ to prevent excessive current draw. Shutdown mode reduces the total supply current (I$_{DVCC}$ + I$_{VCC}$) to 0.01µA.

SIM/Smart Card Insertion/Removal

The SIM/smart card specifications require that the card-side pins (V$_{CC}$, CLK, RST, IO) be at ground potential prior to inserting the SIM/smart card. For applications using the MAX1686H (Figure 3), the easiest way to achieve this is by shutting down the MAX1686H or by
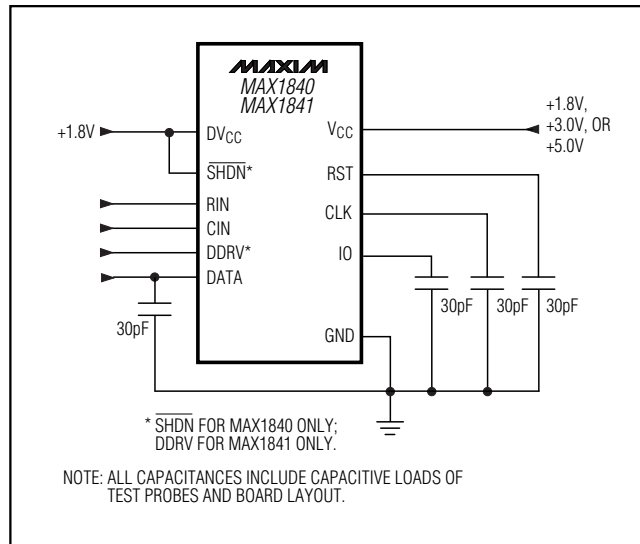

Figure 1. MAX1840/MAX1841 Test Circuit

driving SHDN (MAX1840 only) low. If specific sequencing is desired, pull IO low by driving either DATA or DDRV (MAX1841 only) low, and pull CLK and RST low by driving CIN and RIN low, respectively.

ESD Protection

As with all Maxim devices, ESD-protection structures on all pins protect against ESDs encountered during handling and assembly. For further protection during card insertion and removal, the pins that connect to the card socket (CLK, RST, IO, V$_{CC}$, and GND) provide protection against ±10kV of ESD. The ESD structures withstand high ESD in all states: normal operation, shutdown, and power-down. After an ESD event, the MAX1840/MAX1841 continue working without latchup. A 1µF bypass capacitor from V$_{CC}$ to GND is required to exceed ±10kV ESD specifications.

ESD Test Conditions

ESD performance depends on a variety of conditions. Contact Maxim for a reliability report documenting test setup, test methodology, and test results.

Applications Information

SIM/Smart Card Interface

To provide 5V when interfacing with a 5V SIM/smart card, +3V systems require a DC-DC converter. The MAX1686H +5V regulating charge pump for SIM cards provides
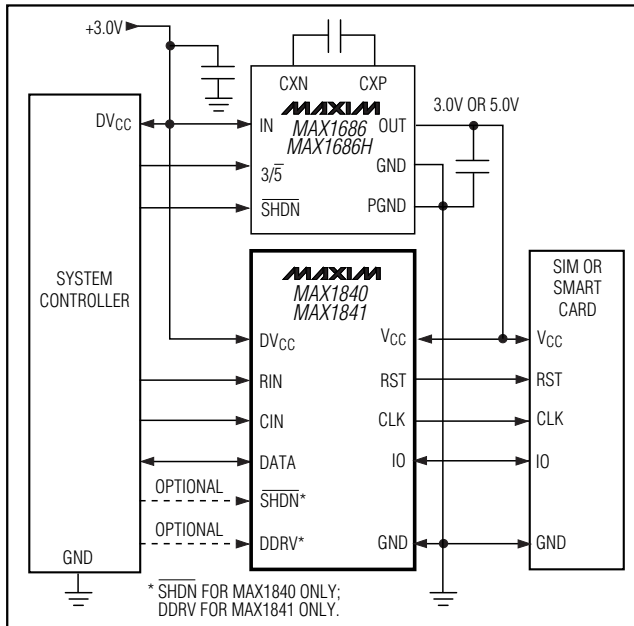
MAXIM

Figure 2. Using MAX1840/MAX1841 and MAX1686/MAX1686H Charge Pump for SIM Card Applications
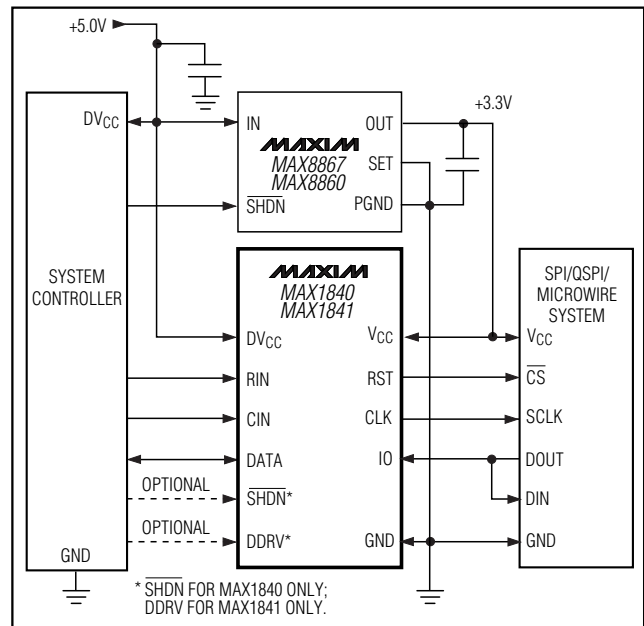

Figure 3. MAX1840/MAX1841 with SPI/QSPI/MICROWIRE Interfaces

0V/3V/5V for full compatibility with SIM/smart-card specifications. Figure 2 shows the charge pump for SIM card applications. Alternatively, the MAX619 generates a regulated 5V from input voltages as low as 2V.

SPI/QSPI/MICROWIRE Interface

The MAX1840/MAX1841 are also useful as 3V/5V level shifters in SPI, QSPI, and MICROWIRE applications (Figure 3). On the slave side, connect CLK to SCLK, RST to $\overline{CS}$, and IO t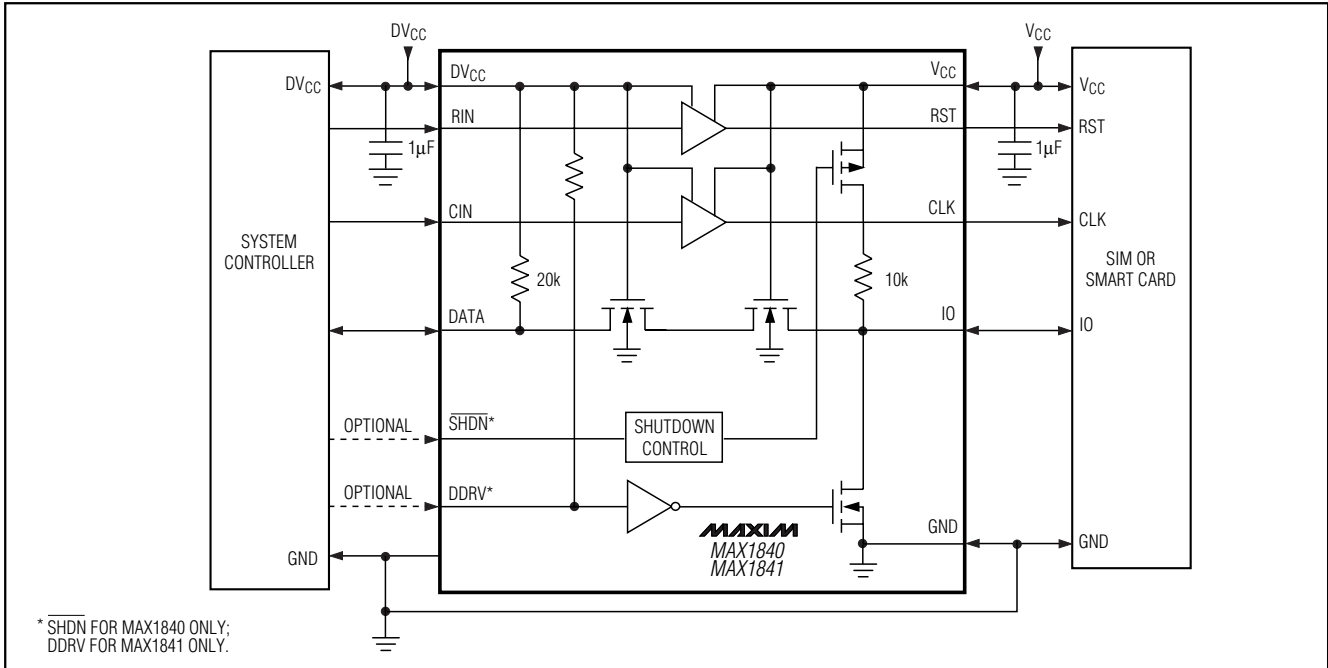o DOUT and DIN. The unidirectional level shifters transfer chip select and clock signals to the slave device(s), while the bidirectional level shifter transfers data.
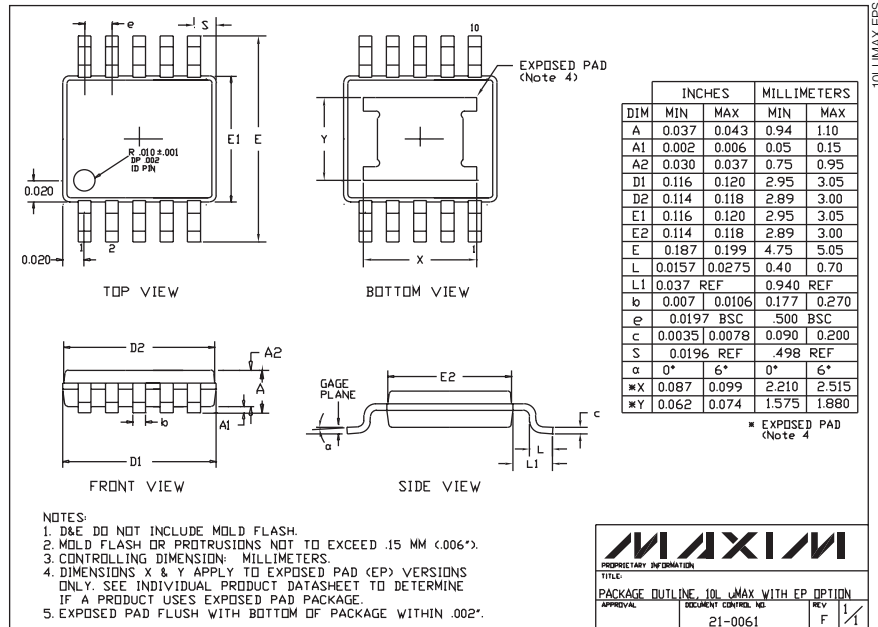
Chip Information

TRANSISTOR COUNT: 211

MAX1840/MAX1841

Typical Application Circuit

* SHDN FOR MAX1840 ONLY;
  DDRV FOR MAX1841 ONLY.

Packaging Information

Note: The MAX1840/MAX1841 do not have an exposed pad.

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