

February 2010



- Pletronics' VPU7 Series is a quartz crystal controlled precision square wave generator with a PECL output
- See VLU7 for LVDS output
- The package is designed for high density surface mount designs
- 10.9 MHZ to 1,175MHz
- 5 x 7 mm LCC Ceramic Package
- Low Jitter

Pletronics Inc. certifies this device is in accordance with the RoHS (2002/95/EC) and WEEE (2002/96/EC) directives.

Pletronics Inc. guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's Weight of the Device: 0.28 grams Moisture Sensitivity Level: 1 As defined in J-STD-020D.1 Second Level Interconnect code: e4

Absolute Maximum Ratings:

Parameter	Unit
V _{cc} Supply Voltage	-0.5V to +4.6V
Vi Input Voltage	-0.5V to V _{cc} + 0.5V
Vo Output Voltage	-0.5V to V _{cc} + 0.5V
I _o Output Current	-50mA

Thermal Characteristics

The maximum die or junction temperature is 155°C The thermal resistance junction to board is 30 to 50°C/Watt depending on the solder pads, ground plane and construction of the PCB.



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Part Number:

VPU7029036	EG	000	050	- 312.5M	-XX	
						Packaging code or blank T250 = 250 per Tape and Reel T500 = 500 per Tape and Reel T1K = 1000 per Tape and Reel
						Frequency in MHZ
						Pullability in ppm (Vcontrol) APR $050 = \pm 50$ ppm minimum is standard $075 = \pm 75$ ppm minimum $100 = \pm 100$ ppm minimum
						Series Model
						Temperature Range EG = -10 to +70°C LK = -40 to +85°C
						Series Model

Part Marking:

PLE VPU7	Marking Legend:
<i>FF.FFF</i> M	PLE = Pletronics
• YMDXX	<i>FF.FFF</i> M = Frequency in MHZ <i>YMD</i> = Date of Manufacture (year-month-day) All other marking is internal factory codes

Codes for Date Code YMD

Code	9	0	1	2	3	Code	⇒ A	В	С	D	Ε	F	G	Н	J	Κ	L	М
Year	2009	2010	2011	2012	2 201	3 Mont	h JAN	FEB	MAR	APR	MAY	JUN	JUL	AUG	SEP	OCT	NOV	DEC
(Code		1	2	3	4	5	6	7	8	9	Α	В	С	D	Е	F	G
	Day		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
(Code		Н	J	К	L	М	Ν	Р	R	Т	U	V	W	Х	Υ	Z	
	Day		17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	



VPU7 High Frequency PECL VCXO February 2010

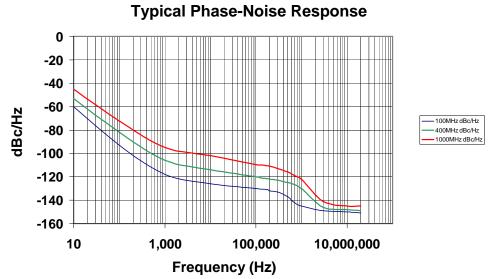
Electrical Specification for $3.30V \pm 10\%$ over the specified temperature range and the frequency range of 10.9 MHZ to 766 MHZ and 876 MHZ to 1,175MHz

Item	Min	Max	Unit	Condition
Pullability, Absolute Pull Range	-50 -75 -100	+50 +75 +100	ppm	APR includes the effect of temperature stability, aging, supply voltage and load. Defined by part number.
Output Waveform		PECL / E	ECL	
Output High Level	2.12	2.49	volts	Referenced to Ground, V_{cc} = 3.3 V
	0.82	1.19	volts	Referenced to termination voltage, V_{cc} = 3.3 V
	-1.18	-0.81	volts	Referenced to Vcc, V_{cc} = 3.3 V
Output Low Level	1.83	1.99	volts	Referenced to Ground, V_{cc} = 3.3 V
	0.53	0.69	volts	Referenced to termination voltage, V_{cc} = 3.3 V
	-1.47	-1.31	volts	Referenced to Vcc, V_{cc} = 3.3 V
Output Peak to Peak Level	0.405	1.076	volts	
Output Symmetry	47	53	%	at 50% point of V_{cc} (See load circuit)
Modulation Bandwidth	10	-	KHz	Vcontrol = 1.65V <u>+</u> 1.50 V , -3dB
Vcontrol Resistance (Pad 1)	20	-	Kohm	
Voltage vs Frequency Linearity	-10	+10	%	Vcontrol = 1.65V <u>+</u> 1.50 V
Jitter	-	0.8	pS RMS	12 KHz to 20 MHZ from the output frequency
	-	3.2	pS RMS	10 Hz to 20 MHZ from the output frequency
Output T_{RISE} and T_{FALL}	100	300	pS	Vth is 20% and 80% of waveform
V _{cc} Supply Current (I _{cc})	-	110	mA	
Enable/Disable Internal Pull-up	50	-	Kohm	to V _{cc}
V disable	-	0.8	volts	Referenced to pad 3
V enable	2.00	-	volts	Referenced to pad 3
Output leakage $V_{OUT} = V_{CC}$	-50	+50	uA	Pad 1 low, device disabled
$V_{OUT} = 0V$	-50	+50	uA	
Enable time	-	10	nS	Time for output to reach a logic state
Disable time	-	10	nS	Time for output to reach a high Z state
Start up time	-	5	mS	Time for output to reach specified frequency
Operating Temperature Range	-10	+70	°C	Standard Temperature Range
	- 40	+85	°C	Extended Temperature Range
Storage Temperature Range	-55	+125	°C	

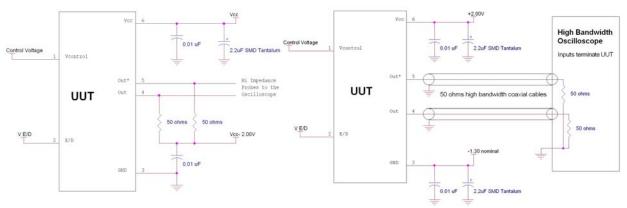
Specifications with Pad 2 E/D open circuit or connected to $V_{\mbox{\tiny CC}}$



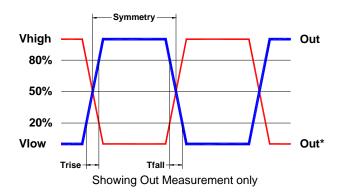
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Load Circuit







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425-776-1880



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Reliability: Environmental Compliance

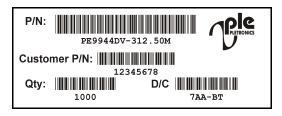
Parameter	Condition
Mechanical Shock	MIL-STD-883 Method 2002, Condition B
Vibration	MIL-STD-883 Method 2007, Condition A
Solderability	MIL-STD-883 Method 2003
Thermal Shock	MIL-STD-883 Method 1011, Condition A

ESD Rating

Model	Minimum Voltage	Conditions		
Human Body Model	2000	MIL-STD-883 Method 3115		
Charged Device Model	1500	JESD 22-C101		

Package Labeling

Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Courier New Bar code is 39-Full ASCII The part number will be in the PE99 line.



Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Arial

RoHS Compliant

2nd LvL Interconnect Category=e4 Max Safe Temp=260C for 10s 2X Max

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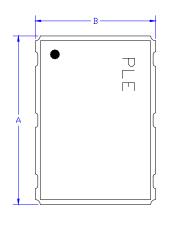


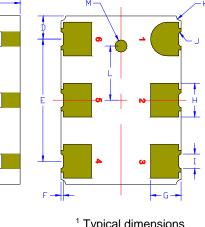
Inches

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mm

Mechanical:





Contacts (pads): Gold 11.8 to 39.4 µinches (0.3 to 1.0 µm) over Nickel 50 to 350 µinches (1.27 to 8.89 µm)

Center metalized pad on the base is internally connected, may be open or connected to $V_{\rm cc}$ or to Ground.

¹ Typical dimensions

Not to Scale

К	А	0.276 <u>+</u> 0.006	7.00 <u>+</u> 0.15
	В	0.197 <u>+</u> 0.006	5.00 <u>+</u> 0.15
	С	0.117 max	2.97 max
	D^1	0.038	0.96
	E ¹	0.200	5.08
	F ¹	0.004	0.10
	G1	0.050	1.27
	H^1	0.055	1.40
	l ¹	0.024	0.60
	J^1	0.004r	0.10r
	K ¹	0.008r	0.20r
	L^1	0.089	2.25
	M^1	0.010r	0.25r

Do not permit solder to bridge the upper gold contacts on the side

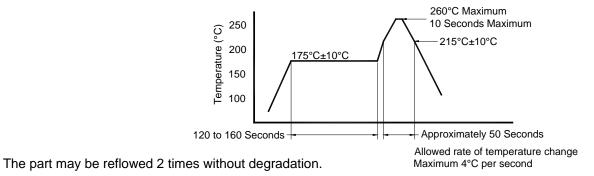
Pad	Function	Note
1	Vcontrol	Modulates the output frequency
2	Output Enable/Disable	When this pad is not connected the oscillator shall operate. When this pad is <0.80 volts, the output will be inhibited (high impedance state.) Recommend connecting this pad to V_{cc} if the oscillator is to be always on
3	Ground (GND)	
4	Output	Both outputs must be terminated and biased for proper operation. The ideal termination is 50 ohms connected to 2.0V below the Supply Voltage.
5	Output*	The outputs become a High Z when disabled and the voltage level is determined by the termination circuitry.
6	Supply Voltage (V _{cc})	Recommend connecting appropriate power supply bypass capacitors as close as possible.

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Reflow Cycle (typical for lead free processing)



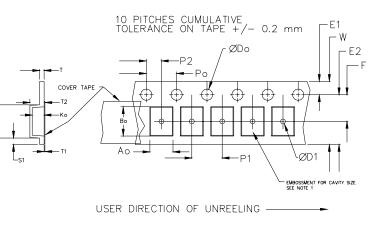
Tape and Reel: available for quantities of 250 to 1000 per reel, cut tape for < 250

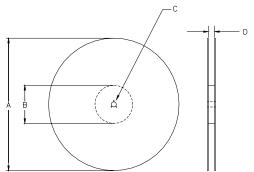
Not to scale

Constant Dimensions Table 1									
Tape Size	D0	D1 Min	E1	P0	P2	S1 Min	T Max	T1 Max	
8mm		1.0			2.0				
12mm	1.5	1.5	1.75	4.0	<u>+</u> 0.05				
16mm	+0.1 -0.0	1.5	<u>+</u> 0.1	<u>+</u> 0.1	2.0	0.6	0.6	0.1	
24mm		1.5			<u>+</u> 0.1				

Variable Dimensions Table 2									
Tape Size	B1 Max	E2 Min	F	P1	T2 Max	W Max	Ao, Bo & Ko		
16 mm	12.1	14.25	7.5 <u>+</u> 0.1	8.0 <u>+</u> 0.1	8.0	16.3	Note 1		

Note 1: Embossed cavity to conform to EIA-481-B Dimensions in mm





		REE	REEL DIMENSIONS							
А	inches	7.0	10.0	13.0						
	mm	177.8	254.0	330.2						
В	inches	2.50	4.00	3.75						
	mm	63.5	101.6	95.3	Tape Width					
С	mm	13	3.0 +0.5 / -0.	.2	width					
D	mm	16.4 +2.0 -0.0	16.4 +2.0 -0.0	16.4 +2.0 -0.0	16.0					
	mm			24.4 +2.0 -0.0	24.0					
	mm			32.4 +2.0 -0.0	32.0					

Reel dimensions may vary from the above

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Contacting Pletronics Inc.

Pletronics Inc. 19013 36th Ave. West Lynnwood, WA 98036-5761 USA Tel: 425-776-1880 Fax: 425-776-2760 E-mail: <u>ple-sales@pletronics.com</u> URL: <u>www.pletronics.com</u>

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