LM195QML

LM195QML Ultra Reliable Power Transistors



Literature Number: SNLS194



LM195QML

Ultra Reliable Power Transistors

General Description

The LM195 is a fast, monolithic power integrated circuit with complete overload protection. This device, which acts as a high gain power transistor, has included on the chip, current limiting, power limiting, and thermal overload protection making it virtually impossible to destroy from any type of overload.

The inclusion of thermal limiting, a feature not easily available in discrete designs, provides virtually absolute protection against overload. Excessive power dissipation or inadequate heat sinking causes the thermal limiting circuitry to turn off the device preventing excessive heating.

The LM195 offers a significant increase in reliability as well as simplifying power circuitry. In some applications, where protection is unusually difficult, such as switching regulators, lamp or solenoid drivers where normal power dissipation is low, the LM195 is especially advantageous.

The LM195 is easy to use and only a few precautions need be observed. Excessive collector to emitter voltage can destroy the LM195 as with any power transistor. When the device is used as an emitter follower with low source impedance, it is necessary to insert a 5.0k resistor in series with the base lead to prevent possible emitter follower oscillations. Although the device is usually stable as an emitter follower, the resistor eliminates the possibility of trouble without degrading performance. Finally, since it has good high frequency response, supply bypassing is recommended.

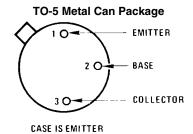
Features

- Internal thermal limiting
- Greater than 1.0A output current
- 3.0 µA typical base current
- 500 ns switching time
- 2.0V saturation
- Base can be driven up to 40V without damage
- Directly interfaces with CMOS or TTL
- 100% electrical burn-in

Ordering Information

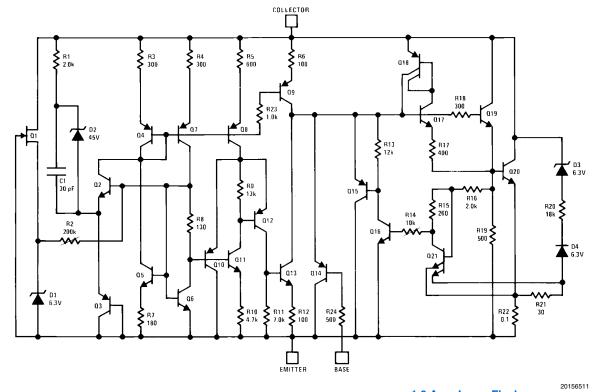
NS Part Number		SMD Part Number	NS Package Number Package Descr		
	LM195H/883	5962-8777801XA	H03B	3LD T0-5 Metal Can	

Connection Diagram

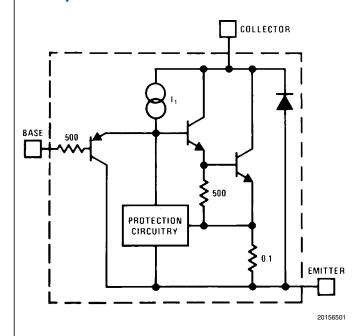


Bottom View
See NS Package Number H03B

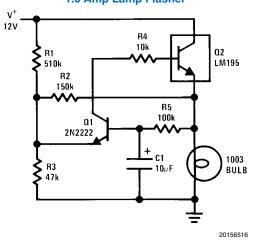
Schematic Diagram



Simplified Circuit



1.0 Amp Lamp Flasher



Absolute Maximum Ratings (Note 1)

Collector to Emitter Voltage 42V
Collector to Base Voltage 42V
Base to Emitter Voltage (Forward) 42V
Base to Emitter Voltage (Reverse) 20V

Collector Current Internally Limited
Power Dissipation (*Note 2*) Internally Limited

Operating Temperature Range

H-Pkg $-55^{\circ}\text{C} \leq \text{T}_{\text{A}} \leq +125^{\circ}\text{C}$ Storage Temperature Range $-65^{\circ}\text{C} \leq \text{T}_{\text{A}} \leq +150^{\circ}\text{C}$

Lead Temperature (Soldering, 10 sec.) 260°C

Thermal Resistance

 θ_{JA}

H-Pkg; Still Air @ 0.5W 192°C/W H-Pkg; 500LF/Min Air Flow @ 0.5W 66°C/W

 θ_{JC}

H-Pkg @ 1.0W 29°C/W

Quality Conformance Inspection

Mil-Std-883, Method 5005 - Group A

Subgroup	Description	Temp (°C)
1	Static tests at	+25
2	Static tests at	+125
3	Static tests at	-55
4	Dynamic tests at	+25
5	Dynamic tests at	+125
6	Dynamic tests at	-55
7	Functional tests at	+25
8A	Functional tests at	+125
8B	Functional tests at	-55
9	Switching tests at	+25
10	Switching tests at	+125
11	Switching tests at	-55
12	Settling time at	+25
13	Settling time at	+125
14	Settling time at	-55

LM195H/883 Electrical Characteristics

DC Parameter Collector to Emitter

Symbol	Parameter	Conditions	Notes	Min	Max	Unit	Sub- groups
V _{CE}	Operating Voltage	I _C ≤ I _{Max}	(<i>Note 3</i>)		42	V	1, 2, 3

DC Parameter Base to Emitter

Symbol	Parameter	Conditions	Notes	Min	Max	Unit	Sub- groups
BV _{BE}	Breakdown Voltage	V _{CE} ≤ 42V	(Note 3)	42		V	1, 2, 3
	Collector Current	V		1.2		Α	1
I _{SC}		V _{CE} ≤ 7V		1		Α	2, 3
V	Octometica Veltoria	I _C = 1A			2	V	1, 2
V _{Sat}	Saturation Voltage	IC - IA			2.5	V	3
I _B	Base Current	$0 \le V_{BE} \le 42V,$ $I_C \le I_{Max}$			5	μΑ	1, 2, 3
IQ	Quiescent Current	$V_{CE} = 42V, V_{BE} = 0V$			5	mA	1, 2, 3
	Breakdown Delta V _{BE}	$V_C = 46-42V,$ $I_L = 50mA$		-0.03	0.01	V	1
V_{Bk}		V _C = 46-38V		-0.03	0.01	V	1
		V _C = 50-42V		-0.03	0.01	V	1
Thr	Thermal Response	100µS		-10	100	mV	1
		500µS		-10	70	mV	1
		2mS		-10	50	mV	1
		20mS		-10	10	mV	1

AC Parameter

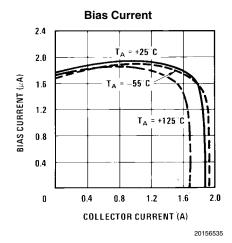
Symbol	Parameter	Conditions	Notes	Min	Max	Unit	Sub- groups
t _{ON}	Response Time	$V_I = 0-2V, R_L = 36\Omega,$ V+ = 36V			1.8	μS	9, 10, 11
t _{OFF}	Response Time	$V_I = 2-0V, R_L = 36\Omega,$ V+ = 36V			1.8	μS	9, 10, 11

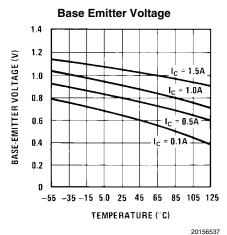
Note 1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not guarantee specific performance limits. For guaranteed specifications and test conditions, see the Electrical Characteristics. The guaranteed specifications apply only for the test conditions listed. Some performance characteristics may degrade when the device is not operated under the listed test

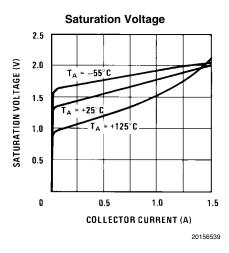
Note 2: The maximum power dissipation must be derated at elevated temperatures and is dictated by T_{Jmax} (maximum junction temperature), θ_{JA} (package junction to ambient thermal resistance), and T_A (ambient temperature). The maximum allowable power dissipation at any temperature is $P_{Dmax} = (T_{Jmax} - T_A)/\theta_{JA}$ or the number given in the Absolute Maximum Ratings, whichever is lower.

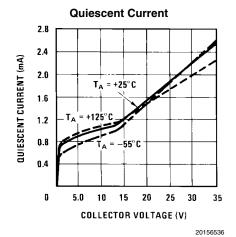
Note 3: Parameter tested go-no-go only.

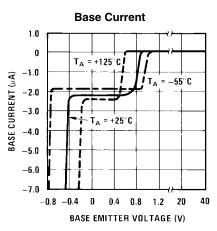
Typical Performance Characteristics

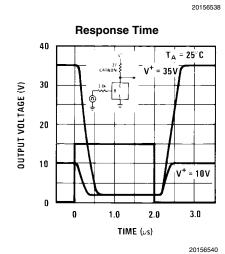


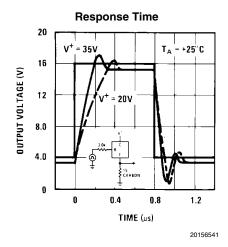


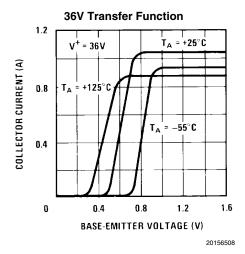


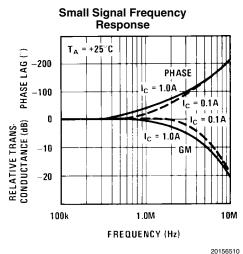


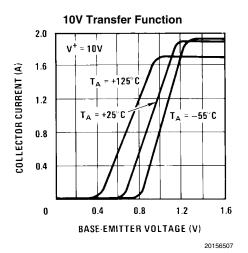


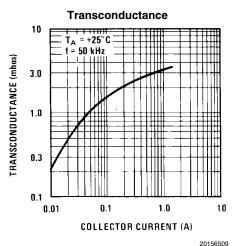










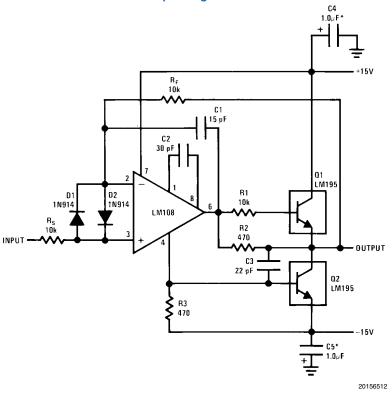


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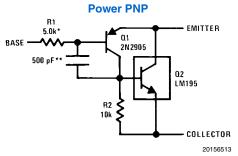
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Typical Applications

1.0 Amp Voltage Follower

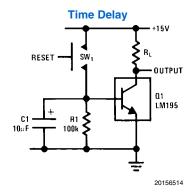


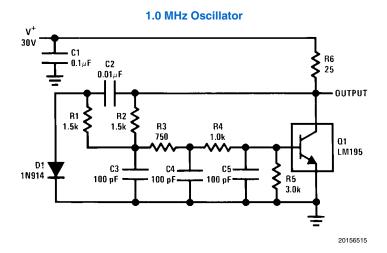
*Solid Tantalum



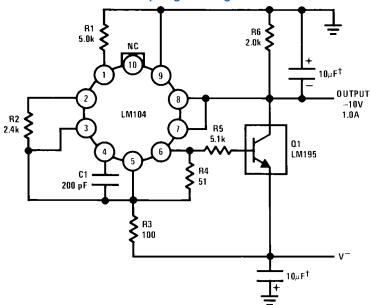
*Protects against excessive base drive

**Needed for stability





1.0 Amp Negative Regulator

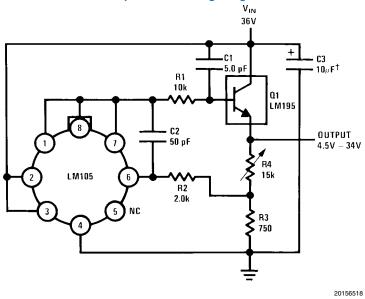


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20156517

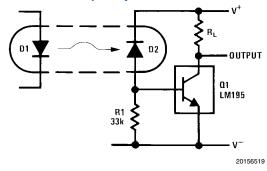
†Solid Tantalum

1.0 Amp Positive Voltage Regulator

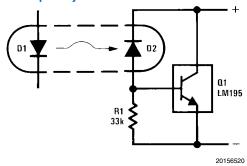


†Solid Tantalum

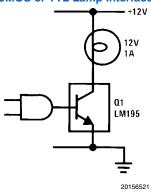
Fast Optically Isolated Switch



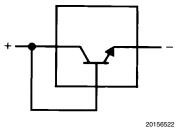
Optically Isolated Power Transistor

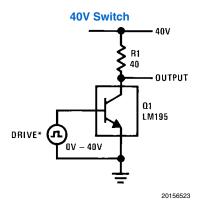


CMOS or TTL Lamp Interface



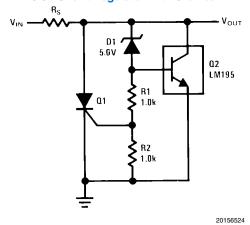
Two Terminal Current Limiter



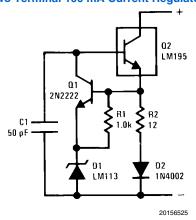


*Drive Voltage 0V to \geq 10V \leq 42V

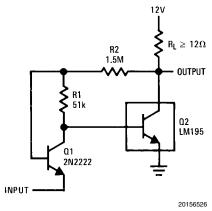
6.0V Shunt Regulator with Crowbar



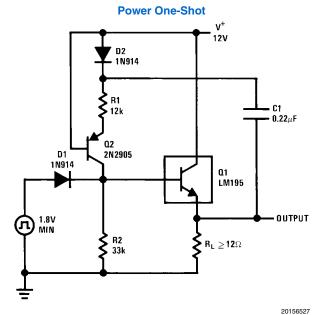
Two Terminal 100 mA Current Regulator



Low Level Power Switch



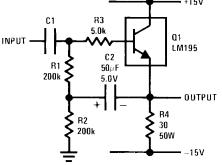
Turn ON = 350 mV Turn OFF = 200 mV



T = R1C R2 = 3R1 $R2 \le 82k$

INPUT R1 5.0k* Q1 LM195 QUTPUT 20156528

High Input Impedance AC Emitter Follower



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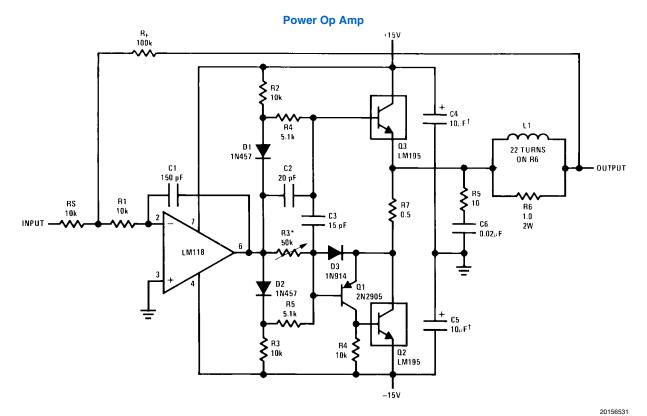
*Need for Stability

Fast Follower N1 5.0k 01 LM195

20156530

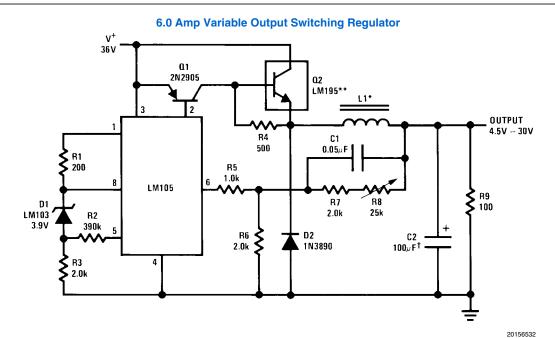
OUTPUT

*Prevents storage with fast fall time square wave drive



*Adjust for 50 mA quiescent current †Solid Tantalum

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^{*}Sixty turns wound on Arnold Type A-083081-2 core.

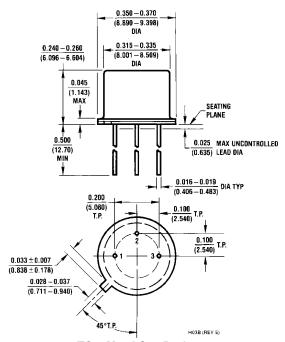
^{**}Four devices in parallel

[†]Solid tantalum

Revision History Section

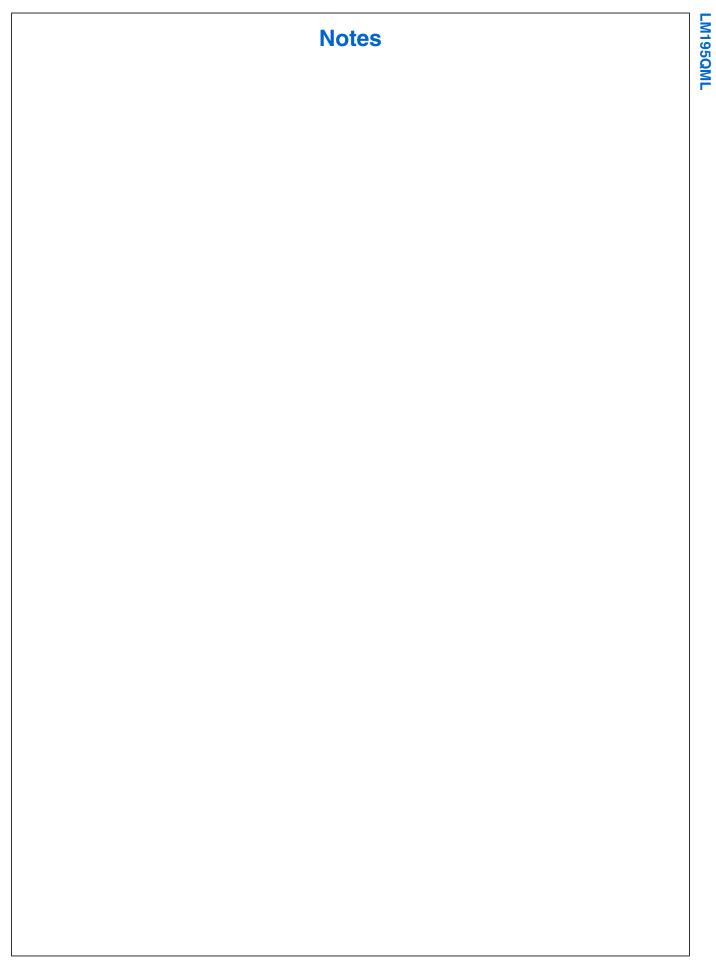
Released	Revision	Section	Changes
11/30/2010	А	New Release, Corporate format	1 MDS data sheets converted into one Corp. data sheet format. MNLM195-H Rev 0BL will be archived.

Physical Dimensions inches (millimeters) unless otherwise noted



TO-5 Metal Can Package NS Package Number H03B

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