

Integrated
Circuit
Systems, Inc.

ICS83023I

DUAL, 1-TO-1
DIFFERENTIAL-TO-LVCMOS TRANSLATOR/BUFFER

GENERAL DESCRIPTION

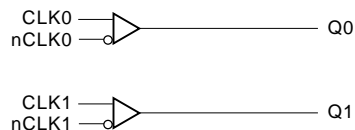


The ICS83023I is a dual, 1-to-1 Differential-to-LVCMOS Translator/Fanout Buffer and a member of the HiPerClockS™ family of High Performance Clock Solutions from ICS. The differential inputs can accept most differential signal types (LVDS, LVHSTL, LVPECL, SSTL, and HCSL) and translate into two single-ended LVCMOS outputs. The small 8-lead SOIC footprint makes this device ideal for use in applications with limited board space.

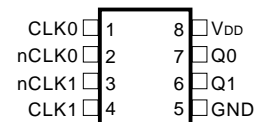
Features

- 2 LVCMOS / LVTTTL outputs
- 2 differential CLKx, nCLKx input pairs
- CLK, nCLK pairs can accept the following differential input levels: LVDS, LVPECL, LVHSTL, SSTL, HCSL
- Maximum output frequency: 350MHz (typical)
- Output skew: 60ps (maximum)
- Part-to-part skew: 500ps (maximum)
- Small 8 lead SOIC package saves board space
- 3.3V operating supply
- -40°C to 85°C ambient operating temperature
- Pin-to-pin compatible with MC100EPT23

BLOCK DIAGRAM



PIN ASSIGNMENT



ICS83023I
8-Lead SOIC

3.8mm x 4.8mm, x 1.47mm package body

M Package
Top View