

PQxxxEZ5MZ Series/ PQxxxEZ01Z Series

■ Features

- Low voltage operation (Minimum operating voltage:2.35V)
2.5V input → available 1.5 to 1.8V
- Low dissipation current
Dissipation current at no load:MAX.2mA
(Conventional model:MAX.10mA)
OFF-state dissipation current:MAX.5μA
- Fixed output and variable output are available
- SC-63 package

■ Applications

- Peripheral equipment of personal computers
- Power supplies for various electronic equipment such as DVD player or STB

■ Model Line-up

Output current (I _o)	Package type	Output voltage (V _o)		
		1.5V	1.8V	2.5V
0.5A	Taping	PQ015EZ5MZP	PQ018EZ5MZP	PQ025EZ5MZP
	Sleeve	PQ015EZ5MZZ	PQ018EZ5MZZ	PQ025EZ5MZZ
1A	Taping	PQ015EZ01ZP	PQ018EZ01ZP	PQ025EZ01ZP
	Sleeve	PQ015EZ01ZZ	PQ018EZ01ZZ	PQ025EZ01ZZ
		3V	3.3V	
0.5A	Taping	PQ030EZ5MZP	PQ033EZ5MZP	
	Sleeve	PQ030EZ5MZZ	PQ033EZ5MZZ	
1A	Taping	PQ030EZ01ZP	PQ033EZ01ZP	
	Sleeve	PQ030EZ01ZZ	PQ033EZ01ZZ	

■ Absolute Maximum Ratings (T_a=25°C)

Parameter	Symbol	Rating	Unit
Input voltage	V _{IN}	10	V
*1 Output control voltage	V _C	10	V
Output current	I _o	0.5	A
		1	
*2 Power dissipation	P _D	8	W
*3 Junction temperature	T _j	150	°C
Operating temperature	T _{opr}	-40 to +85	°C
Storage temperature	T _{stg}	-40 to +150	°C
Soldering temperature	T _{sol}	260 (10s)	°C

*1 All are open except GND and applicable terminals

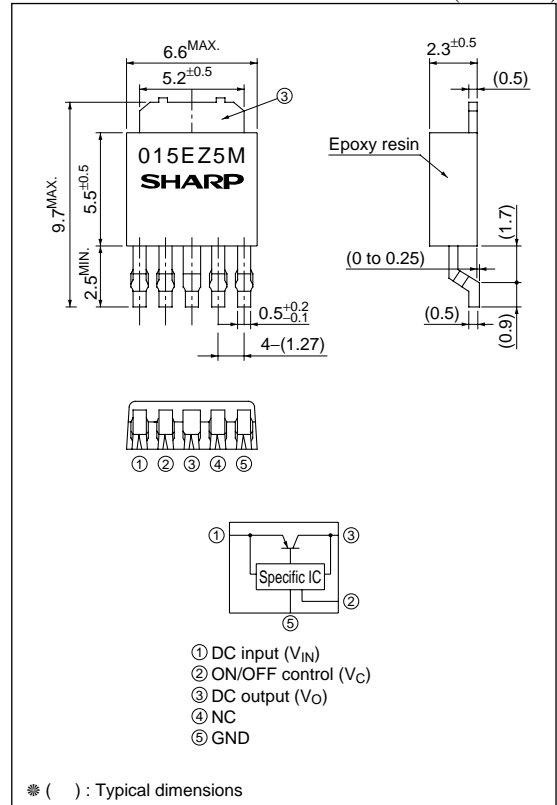
*2 P_D:With infinite heat sink

*3 Overheat protection may operate at the condition T_j=125°C to 150°C

SC-63 Package, Low Voltage Operation, Low Power-Loss Voltage Regulator

■ Outline Dimensions

(Unit : mm)



■ Electrical Characteristics

(Unless otherwise specified, condition shall be $V_{IN}=V_O(\text{TYP.})+1\text{V}$, $I_O=0.3\text{A}$, $V_C=2.7\text{V}$, $T_a=25^\circ\text{C}$ (PQxxxEZ5MZ))

(Unless otherwise specified, condition shall be $V_{IN}=V_O(\text{TYP.})+1\text{V}$, $I_O=0.5\text{A}$, $V_C=2.7\text{V}$, $T_a=25^\circ\text{C}$ (PQxxxEZ01Z))

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit		
Input voltage range	V_{IN}	–	Refer to below table			V		
Output voltage	V_O	–	Refer to below table			V		
Load regulation	PQxxxEZ5MZ	R_{regL}	I _O =5mA to 0.5A		–	0.2	2	%
	PQxxxEZ01Z		I _O =5mA to 1A					
Line regulation	R_{regI}	$V_{IN}=V_O(\text{TYP.})+1\text{V}$ to $V_O(\text{TYP.})+6\text{V}$, I _O =5mA	–	0.1	1	–	–	–
Output voltage temperature coefficient	$T_C V_O$	T _J =0 to 125°C, I _O =5mA	–	±0.01	–	–	–	%/°C
Ripple Rejection	RR	Refer to Fig.2	45	60	–	–	–	dB
*4 Dropout voltage	PQxxxEZ5MZ	V_{I-O}	*5 I _O =0.3A		–	0.2	0.5	V
	PQxxxEZ01Z		*5 I _O =0.5A					
*6 Output on control voltage	$V_{C(\text{ON})}$	–	2	–	–	–	–	V
Output on control current	$I_{C(\text{ON})}$	–	–	–	200	–	–	μA
Output off control voltage	$V_{C(\text{OFF})}$	–	–	–	0.8	–	–	V
Output off control current	$I_{C(\text{OFF})}$	V _C =0.4V	–	–	2	–	–	μA
Quiescent current	I _q	I _O =0A	–	1	2	–	–	mA
Output OFF-state consumption current	I _{qs}	I _O =0A, V _C =0.4V	–	–	5	–	–	μA

*4 Applied PQ030EZ5MZ, PQ033EZ5MZ

*5 Input voltage when output voltage falls 0.95V_O by input voltage falling down

*6 In case of opening control terminal ②, output voltage turns off

■ Input Voltage Range

(Unless otherwise specified, condition shall be I_O=0.3A, V_C=2.7V, T_a=25°C (PQxxxEZ5MZ))

(Unless otherwise specified, condition shall be I_O=0.5A, V_C=2.7V, T_a=25°C (PQxxxEZ01Z))

Model No.	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
PQ015EZ5MZ/PQ015EZ01Z	V_{IN}	–	2.35	–	10	V
PQ018EZ5MZ/PQ018EZ01Z	V_{IN}	–	2.35	–	10	V
PQ025EZ5MZ/PQ025EZ01Z	V_{IN}	–	V _O +5	–	10	V
PQ030EZ5MZ/PQ030EZ01Z	V_{IN}	–	V _O +5	–	10	V
PQ033EZ5MZ/PQ033EZ01Z	V_{IN}	–	V _O +5	–	10	V

■ Output Voltage Range

(Unless otherwise specified, condition shall be $V_{IN}=V_O(\text{TYP.})+1\text{V}$, I_O=0.3A, V_C=2.7V, T_a=25°C (PQxxxEZ5MZ))

(Unless otherwise specified, condition shall be $V_{IN}=V_O(\text{TYP.})+1\text{V}$, I_O=0.5A, V_C=2.7V, T_a=25°C (PQxxxEZ01Z))

Model No.	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
PQ015EZ5MZ/PQ015EZ01Z	V_O	–	1.45	1.5	1.55	V
PQ018EZ5MZ/PQ018EZ01Z	V_O	–	1.75	1.8	1.85	V
PQ025EZ5MZ/PQ025EZ01Z	V_O	–	2.438	2.5	2.562	V
PQ030EZ5MZ/PQ030EZ01Z	V_O	–	2.925	3	3.075	V
PQ033EZ5MZ/PQ033EZ01Z	V_O	–	3.218	3.3	3.382	V

Fig.1 Standard Test Circuit

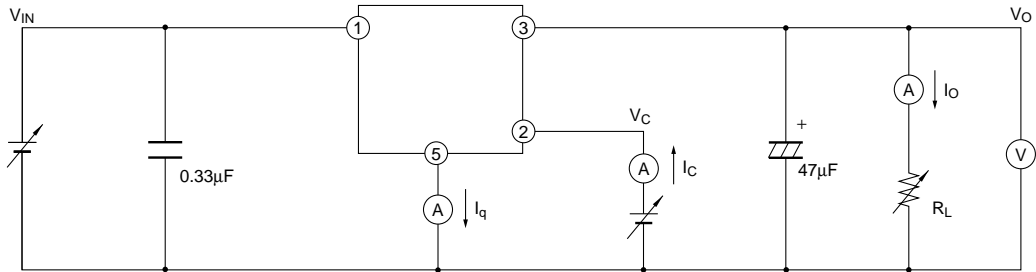
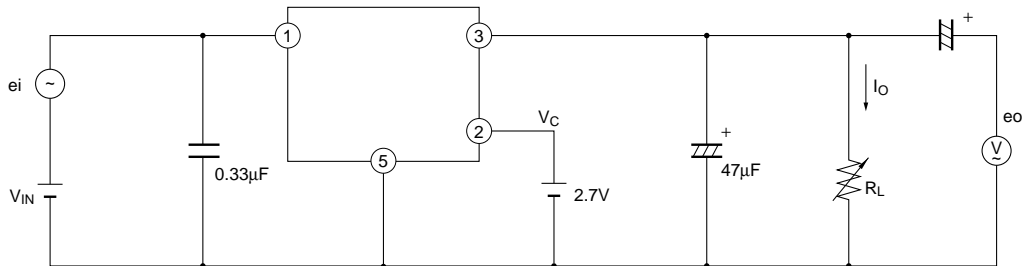
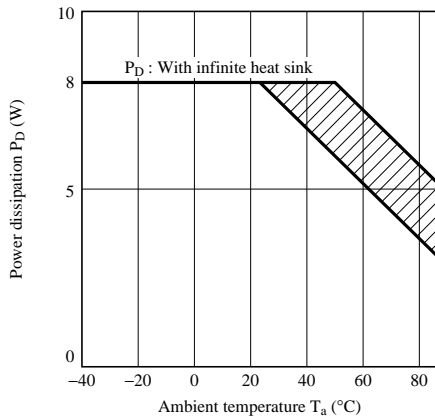


Fig.2 Test Circuit for Ripple Rejection



$f=120\text{Hz}$ (sine wave)
 $e_i(\text{rms})=0.5\text{V}$
 $V_{IN}=V_o(\text{Typ})+2\text{V}$
 $I_o=0.3\text{A}$
 $RR=20\log(e_i(\text{rms})/e_o(\text{rms}))$

Fig.3 Power Dissipation vs. Ambient Temperature



Note) Oblique line portion: Overheat protection may operate in this area

Fig.4 Overcurrent Protection Characteristics (PQ015EZ5MZ)

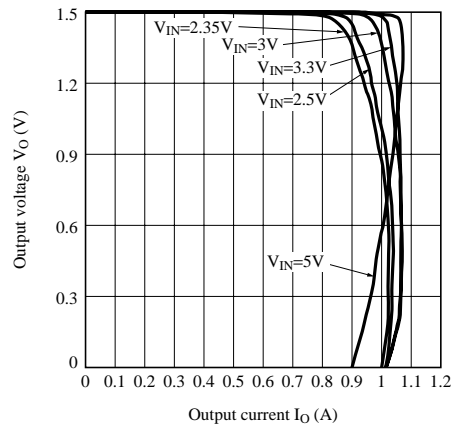


Fig.5 Overcurrent Protection Characteristics (PQ018EZ5MZ)

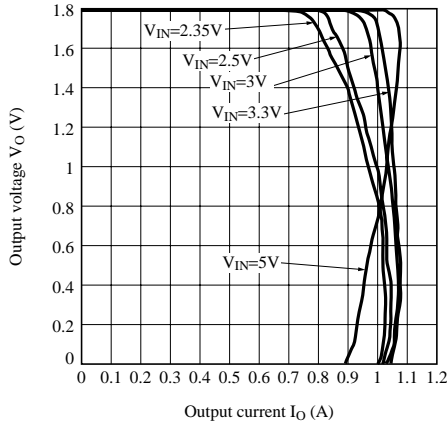


Fig.6 Overcurrent Protection Characteristics (PQ025EZ5MZ)

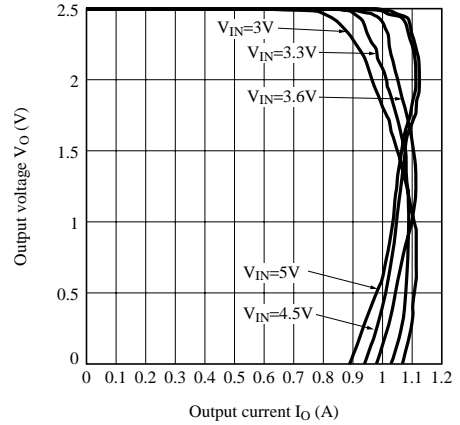


Fig.7 Overcurrent Protection Characteristics (PQ030EZ5MZ)

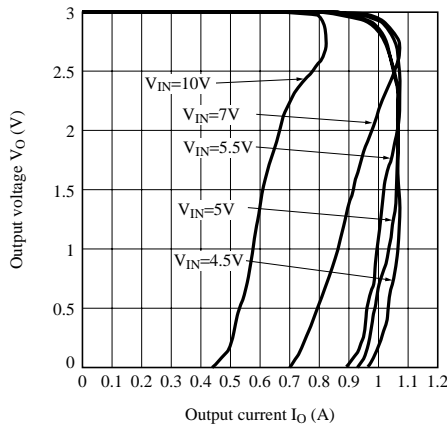


Fig.8 Overcurrent Protection Characteristics (PQ033EZ5MZ)

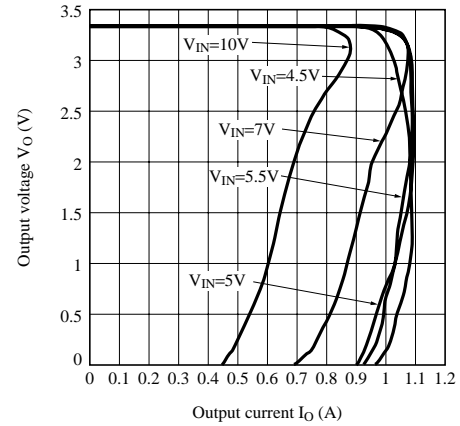


Fig.9 Overcurrent Protection Characteristics (PQ015EZ01Z)

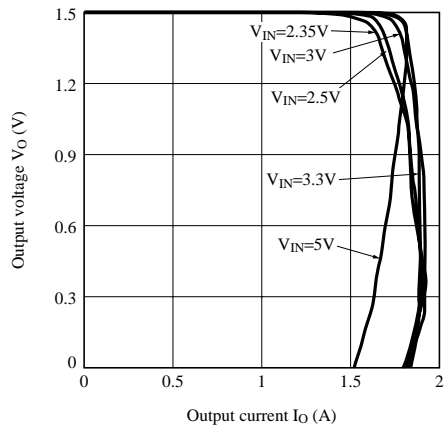


Fig.10 Overcurrent Protection Characteristics (PQ018EZ01Z)

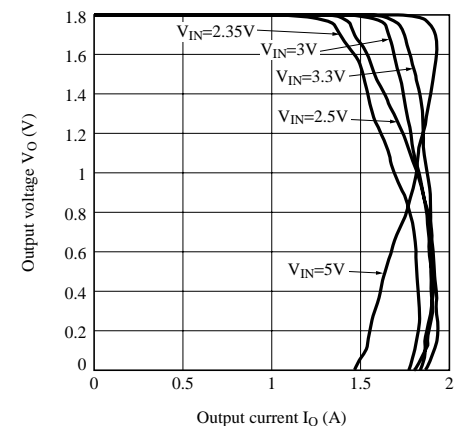


Fig.11 Overcurrent Protection Characteristics (PQ025EZ01Z)

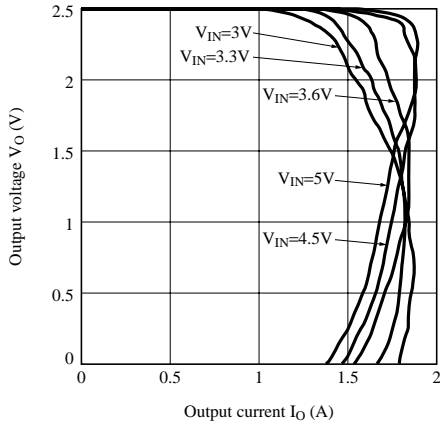


Fig.12 Overcurrent Protection Characteristics (PQ030EZ01Z)

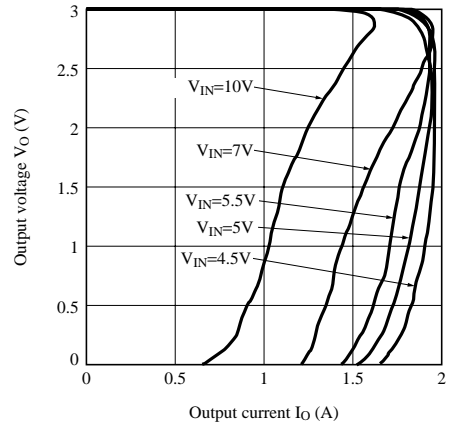


Fig.13 Overcurrent Protection Characteristics (PQ033EZ01Z)

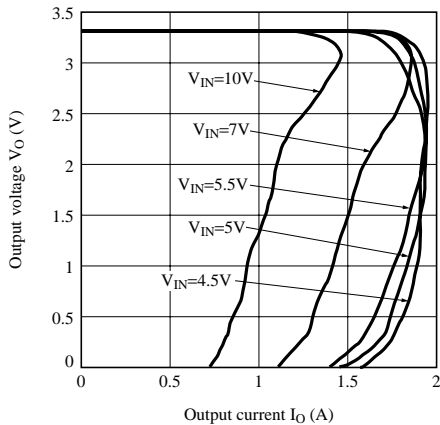


Fig.14 Output Voltage vs. Ambient Temperature (PQ015EZ5MZ/PQ015EZ01Z)

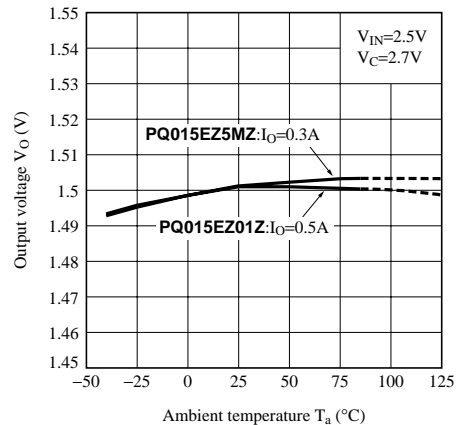


Fig.15 Output Voltage vs. Ambient Temperature (PQ018EZ5MZ/PQ018EZ01Z)

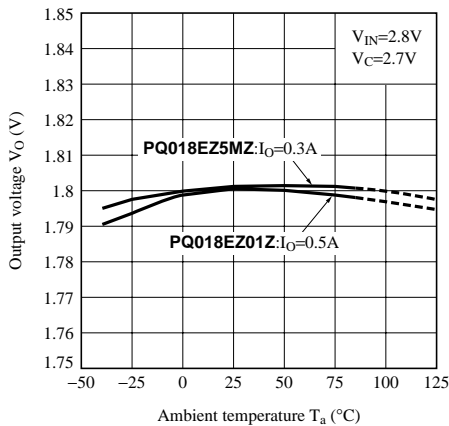


Fig.16 Output Voltage vs. Ambient Temperature (PQ025EZ5MZ/PQ025EZ01Z)

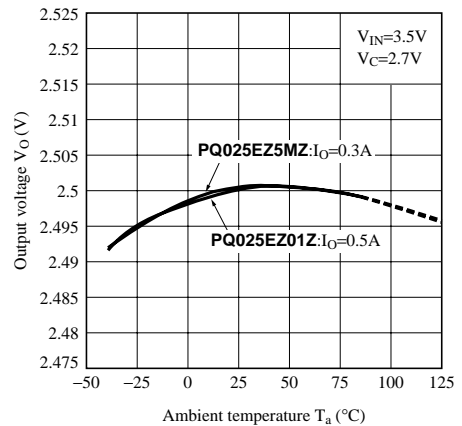


Fig.17 Output Voltage vs. Ambient Temperature (PQ030EZ5MZ/PQ030EZ01Z)

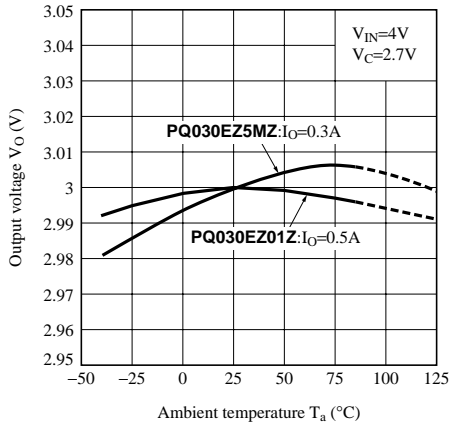


Fig.18 Output Voltage vs. Ambient Temperature (PQ033EZ5MZ/PQ033EZ01Z)

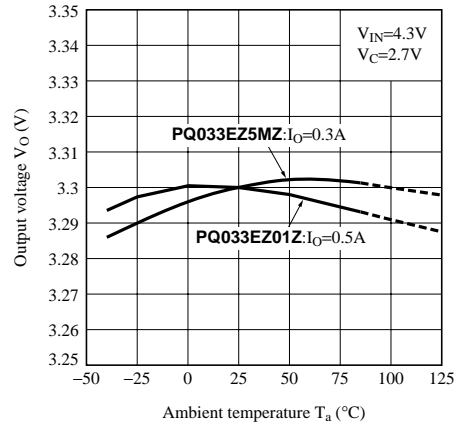


Fig.19 Output Voltage vs. Input Voltage (PQ015EZ5MZ)

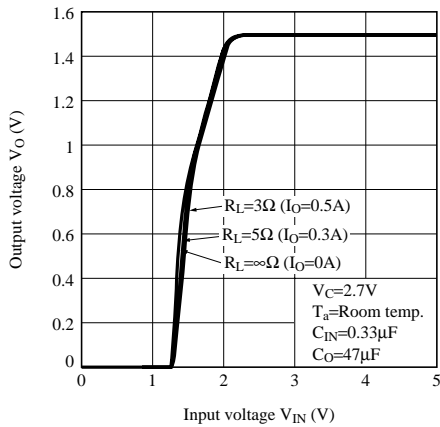


Fig.20 Output Voltage vs. Input Voltage (PQ018EZ5MZ)

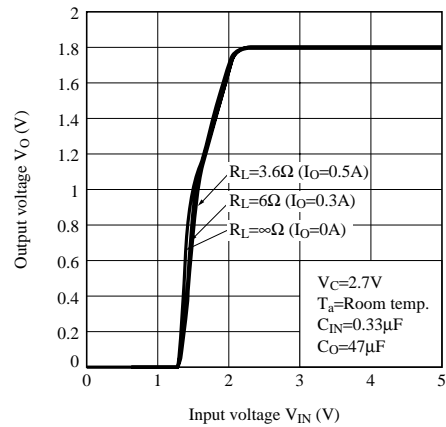


Fig.21 Output Voltage vs. Input Voltage (PQ025EZ5MZ)

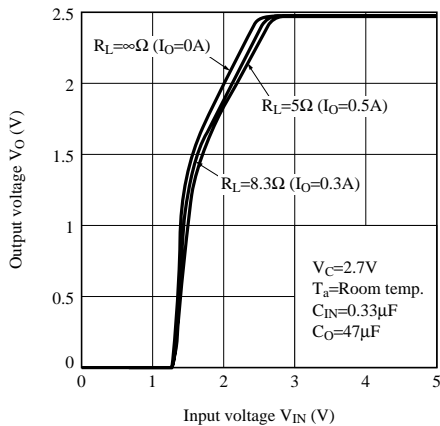


Fig.22 Output Voltage vs. Input Voltage (PQ030EZ5MZ)

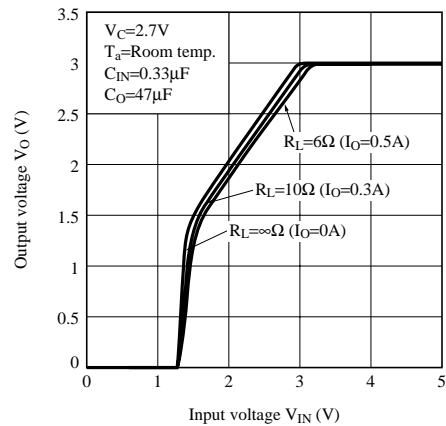


Fig.23 Output Voltage vs. Input Voltage (PQ033EZ5MZ)

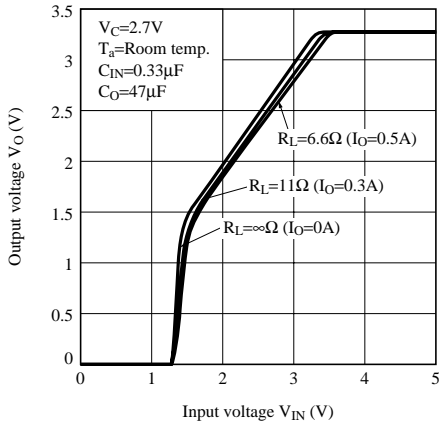


Fig.24 Output Voltage vs. Input Voltage (PQ015EZ01Z)

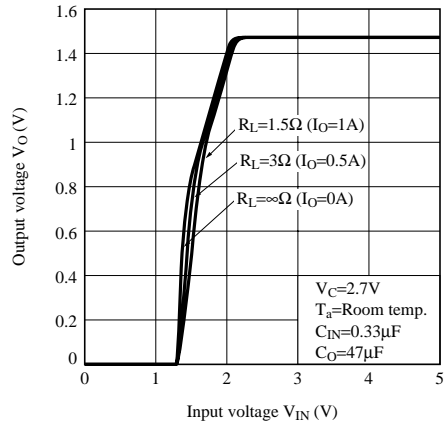


Fig.25 Output Voltage vs. Input Voltage (PQ018EZ01Z)

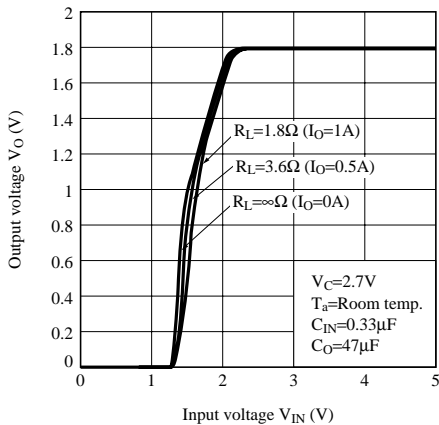


Fig.26 Output Voltage vs. Input Voltage (PQ025EZ01Z)

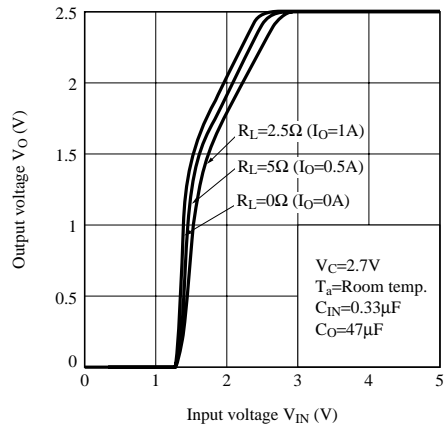


Fig.27 Output Voltage vs. Input Voltage (PQ030EZ01Z)

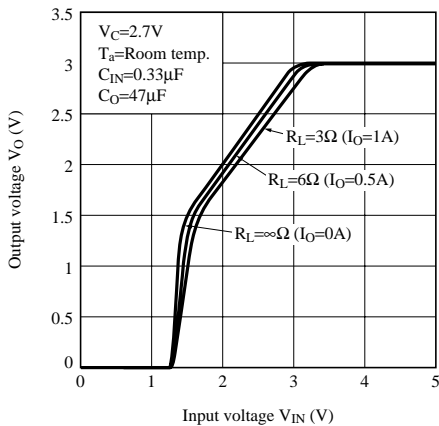


Fig.28 Output Voltage vs. Input Voltage (PQ033EZ01Z)

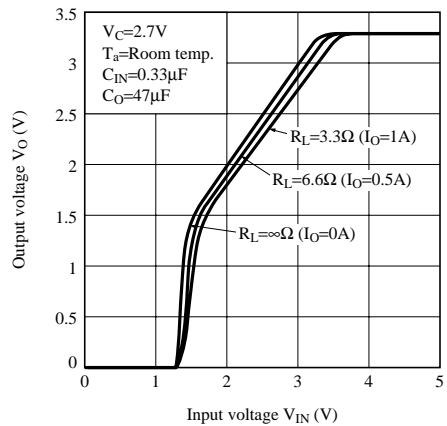


Fig.29 Circuit Operating Current vs. Input Voltage (PQ015EZ5MZ)

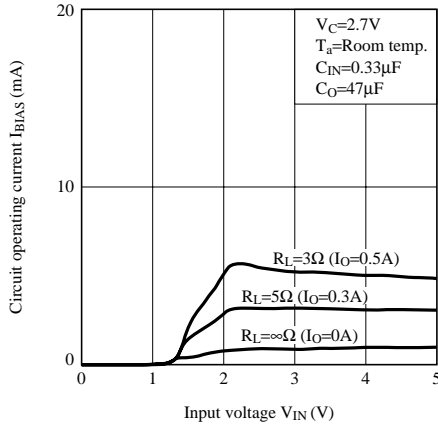


Fig.30 Circuit Operating Current vs. Input Voltage (PQ018EZ5MZ)

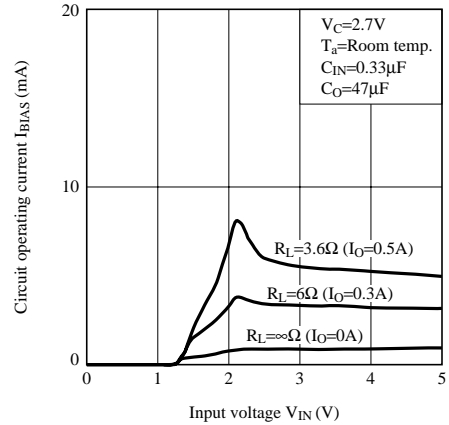


Fig.31 Circuit Operating Current vs. Input Voltage (PQ025EZ5MZ)

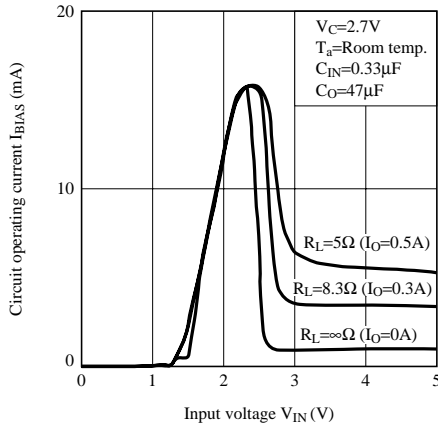


Fig.32 Circuit Operating Current vs. Input Voltage (PQ030EZ5MZ)

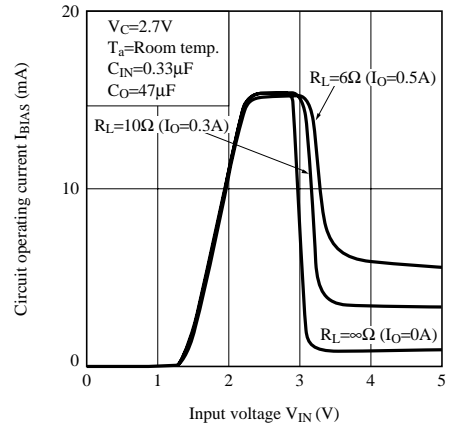


Fig.33 Circuit Operating Current vs. Input Voltage (PQ033EZ5MZ)

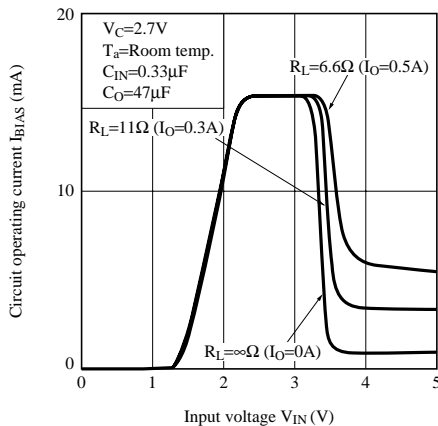


Fig.34 Circuit Operating Current vs. Input Voltage (PQ015EZ01Z)

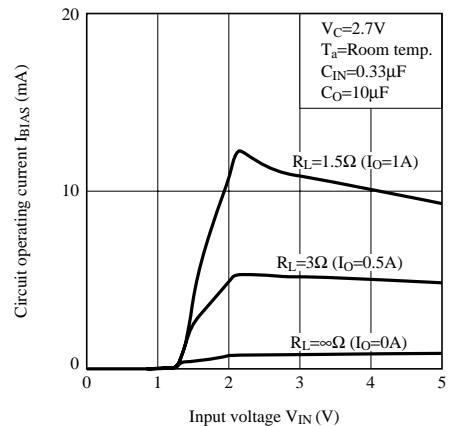


Fig.35 Circuit Operating Current vs. Input Voltage (PQ018EZ01Z)

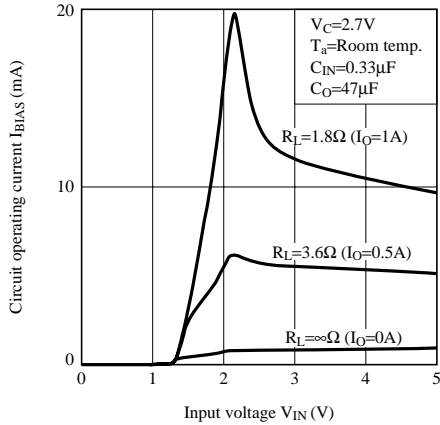


Fig.36 Circuit Operating Current vs. Input Voltage (PQ025EZ01Z)

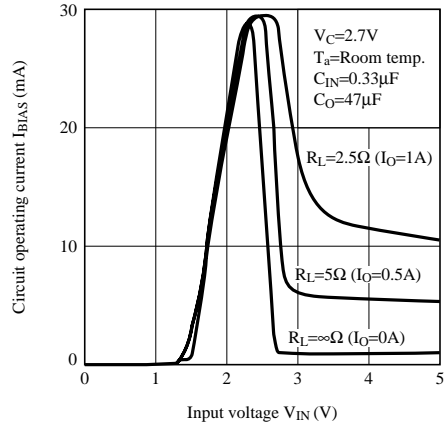


Fig.37 Circuit Operating Current vs. Input Voltage (PQ030EZ01Z)

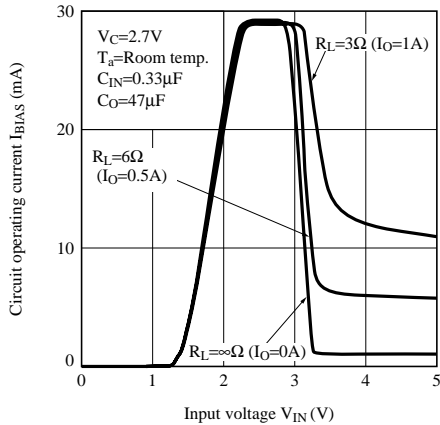


Fig.38 Circuit Operating Current vs. Input Voltage (PQ033EZ01Z)

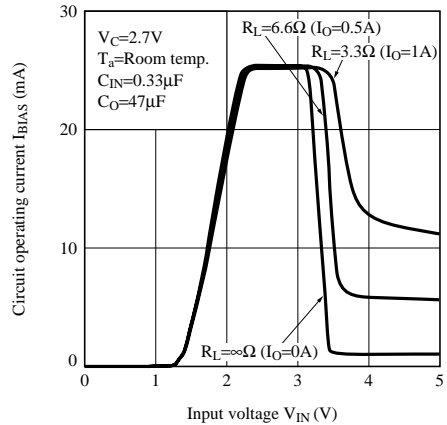


Fig.39 Quiescent Current vs. Ambient Temperature (PQxxxEZ5MZ/PQxxxEZ01Z)

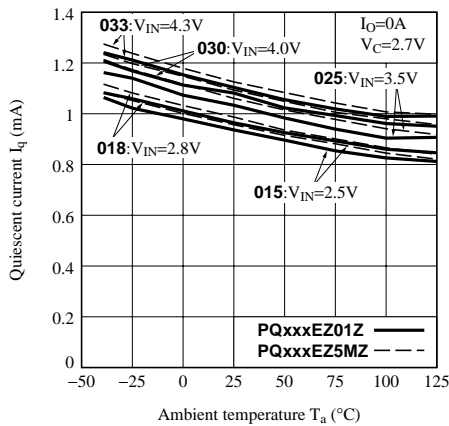


Fig.40 Dropout Voltage vs. Ambient Temperature (PQxxxEZ5MZ/PQxxxEZ01Z)

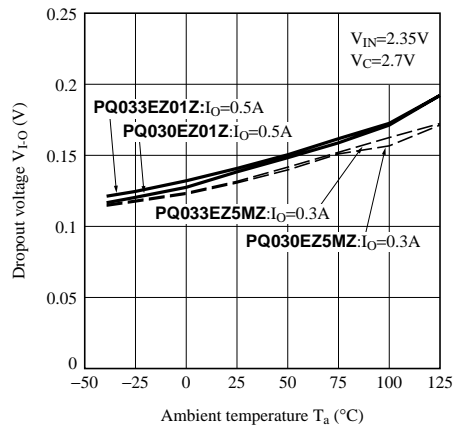


Fig.41 Ripple Rejection vs. Input Ripple Frequency (PQxxxEZ5MZ/PQxxxEZ01Z)

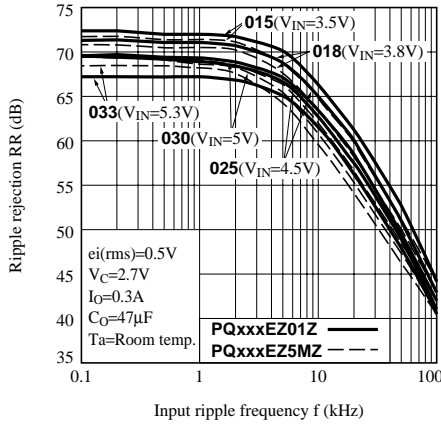


Fig.42 Ripple Rejection vs. Output Current (PQxxxEZ5MZ/PQxxxEZ01Z)

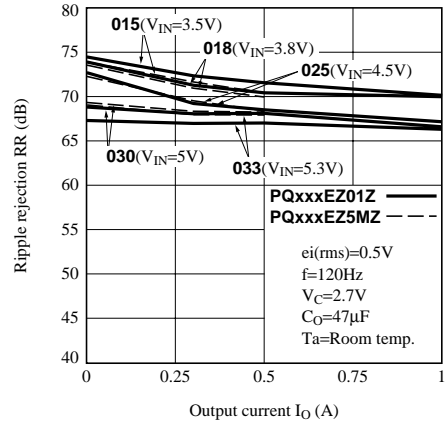


Fig.43 Example of Application

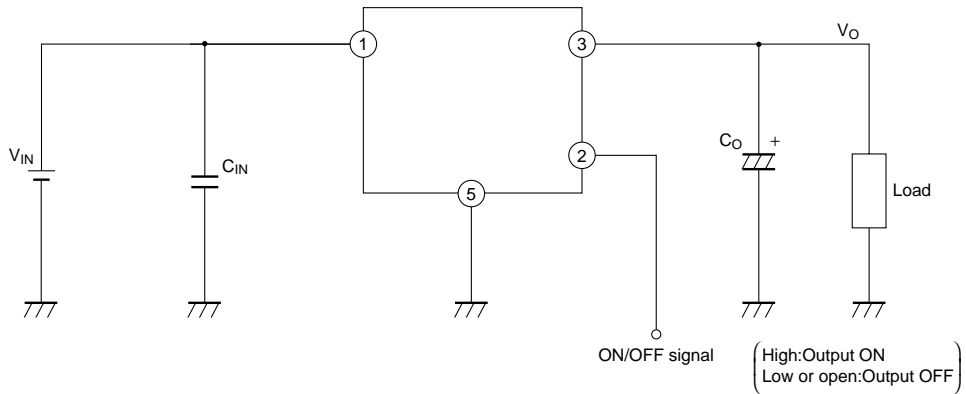
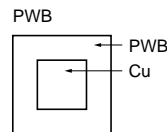
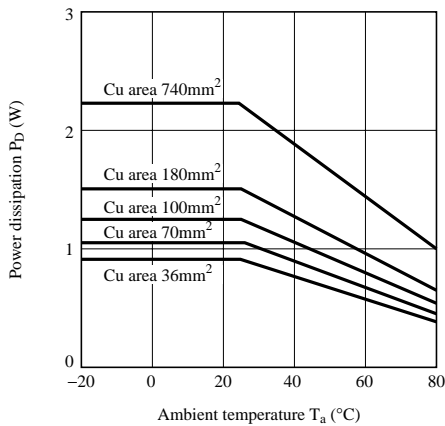
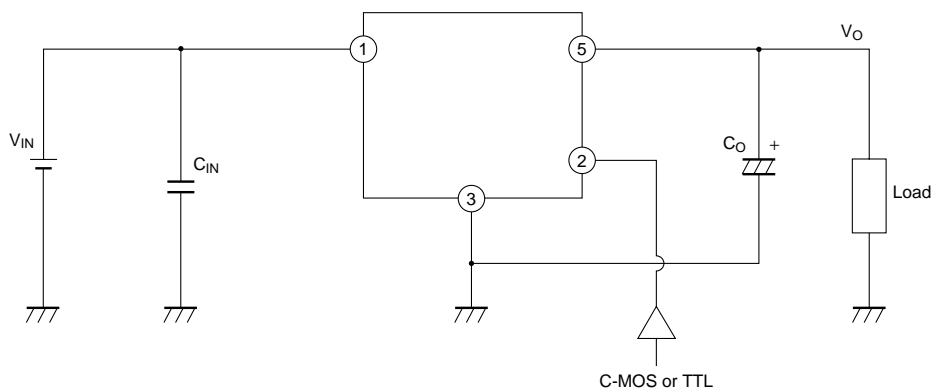


Fig.44 Power Dissipation vs. Ambient Temperature (Typical Value)



Material : Glass-cloth epoxy resin
 Size : 50×50×1.6mm
 Cu thickness : 35μm

■ Precautions for Use



1. External connection

- (1) The connecting wiring of C_O , C_{IN} and each terminal, fin portion must be as short as possible. It may oscillate by type, value and wiring condition of capacitor. Confirm the output waveform in actual using condition beforehand.
- (2) ON/OFF control terminal ② is compatible with LS-TTL. It enables to be directly driven by TTL or C-MOS standard logic (RCA4000 series).
- (3) If voltage is applied under the conditions that device pin is connected divergently or reversely, the deterioration of characteristics or damage may occur. Never allow improper mounting.

2. Thermal protection design

Maximum power dissipation of devices is obtained by the following equation.

$$P_D = I_O \times (V_{IN} - V_O) + V_{IN} \times I_q$$

When ambient temperature T_a and power dissipation P_D (MAX.) during operation are determined, operate element within the safety operation area specified by the derating curve. Insufficient radiation gives an unfavorable influence to the normal operation and reliability of the device.

In the external area of the safety operation area shown by the derating curve, the overheat protection circuit may operate to shut-down output. However please avoid keeping such condition for a long time.

3. ESD (Electrostatic Sensitivity Discharge)

Be careful not to apply electrostatic discharge to the device since this device employs a bipolar IC and may be damaged by electrostatic discharge. Followings are some methods against excessive voltage caused by electro static discharge.

- (1) Human body must be grounded to discharge the electro charge which is charged in the body or cloth.
- (2) Anything that is in contact with the device such as workbench, inserter, or measuring instrument must be grounded.
- (3) Use a soldering dip basin with a minimum leak current (isolation resistance $10M\Omega$ or more) from the AC power supply line.

Also the soldering dip basin must be grounded.

NOTICE

- The circuit application examples in this publication are provided to explain representative applications of SHARP devices and are not intended to guarantee any circuit design or license any intellectual property rights. SHARP takes no responsibility for any problems related to any intellectual property right of a third party resulting from the use of SHARP's devices.
- Contact SHARP in order to obtain the latest device specification sheets before using any SHARP device. SHARP reserves the right to make changes in the specifications, characteristics, data, materials, structure, and other contents described herein at any time without notice in order to improve design or reliability. Manufacturing locations are also subject to change without notice.
- Observe the following points when using any devices in this publication. SHARP takes no responsibility for damage caused by improper use of the devices which does not meet the conditions and absolute maximum ratings to be used specified in the relevant specification sheet nor meet the following conditions:
 - (i) The devices in this publication are designed for use in general electronic equipment designs such as:
 - Personal computers
 - Office automation equipment
 - Telecommunication equipment [terminal]
 - Test and measurement equipment
 - Industrial control
 - Audio visual equipment
 - Consumer electronics
 - (ii) Measures such as fail-safe function and redundant design should be taken to ensure reliability and safety when SHARP devices are used for or in connection with equipment that requires higher reliability such as:
 - Transportation control and safety equipment (i.e., aircraft, trains, automobiles, etc.)
 - Traffic signals
 - Gas leakage sensor breakers
 - Alarm equipment
 - Various safety devices, etc.
 - (iii) SHARP devices shall not be used for or in connection with equipment that requires an extremely high level of reliability and safety such as:
 - Space applications
 - Telecommunication equipment [trunk lines]
 - Nuclear power control equipment
 - Medical and other life support equipment (e.g., scuba).
- Contact a SHARP representative in advance when intending to use SHARP devices for any "specific" applications other than those recommended by SHARP or when it is unclear which category mentioned above controls the intended use.
- If the SHARP devices listed in this publication fall within the scope of strategic products described in the Foreign Exchange and Foreign Trade Control Law of Japan, it is necessary to obtain approval to export such SHARP devices.
- This publication is the proprietary product of SHARP and is copyrighted, with all rights reserved. Under the copyright laws, no part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, for any purpose, in whole or in part, without the express written permission of SHARP. Express written permission is also required before any use of this publication may be made by a third party.
- Contact and consult with a SHARP representative if there are any questions about the contents of this publication.