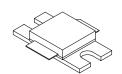
The RF MOSFET Line **RF Power Field Effect Transistors**N-Channel Enhancement-Mode Lateral MOSFETs

Designed for PCN and PCS base station applications from frequencies up to 2.0 to 2.2 GHz. Suitable for FM, TDMA, CDMA and multicarrier amplifier applications. To be used in class AB for PCN–PCS/cellular radio and WLL applications.

- Wideband CDMA Performance: -45 dB ACPR @ 4.096 MHz, 28 Volts
 Output Power 3.5 Watts
 Power Gain 14 dB
 Efficiency 15%
- High Gain, High Efficiency and High Linearity
- Integrated ESD Protection
- Ease of Design for Gain and Insertion Phase Flatness
- Capable of Handling 10:1 VSWR, @ 28 Vdc, 2.11 GHz, 30 Watts CW Output Power
- Excellent Thermal Stability
- Characterized with Series Equivalent Large-Signal Impedance Parameters

MRF21030 MRF21030S

2.2 GHz, 30 W, 28 V LATERAL N-CHANNEL RF POWER MOSFETs



CASE 465E-02, STYLE 1 (MRF21030)



CASE 465F-02, STYLE 1 (MRF21030S)

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Drain-Source Voltage	VDSS	65	Vdc
Gate–Source Voltage	V _{GS}	+15, -0.5	Vdc
Total Device Dissipation @ T _C = 25°C Derate above 25°C	PD	83.3 0.48	Watts W/°C
Storage Temperature Range	T _{stg}	-65 to +200	°C
Operating Junction Temperature	TJ	200	°C

ESD PROTECTION CHARACTERISTICS

Test Conditions	Class	
Human Body Model	2 (Typical)	
Machine Model	M3 (Typical)	

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	$R_{\theta JC}$	2.1	°C/W

 ${\tt NOTE-\underline{CAUTION}-MOS\ devices\ are\ susceptible\ to\ damage\ from\ electrostatic\ charge.\ Reasonable\ precautions\ in\ handling\ and\ packaging\ MOS\ devices\ should\ be\ observed.}$

REV 3



ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Drain–Source Breakdown Voltage (VGS = 0 Vdc, I _D = 20 μA)	V(BR)DSS	65	_	_	Vdc
Zero Gate Voltage Drain Current (VDS = 28 Vdc, VGS = 0)	IDSS	_	_	1	μAdc
Gate-Source Leakage Current (VGS = 5 Vdc, VDS = 0)	I _{GSS}	_	_	1	μAdc
ON CHARACTERISTICS					
Gate Threshold Voltage (V _{DS} = 10 Vdc, I _D = 100 μAdc)	VGS(th)	2	3	4	Vdc
Gate Quiescent Voltage (V _{DS} = 28 Vdc, I _D = 250 mA)	V _{GS(Q)}	2	3.3	4.5	Vdc
Drain-Source On-Voltage (VGS = 10 Vdc, I _D = 1 Adc)	V _{DS(on)}	_	0.29	0.4	Vdc
Forward Transconductance (V _{DS} = 10 Vdc, I _D = 1 Adc)	9fs	_	2	_	S
DYNAMIC CHARACTERISTICS			•		•
Input Capacitance (Including Input Matching Capacitor in Package) (1) $(V_{DS} = 28 \text{ Vdc}, V_{GS} = 0, f = 1 \text{ MHz})$	C _{iss}	_	98.5	_	pF
Output Capacitance (1) (V _{DS} = 28 Vdc, V _{GS} = 0, f = 1 MHz)	C _{oss}	_	37	_	pF
Reverse Transfer Capacitance (V _{DS} = 28 Vdc, V _{GS} = 0, f = 1 MHz)	C _{rss}	_	1.3	_	pF
FUNCTIONAL TESTS (In Motorola Test Fixture)					
Two–Tone Common–Source Amplifier Power Gain $(V_{DD} = 28 \text{ Vdc}, P_{out} = 30 \text{ W PEP}, I_{DQ} = 250 \text{ mA}, f1 = 2140.0 \text{ MHz}, f2 = 2140.1 \text{ MHz})$	G _{ps}	_	13	_	dB
Two-Tone Drain Efficiency $(V_{DD} = 28 \text{ Vdc}, P_{Out} = 30 \text{ W PEP}, I_{DQ} = 250 \text{ mA}, f1 = 2140.0 \text{ MHz}, f2 = 2140.1 \text{ MHz})$	η	_	33	_	%
3rd Order Intermodulation Distortion $(V_{DD} = 28 \text{ Vdc}, P_{Out} = 30 \text{ W PEP}, I_{DQ} = 250 \text{ mA}, f1 = 2140.0 \text{ MHz}, f2 = 2140.1 \text{ MHz})$	IMD	_	-30	_	dBc
Input Return Loss (V _{DD} = 28 Vdc, P _{Out} = 30 W PEP, I _{DQ} = 250 mA, f1 = 2140.0 MHz, f2 = 2140.1 MHz)	IRL	_	-13		dB
Two–Tone Common–Source Amplifier Power Gain (V_{DD} = 28 Vdc, P_{Out} = 30 W PEP, I_{DQ} = 250 mA, f1 = 2110.0 MHz, f2 = 2110.1 MHz and f1 = 2170.0 MHz, f2 = 2170.1 MHz)	G _{ps}	12	13	_	dB
Two–Tone Drain Efficiency $(V_{DD} = 28 \text{ Vdc}, P_{Out} = 30 \text{ W PEP}, I_{DQ} = 250 \text{ mA}, f1 = 2110.0 \text{ MHz}, f2 = 2110.1 \text{ MHz} and f1 = 2170.0 \text{ MHz}, f2 = 2170.1 \text{ MHz})$	η	31	33	_	%
3rd Order Intermodulation Distortion (V_{DD} = 28 Vdc, P_{Out} = 30 W PEP, I_{DQ} = 250 mA, f1 = 2110.0 MHz, f2 = 2110.1 MHz and f1 = 2170.0 MHz, f2 = 2170.1 MHz)	IMD	_	-30	-27.5	dBc
Input Return Loss (V _{DD} = 28 Vdc, P _{Out} = 30 W PEP, I _{DQ} = 250 mA, f1 = 2110.0 MHz, f2 = 2110.1 MHz and f1 = 2170.0 MHz, f2 = 2170.1 MHz)	IRL	_	-13	– 9	dB
Output Mismatch Stress (V _{DD} = 28 Vdc, P _{Out} = 30 W CW, I _{DQ} = 250 mA, f = 2110 MHz, VSWR = 10:1, All Phase Angles at Frequency of Tests)	Ψ	No	Degradation Before and	In Output Po d After Test	wer

⁽¹⁾ Part is internally matched both on input and output.

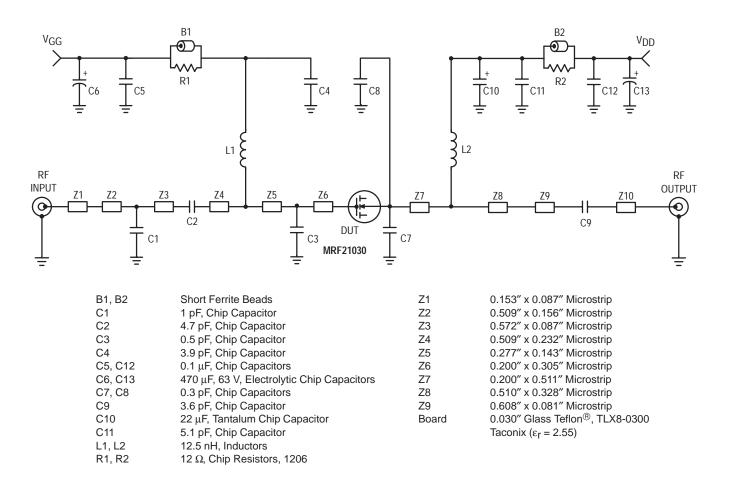


Figure 1. MRF21030 Schematic

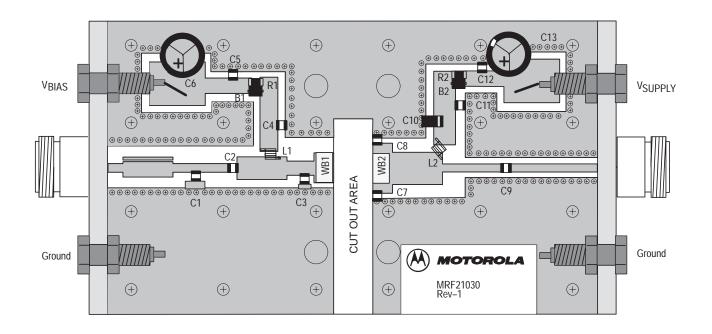


Figure 2. MRF21030 Populated PC Board Layout Diagram

RF DEVICE DATA MRF21030 MRF21030S

TYPICAL CHARACTERISTICS

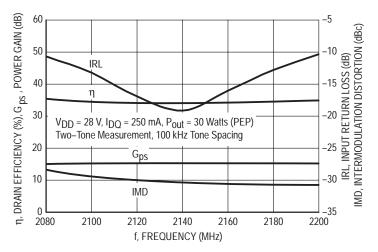


Figure 3. Class AB Broadband Circuit Performance

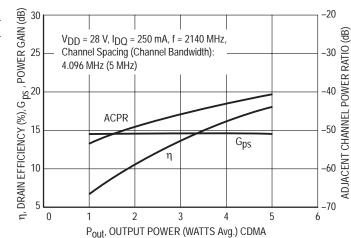


Figure 4. CDMA ACPR, Power Gain and Drain Efficiency versus Output Power

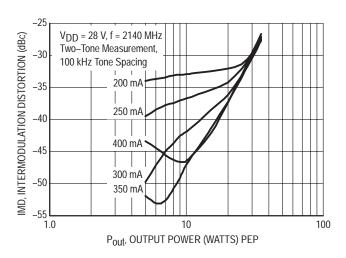


Figure 5. Intermodulation Distortion versus Output Power

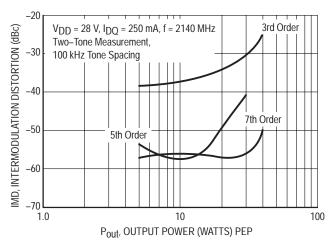


Figure 6. Intermodulation Distortion Products versus Output Power

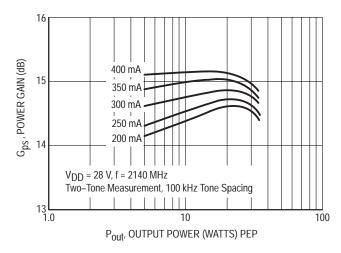


Figure 7. Power Gain versus Output Power

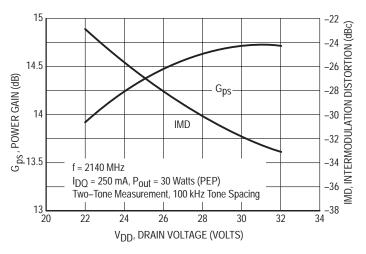
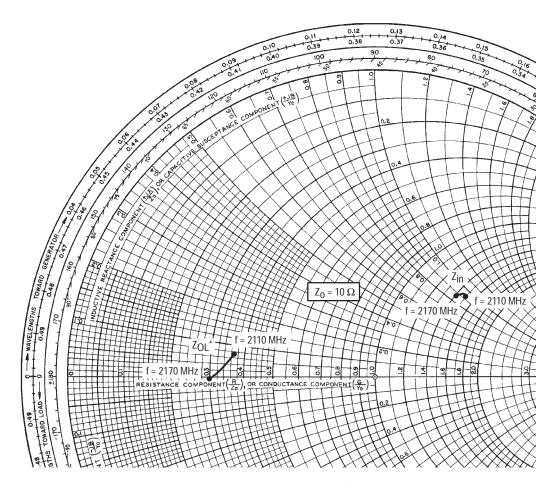


Figure 8. Power Gain and Intermodulation Distortion versus Supply Voltage

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 $V_{DD} = 28 \text{ V}, I_{DQ} = 250 \text{ mA}, P_{out} = 30 \text{ Watts (PEP)}$

1BB = 1,1BQ = 11111,1 Out = 111111 (1 = 1)				
f MHz	Z _{in} Ω	Z_{OL} * Ω		
2110	15.3 + j9.4	3.7 + j0.78		
2140	14.6 + j9.4	3.4 + j0.37		
2170	14.3 + j8.8	3.0 – j0.13		

 Z_{in} = Complex conjugate of source impedance.

Z_{OL}* = Complex conjugate of the optimum load impedance at a given output power, voltage, IMD, bias current and frequency.

Note: Z_{OL}* was chosen based on tradeoffs between gain, output power, drain efficiency and intermodulation distortion.

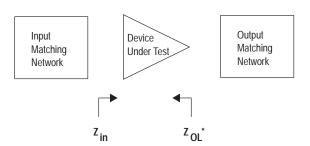


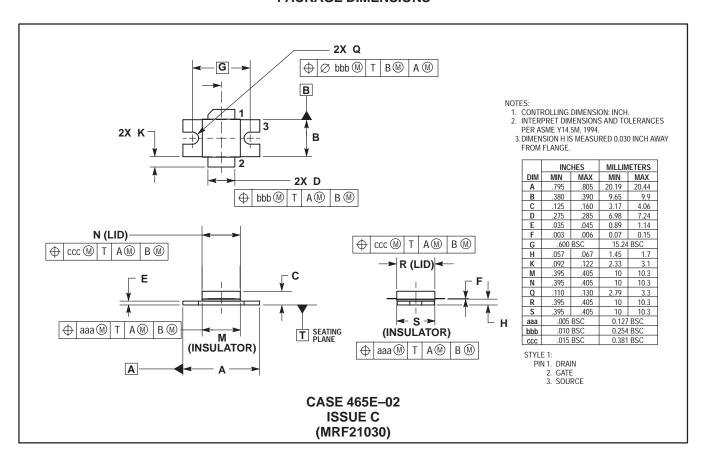
Figure 9. Series Equivalent Input and Output Impedance

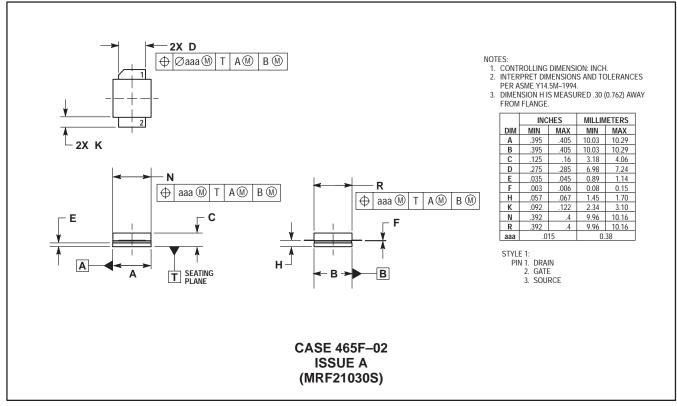
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NOTES

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PACKAGE DIMENSIONS





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