

STRUCTURE

Silicon Monolithic Integrated Circuit

PRODUCT

CMOS Type series regulator

TYPE BH NB 1 WHF V Series

OBLOCK DIAGRAM and APPLICATION CIRCUIT

VIN VIN 3 CIN VIN 3 VOLTAGE REFERENCE OVER CURRENT PROTECTION PROTECTION USER CURRENT PROTECTION STBY OVER CURRENT PROTECTION STBY OVER CURRENT PROTECTION STBY OVER CURRENT STBY OVER CURREN

Cin···0.1 μ F (Ceramic) Co ···2.2 μ F (Ceramic)

OPIN DESCRIPTION

PIN No.	PIN NAME	DESCRIPTION			
1	STBY	OUTPUT CONTROL (High:ON, Low:OFF)			
2	GND	GROUND Pin			
3	VIN	INPUT Pin			
4	VOUT	OUTPUT Pin			
5	NC	NO CONNECT			

Fig. 1 BLOCK DIAGRAM and APPLICATION CIRCUIT

○ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

PARAMETER	Symbol	Limit	Unit
Power Supply Voltage	VMAX	-0.3 ~ +6.5	٧
Power Dissipation	Pd	410 (Note.1)	mW
Operating Temperature Range	Topr	-40 ~ +85	Ĉ
Storage Temperature Range	Tstg	−55 ~ +125	Ĉ

Note.1 Pd derated at 4.1mW/°C for temperature above Ta=25°C, mounted on 70mm×70mm×1.6mm glass-epoxy PCB.

Application example

The application circuit is recommended for use. Make sure to confirm the adequacy of the characteristics.

When using the circuit with changes to the external circuit constants, make sure to leave an adequate margin for external components including static and transitional characteristics as well as dispersion of the IC.

Note that ROHM cannot provide adequate confirmation of patents.

The product described in this specification is designed to be used with ordinary electronic equipment or devices (such as audio-visual equipment, office-automation equipment, communications devices, electrical appliances, and electronic toys).

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ORECOMMENDED OPERATING RANGE

PARAMETER	Symbol	Limit	Unit
Power Supply Voltage	VIN	2.5~5.5	٧
Output Max Current	IMAX	150	mA

O ELECTRICAL CHARACTERISTICS

 $(Ta=25^{\circ}C, VIN=VOUT+1.0V, STBY=1.5V, Cin=0.1 \mu F, Co=2.2 \mu F, unless otherwise noted.)$

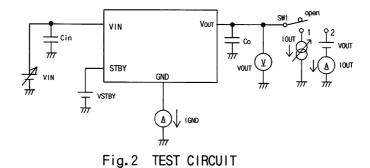
PARAMETER		Symbol	Limit			Unit	Conditions	
		Syllibol	MIN.	TYP.	MAX.	Unit	Conditions	
[Regulator]	[Regulator]							
Output Voltage		VOUT	V0UT×0.99	VOUT	V0UT×1.01	٧	IOUT=1mA	
Circuit Current		IGND	_	60	100	μΑ	IOUT=50mA	
Circuit Current (S	TBY)	ISTBY	_	_	1.0	μΑ	STBY=0V	
Ripple Rejection Ra	atio	RR	_	80	_	dB	VRR=-20dBv, fRR=1kHz, IOUT=10mA	
Load Response 1		LTV1	_	25	_	mV	IOUT=1mA to 30mA	
Load Response 2		LTV2	_	25	-	mV	IOUT=30mA to 1mA	
Input output Voltage differen	ce 1	VSAT1	-	80	150	mV	VIN=0.98×VOUT, IOUT=30mA	
Input output Voltage differen	ce 2	VSAT2	-	250	450	mV	VIN=0.98×VOUT, IOUT=100mA	
Line Regulation		VDLI	-	1	20	mV	VIN=VOUT+0.5V to 5.5V, IOUT=50mA	
Load Regulation 1		VDL01	1	6	30	mV	IOUT=1mA to 100mA	
Load Regulation 2		VDL02	_	9	90	mV	IOUT=1mA to 150mA	
[Over Current Protection]								
Limit Current		ILMAX	_	250	_	mA	Vo=V0UT×0.98	
Short Current		I SHORT		50	_	mΑ	Vo=0V	
[Stand-by block]								
STBY Pull-down Resistor		RSTB	275	550	1100	kΩ		
STBY Control	ON	VSTBH	1.5	-	VCC	٧		
Voltage	0FF	VSTBL	-0.3	_	0.3	٧		

[●]This product is not designed for protection against radio active rays.

ORECOMMENDED OPERATING CONDITION

PARAMETER	Symbol	MIN.	TYP.	MAX.	Unit	CONDITION
Input Capacitor	Cin	0.1	-	-	μF	Ceramic capacitor recommended
Output Capacitor	Co	2.2	_	_	μF	Ceramic capacitor recommended

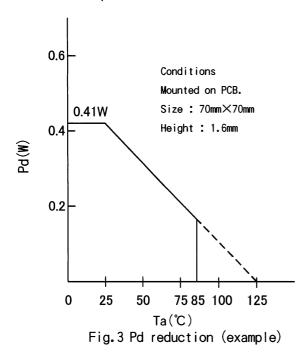
OTEST CIRCUIT



Rev. A



OPower Dissipation Reduction



ODevice Name and Marking

Device	Name :	BH <u>□□</u> N	B1WHFV
Symbol	Desc	Device	
		Output Voltage	Mark
	25	2.5V typ.	CV
	28	2.8V typ.	CW
a	2J	2.85Vtyp.	C2
	29	2.9V typ.	CX
	30	3.0V typ.	CY
	31	3.1V typ.	CZ
	33	3.3V typ.	C0

O Package dimensions (HVS0F5)

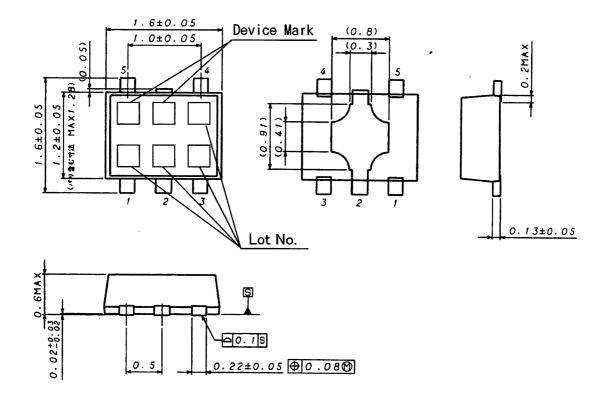


Fig. 4 Package dimensions (UNIT:mm)



Operation Notes

1.) Absolute maximum ratings

May be destroyed if it is operated beyond its absolute maximum ratings. If the device is destroyed in exceeding the recommended maximum ratings, the failure mode will be difficult to determine. (E.g. short mode, open mode) Therefore, physical protection counter-measures (like fuse) should be implemented when operating conditions are beyond the absolute maximum ratings specified.

2.) GND potential

GND potential must be the lowest potential no matter what may happen. Actually, including transitional states, all pins except GND must not be the voltage below GND.

3.) Setting of heat

Consider Pd of actually using states, carry out the heat design that have adequate margin.

4.) Pin short and mistake fitting

When mounting the IC on the PCB, pay attention to the orientation of the IC. If there is a placement mistake, the IC may be burned up.

5.) Actions in strong magnetic field

Using the IC within a strong magnetic field may cause a malfunction.

6.) Mutual impedance

Use short and wide wiring tracks for the power supply and ground to keep the mutual impedance as small as possible. Use a capacitor to keep ripple to a minimum.

7.) Voltage of STB pin

For standby mode, set STB voltage below 0.3V. For normal operation, set the pin voltage beyond 1.2V. It is not recommended to set STB voltage between 0.3V and 1.5V, and it may cause improper operation.

8.) Over current protection circuit

Over current and short circuit protection is built-in at the output, and IC destruction is prevented at the time of load short circuit. These protection circuits is effective in the destructive prevention by the sudden accident, please avoid use to which a protection circuit operates continuously.

9.) Thermal shutdown

In cases of operation at high temperature, thermal shut-down will be activated and output will be turned off. Once IC is returned on normal operating temperature, the output will be turned back on.

10.)Output capacitor

To prevent oscillation at output, it is recommended that the IC be operated at the stable region show as Fig. 5. It is recommended that the IC operated at the capacitor of more than 2.2 $\mu\,\text{F},$ and also ESR=500m Ω below.

As capacitance is larger, stability becomes more stable and characteristic of output load fluctuation is also improved.

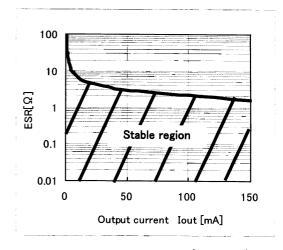


Fig. 5 Stable region (Example)

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