

**AI-JTAG/OPTO**

Opto-isolation board for galvanic isolation for use between applications and development tools using a 20-pin

Active

**Quick view****Design support****Orderable products****Description**

The JTAG opto-isolation (AI-JTAG/OPTO-1) board can be connected between development tools and an application board to provide electrical isolation. This is essential when the development tools are not connected to the same ground as the application to protect the development tools from electrical spikes that often occur in some applications, such as motor control applications.

The JTAG opto-isolation board can be used with any tools that use the 20-pin JTAG standard connection for in-circuit development programming of ST ARM core-based microcontrollers.

**Key Features**

- 1kVDC isolation
- Supports 3.3V or 5V operation, configurable by solder bridge
- Powered from target application using DC/DC converter powered by 3.3V or 5V
- JTAG standard 20-pin connection supporting TRST, TDI, TMS, TCK, RTCK, TDO, RESET signals
- 3.3V or 5V CMOS level compatibility
- 40 nS maximum propagation delay for all unidirectional signals
- Power consumption on target application power supply less than 200 mA

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