

Product Update

UP012302-0807

Errata for eZ80F910300ZCOG/ eZ80F915050MODG/eZ80F916050MODG/ eZ80L925048MODG/eZ80F917050SBCG

Errata for eZ80F910300ZCOG with eZ80F91 e-NET Module/ eZ80F915050ZCOG eZ80F91 e-NET Module/eZ80F916050ZCOG eZ80F91 e-NET Module/eZ80F917050SBCG Zdots[®]

The issues listed in Table 1 are found in eZ80F910300ZCOG with eZ80F91 e-NET Module, eZ80F915050ZCOG eZ80F91 e-NET Module, eZ80F916050ZCOG eZ80F91 e-NET Module, and eZ80F917050SBCG Zdots. When reviewing the following errata, refer to the most recent version of the product specification, eZ80AcclaimPlus!TM Connectivity ASSP eZ80F91 ASSP Product Specification (PS0270) available for download on www.zilog.com.

Table 1. Errata for Z80F910300ZCOG with eZ80F91 e-NET Module/eZ80F915050ZCOG eZ80F91 e-NET Module/eZ80F916050ZCOG eZ80F91 e-NET Module/eZ80F917050SBCG Zdots

No	Summary	Detailed Description
1	When AMD Flash is installed on the 99C0879-001 module,	On an earlier shipment of the kit with AMD Flash on board the eZ80F91 module, U1-10 (WP) is connected to U9-12 of the Flash.
	WP is not a valid func-	Workaround
	tion.	This can be disconnected, and you can, if needed, use the AMD Flash pin 12 as the RY/BY/ signal. Please refer to the AMD Am29LV008T/ Am29LV008B datasheet for details.
2	U5 (eZ80F91) pin 49	MCU U5 pin 49 IOREQ is not connected to JP1 pin 54 IOREQ.
	IOREQ is not connected	
	to JP1 pin 54 IOREQ	Workaround
	·	If the signal or function is needed, it needs to be wired or connected using a jumper.

Errata for eZ80L925048MODG eZ80L92 Module

The issue listed in Table 2 is found in the eZ80L925048MODG eZ80L92 Module. When reviewing the following errata, refer to the most recent version of the product specification, eZ80L92 Module Product Specification (PS0170) and eZ80Acclaim! Flash Microcontrollers eZ80L92 MCU (PS0130) available for download on www.zilog.com.

Table 2. Errata for eZ80L925048MODG eZ80L92 Module

No	Summary	Detailed Description
1	When AMD Flash is installed on the 99C0857-001	On an earlier shipment of the kit with AMD Flash on board the eZ80L92 module, U2-2 (WP) is connected to U3-12 of the Flash.
	module, WP is not a valid function.	Workaround This can be disconnected, and you can, if needed, use the AMD Flash pin 12 as the RY/BY/ signal. Please refer to the AMD Am29LV008T/Am29LV008B datasheet for details

UP012302-0807 Page 2 of 3



Warning: DO NOT USE IN LIFE SUPPORT

LIFE SUPPORT POLICY

ZILOG'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS PRIOR WRITTEN APPROVAL OF THE PRESIDENT AND GENERAL COUNSEL OF ZILOG CORPORATION.

As used herein

Life support devices or systems are devices which (a) are intended for surgical implant into the body, or (b) support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in a significant injury to the user. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system or to affect its safety or effectiveness.

Document Disclaimer

©2007 by Zilog, Inc. All rights reserved. Information in this publication concerning the devices, applications, or technology described is intended to suggest possible uses and may be superseded. ZILOG, INC. DOES NOT ASSUME LIABILITY FOR OR PROVIDE A REPRESENTATION OF ACCURACY OF THE INFORMATION, DEVICES, OR TECHNOLOGY DESCRIBED IN THIS DOCUMENT. ZILOG ALSO DOES NOT ASSUME LIABILITY FOR INTELLECTUAL PROPERTY INFRINGEMENT RELATED IN ANY MANNER TO USE OF INFORMATION, DEVICES, OR TECHNOLOGY DESCRIBED HEREIN OR OTHERWISE. The information contained within this document has been verified according to the general principles of electrical and mechanical engineering.

eZ80, eZ80Acclaim!, eZ80Acclaim*Plus!*, and Zdots are the trademarks or registered trademarks of Zilog, Inc. All other product or service names are the property of their respective owners.

UP012302-0807 Page 3 of 3