Single-Channel Transient Voltage Suppressor

Product Description

The CM6110 is an Application Specific Integrated Passive $^{\text{TM}}$ (ASIP $^{\text{TM}}$) component in a 2x2, 4-bump, 0.4 mm pitch, CSP form factor. This device is designed for:

- Transient Voltage Suppression
- Electrostatic Discharge Protection
- Electrical Overstress Protection

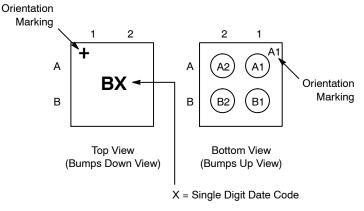
Features

- 4-Bump, 0.8 mm x 0.8 mm Footprint Chip Scale Package (CSP)
- These Devices are Pb-Free and are RoHS Compliant

Table 1. PIN DESCRIPTIONS

4-bump CSP Package			
Pin	Description		
A1 and A2	TVS Channel		
B1 and B2	Device Ground		

PACKAGE / PINOUT DIAGRAMS



4-Bump CSP Package



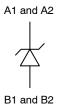
ON Semiconductor®

http://onsemi.com



WLCSP4 CASE 567CB

ELECTRICAL SCHEMATIC



MARKING DIAGRAM



B = Specific Device codeX = Single Digit Date Code

ORDERING INFORMATION

Device	Package	Shipping [†]
CM6110	WLCSP4 (Pb-Free)	10,000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

ELECTRICAL SPECIFICATIONS AND CONDITIONS

Table 2. PARAMETERS AND OPERATING CONDITIONS

Parameter	Rating	Units	
Storage Temperature Range	-55 to +150	°C	
Operating Temperature Range	−30 to +85	°C	

Table 3. ABSOLUTE RATINGS

Parameter	Rating	Units
Failing to nonconductive, I ² t (Maximum I _{PP} value using 10/1000 μs pulse). (Notes 1 and 2)	100	Α

- The device must not burn to open-circuit, when the value is below maximum I_{PP}.
 This parameter is characterized at 25°C using an ON Semiconductor-specific test board.

Table 4. ELECTRICAL OPERATING CHARACTERISTICS (Note 3)

Symbol	Parameter	Conditions	Min	Тур	Max	Units
I _{OFF}	Stand-off quiescent current	Stand-off voltage V _{OFF} = 8 V			800	nA
V _{BR}	Break down voltage	Break down current I _{BR} = 15 mA	10			V
V _{CL}	Clamping voltage during transient	Clamping current I _{CL} = 1 A (Notes 4 and 6)			13	V
V _F	Forward voltage	Forward current I _F = 850 mA (Note 4)			1.1	V
C _{L1}	Line capacitance	V _{BIAS} = 0 V (Note 4)		310		pF
C _{L2}		V _{BIAS} = 5 V; T _A = 25°C	124	155		pF
V _{ESD}	ESD Protection Peak Discharge Voltage at any channel input a) Contact Discharge per IEC 61000-4-2 standard b) Air Discharge per IEC 61000-4-2 standard	T _A = 25°C (Notes 4 and 5)	±30 ±30			kV
	Minimum Attenuation Freq = 80 MHz - 1 GHz Freq = 1 - 4 GHz	R _{SOURCE} = R _{LOAD} = 50 Ω T _A = 25°C (Note 4)	±30	12 20		dB

- 3. All parameters specified for $T_A = -30^{\circ}C$ to $85^{\circ}C$ unless otherwise noted.
 4. These parameters guaranteed by design and characterization.
 5. Standard IEC 61000–4–2 with $C_{Discharge} = 150$ pF, $R_{Discharge} = 330$ Ω .
 6. Transient: 8 x 20 μ s current pulse.

RF CHARACTERISTICS

T_A = 25°C, 50 Ω Environment

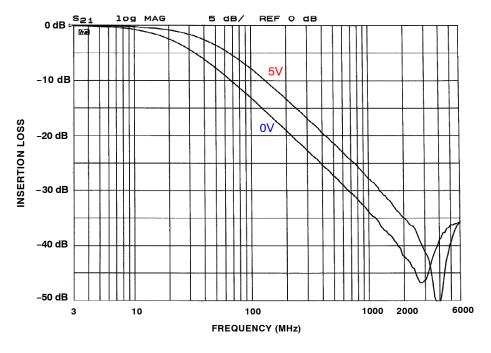
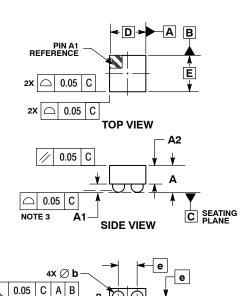


Figure 1. Insertion Loss (0 V and 5 V Bias)

PACKAGE DIMENSIONS

WLCSP4, 0.8x0.8 CASE 567CB-01 **ISSUE O**



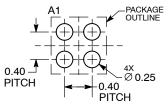
BOTTOM VIEW

NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M. 1994.
- CONTROLLING DIMENSION: MILLIMETERS. COPLANARITY APPLIES TO SPHERICAL CROWNS OF SOLDER BALLS.

	MILLIMETERS			
DIM	MIN	MAX		
Α	0.57	0.63		
A1	0.17	0.24		
A2	0.41 REF			
b	0.24	0.29		
D	0.80 BSC			
E	0.80 BSC 0.40 BSC			
е				

RECOMMENDED **SOLDERING FOOTPRINT***



DIMENSIONS: MILLIMETERS

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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