■ FEATURE AND APPLICATION

• Feature

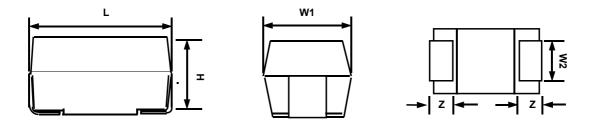
The product is a standard type that has been most widely used among tantalum chip capacitors.

- Molded Case available in six case codes.
- Compatible with automatic pick and place equipment.
- Meets or Exceeds EIA standard 535BAAC .

• Application

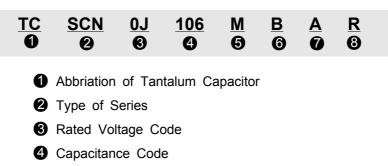
- General electronic equipment
- Smoothing Circuit of DC-DC Converters & Output side of AC-DC Converters
- De-Coupling Circuit of High Speed ICs & MPUs
- Various Other High Frequency Circuit Applications

■ APPEARANCE AND DIMENSION



Code	EIA Code	DIMENSION (mm)								
Code	EIA COUE	L	W ₁	W ₂	Н	Z				
J	1608	16+0.15 -0.1	0.85+0.15 -0.1	0.6±0.1	0.85+0.15 -0.1	0.4±0.1				
Р	2012	2012	$2.0\ \pm 0.2$	1.25 ±0.2	0.9 ±0.1	1.2 MAX				
Α	3216	3.2 ±0.2	1.6 ±0.2	1.2 ±0.1	1.6 ±0.2	0.8 ±0.3				
В	3528	3.5 ±0.2	2.8 ±0.2	2.2 ±0.1	1.9 ±0.2	0.8 ±0.3				
С	6032	6.0 ±0.3	3.2 ±0.3	2.2 ±0.1	2.5 ±0.3	1.3 ±0.3				
D	7343	7.3 ±0.3	4.3 ±0.3	2.4 ±0.1	2.8 ±0.3	1.3 ±0.3				

PART NUMBERING



- **5** Capacitance Tolerance Code
- 6 Case size Code
- Packing Code
- 8 Packing polarity Code

1 Tantalum Capacitor

The symbol shows a simplified character of the tantalum capacitor.

2 Type of Series

The symbol shows the type of the capacitor. (SCN, SCS)

- SCN : Samsung Capacitor Normal - Standard series

- SVN : Samsung en Vironmental capacitor Normal - Pb-free series

8 Rated Voltage Code

Symbol	DC Rated Voltage	Symbol	DC Rated Voltage
0E	2.5	1C	16
0G	4	1D	20
0J	6.3	1E	25
1A	10	1V	35

4 Capacitance Code

Symbol	Capacitance (µF)	Pico Farad (pF)	Symbol	Capacitance (µF)	PicoFarad (pF)
105	1.0	10×10⁵	685	6.8	68×10⁵
106	10.0	10×10 ⁶	476	47	47×10 ⁶
107	100.0	10×10 ⁷	477	470	47×10 ⁷

6 Capacitance tolerance Code

Symbol	Tolerance(%)	Symbol	Tolerance(%)
к	±10	Μ	±20

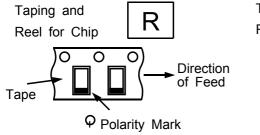
6 Case size Code

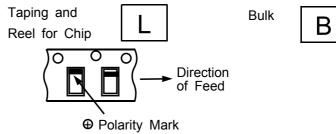
Case	EIA Code	Case	EIA Code
J	1608	С	6032
Р	2012	D	7343
Α	3216		
В	3528		

Packing Code

Symbol	Packing Code
Α	7 inch
С	13 inch

(3) Packing polarity Code

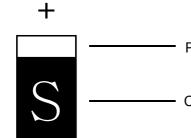






• Marking

- ► J Case
 - J case 4.7µF 6.3V



Polarity (White Stripe)

- Capacitance & Working voltage Code

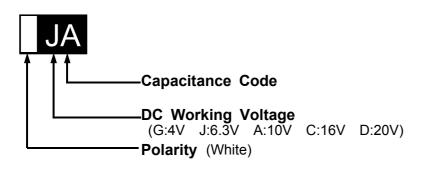
Morking	CAP.	1.0	1.5	2.2	3.3	4.7	6.8	10
Marking (6.3V basis)	Code	а	Е	J	Ν	S	W	А

[Code Reference]

۷ پل ^E	4	6.3	10	16
1.0		а	В	С
1.5	D	E	F	G
2.2	I	J	к	L
3.3	М	N	0	Р
4.7	R	S	т	U
6.8	V	w	X	Y
10	Z	Α		



▶ P Case



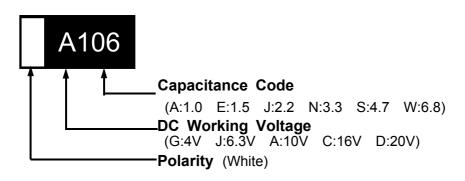
Capacitance Range	1 DIGIT	2 DIGIT		
< 1.0 ^{µF}	A Small Letter	A Small Letter		
1.0 ^µ F≤ Cap.< 10 ^µ F	A Capital Letter	A Small Letter		
≥ 10,4F	A Capital Letter	A Capital Letter		

[Code Reference]

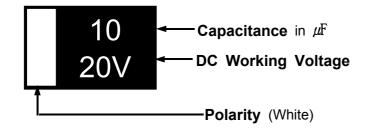
V #F	4	6.3	10	16	20
0.22	gj	jj	aj	cj	
0.33					
0.47	gs	js	as	CS	ds
0.68	gw	jw	aw	CW	dw
1.0	Ga	Ja	Aa	Са	
1.5					
2.2	Gj	Jj	Aj		
3.3	Gn	Jn	An		
4.7	Gs	Js	As		
6.8	Gw	Jw			
10	GA	JA	AA		
15					
22	GJ				



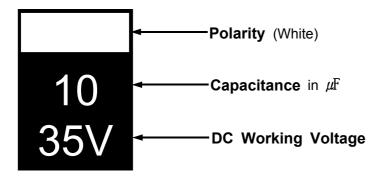
► A Case



► B Case



► C,D Case

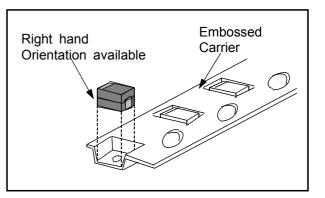


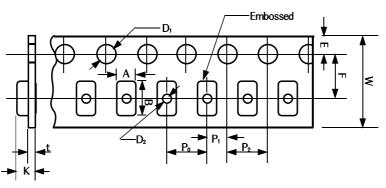


• Embossed Plastic Type

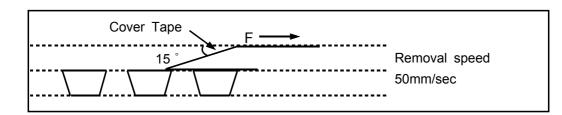
The tantalum chip capacitors shall be packaged in tape and reel form for effective use.

- Tape : Semitransparent embossed plastic
- Cover tape : Attached with press, polyester
- The tension of removing the cover tape, F=10 $^{\sim}\,70g$



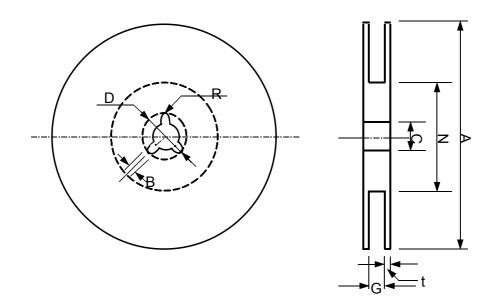


Case Code	W±0.3 (± 0.012)	F±0.1 (± 0.004)	E±0.1 (± 0.004)	P₀±0.1 (± 0.004)	P₁±0.1 (± 0.004)	P₂±0.1 (± 0.004)	D₁+0.1 (+0.00 4)	D₂Min.	t	A±0.2 (± 0.008)	B±0.2 (± 0.008)	K±0.2 (± 0.008)		
J*								ø0.6 (0.024)	0.25 (0.0098)	0.98 (0.039)	1.80 (0.071)	1.0 (0.039)		
P*	8	3.5		4					0.2	1.4 (0.055)	2.3 (0.091)	1.4 (0.055)		
Α	(0.315)	(0.138)	1.75	(0.157)	2 (0.079)	4	ø1.5	ø1.0 (0.039)	(0.008)	1.9 (0.075)	3.5 (0.138)	1.9 (0.075)		
В			(0.069)			(0.157)	(0.059)			3.3 (0.130)	3.8 (0.150)	2.1 (0.083)		
С	12 5.5	5.5	8				ø1.5	0.3 (0.012)	3.7 (0.146)	6.4 (0.252)	3.0 (0.118)			
D	(0.472)	(0.217)		(0.315)				(0.059)				4.8 (0.189)	7.7 (0.303)	3.3 (0.130)





Reel Demension



Tape Width	A±2 (±0.079)	N Min.	C±0.5 (±0.020)	D±0.5 (±0.020)	B±051 (±0.020)		t+0.5 (±0.020)	R
8mm	ø178	ø70 (2.756)	ø13	ø21	2	10 (0.394)	2	0.99
12mm	(7)	ø60 (2.362)	(0.512)	(0.827)	(0.079)	14 (0.551)	(0.079)	(0.039)
8mm	ø330	ø80	ø13	ø21	2	10 (0.394)	2	0.99
12mm	(13)		0.827) (0.079)		(0.079)	(0.039)		

Case Size reference	180mm(7") reel	330mm(13") reel
J	4,000pcs	_
Р	3,000pcs	-
А, В	2,000pcs	8,000pcs
C , D	500pcs	2,500pcs



■ RELIABILITY TEST DATA

NO	ITEMS	TEST CONDITION	PERFORMANCE
1	RATED DC VOLTAGE	-55℃ ~ +85℃	2.5~35V
2	CAPACITANCE	MEASURING FREQUENCY : 120±12Hz MEASURING VOLTAGE : 0.5Vrms + 0.5~2V DC MEASURING CIRCUITS : EQUIVALENT SERIES CIRCUIT	CAPACITANCE RANGE $0.15 \sim 470 \mu$ F TOLERANCE ON CAP. $\pm 10\%, \pm 20\%$
3	TANGENT OF LOSS ANGLE	MEASUREMENT SHALL BE MADE UNDER THE SAME CONDITIONS AS THOSE GIVEN FOR THE MEASUREMENT OF CAPACITANCE.	
4	LEAKAGE CURRENT	THE RATED DC VOLTAGE SHALL BE APPLIED TO TERMINALS ACROSS THE TEST CAPACITOR Cx, BY THE METHOD AS SHOWN BELOW. THE LEAKAGE CURRENT SHALL THEN BE MEASURED AFTER CHARGE FOR 5 MIN. MEASURING CIRCUITS MEASURING CIRCUITS	0.01CV or 0.5 ^{#A} WHICHEVER IS GREATER
5	IMPEDENCE	AC VOLTAGE(0.5Vrms OR LESS) OF A FREQUENCY SPECIFIED ON NEXT PAGE SHALL BE APPLIED AND THE VOLTAGE DROP ACROSS CAPACITOR TERMINALS SHALL BE MEASURED THE IMPEDANCE SHALL BE CALCULATED BY THE FOLLOWING EQUATION. $Impedance Z = \frac{E}{I}$ E : VOLTAGE DROP ACROSS THE CAPACITOR TERMINALS I : CURRENT FLOWING THROUGH THE CAPACITOR (FREQUENCY : 100±10kHz)	

NO	ITEMS	TEST CONDITION					PERFORMANCE		
		THE CAPACITOR SHALL BE SUBJECTED IN TURN TO PROCEDU							
		BELOW						0120	
		STEP	STEP TEMP. DURATIO		CHANGE IN CAPACITANCE (\Delta C)		TANGENT OF LOSS ANGLE (D.F.)		LEAKAGE
		1	25±2℃		SPEC	'HIN IFIED RANCE	TABLE 1 ON PAGE 13		WITHIN ORIGINAL LIMIT
6	TEMPERATURE STABILITY	2	-55 <mark>0</mark> ℃ -3 ℃	2 HOURS.		0% OF VALUE	TABLE PAGE	-	N/A
		3	25±2℃	25 MIN.					
		4	+85 <mark>+3</mark> ℃ 0 ℃	2 HOURS.		10% OF VALUE	TABLE PAGE	-	WITHIN 10X ORIGINAL LIMIT
		5	+125 ⁺³ ℃ 0	2 HOURS.		0 TO +12% OF TABLE 1 ON INITIAL VALUE PAGE 13		WITHIN 12.5X ORIGINAL LIMIT	
7	SURGE TEST	5+125 \odot 2HOURS.INITIAL VALUEPAGE 13THE CAPACITOR SHALL BE SUBJECTED TO THE SURGE VOLTAGE AS SPECIFIED ON NEXT PAGE IN A CYCLE OF 6± 0.5 MIN. WHICH CONSISTS OF 30±5 SEC. FOLLOWED BY A DISCHARGE PERIOD OF APPROX. 5 MIN 30 SEC. AT A TEMPERATURE OF +85°C FOR 1,000 CYCLES. AND THE CAPACITOR SHALL BE STORED UNDER STANDARD ATMOSPHERIC CONDITIONS TO OBTAIN THERMAL EQUILIBRIUM AFTER MEASUREMN\ENT. MEASURING_CIRCUITR1 S S $R2$ C C WHERE R1 : PROTECTIVE SERIES RESISTOR (33 Ω) R2 : DISCHARGE RESISTOR(33 Ω) CX : TEST CAPACITOR V : DC VOLTAGE S : SWITCH						-	

SAMSUNG

NO	ITEMS	TEST CONDITION	PERFORMANCE			
8	DERATING VOLTAGE	WHEN OPERATING AT HIGH TEMPERATURE RANGE FROM 85°C to 125°C, THE OPERATION SHALL BE CARRIED OUT AT A DERATED VOLTAGE OR LESS DERATING VOLTAGE Vt AT ANY TEMPERATURE BETWEEN 85°C AND 125°C SHALL BE CALCULATED BY THE FOLLOWING EQUATION VOLTAGE DERATING % 80 60 40 20 0 -55 0 20 85 125 OPERATING TEMPERATURE $Vt = Vr - \frac{Vr - Vd}{40}(T - 85)$ WHERE Vt : DERATED VOLTAGE AT ANY TEMP. BETWEEN 85°C to 125°C Vr : RATED VOLTAGE AT 125°C				
9	ELECTRODE (TERMINAL STRENGTH)	APPLY PRESSURE IN THE DIRECTION OF THE ARROW AT A RATE OF ABOUT 0.5MM/SEC. UNTIL IT REACHES A BENT WIDTH OF 3MM AND HOLD FOR 30 SEC. THE TEST BOARD SHALL BE IEC 40(S) 541. FOR OTHER PROCEDURES REFER TO IEC 40(S) 541.	THERE SHALL BE NO EVIDENCE OF MECHANICAL DAMAGE. ELECTRICAL CHARACTERISTICS SHALL SATISFY THE INITIAL REQUIREMENT. IF THERE ARE ELECTRODES ON BOTH SURFACES, IT SHALL SATISFY THE ABOVE REQUIREMENT ON WHICHEVER SURFACE IT MAY BE FIXATED ON.			

NO	ITEMS		
<u>NO</u>	ADHESION (ELECTRODE PEELING STRENGTH)	A STATIC LAOD OF 19.6N USING A R0.5 SCRATCH TOLL SHALL BE APPLIED ON THE CORE OF THE COMPONENT AND IN THE DIRECTION OF THE ARROW AND HOLD FOR 5 SEC. THE TEST BOARD SHALL BE IEC 40(S)541. HOWEVER THE BASE MATERIAL SHALL BE G-10 or FR-4 (ANSI GRADE) Scratch tool	PERFORMANCE THERE SHALL BE NO EVIDENCE OF MECHANICAL DAMAGE. ELECTRICAL CHARACTERISTICS SHALL SATISFY THE INITIAL REQUIREMENT. IF THERE ARE ELECTRODES ON BOTH SURFACES, IT SHALL SATISFY THE ABOVE REQUIREMENT ON WHICHEVER SURFACE IT MAY BE FIXATED ON.
11	CORE BODY STRENGTH	Chip A ROD OF 9.8N USING A R0.5 PRESSURE ROD SHALL BE APPLIED TH THE CENTER IN THE DIRECTION OF THE ARROW AND HOLD FOR 10 SEC Pressure Chip 0.5L Chip 0.5L L > W	THERE SHALL BE NO EVIDENCE OF MECHANICAL DAMAGE. ELECTRICAL CHARACTERISTICS SHALL SATISFY THE INITIAL REQUIREMENT.

NO	ITEMS	TEST CONDITION	PERFORMANCE		
12	SOLDERABILITY [Pb-free]	SOLDER TEMPERATURE : 245±5℃ DIP TIME : 3±0.5 SEC. SOLDER : Sn-3Ag-0.5Cu FLUX : ROSIN(KSM2951)+Solvent(ISA) (ROSIN 25WT%)	MORE THAN 75% OF THE TERMINAL SURFACE MUST BE SOLDERED NEWLY.		
13	RESISTANCE TO SOLDERING HEAT	PREHEAT : $100 \sim 110^{\circ}$ FOR 30 SEC. TEMPERATURE : $260\pm5^{\circ}$ DIP TIME : 10 ± 1 SEC ALL SAMPLES SHALL BE DIPPED IN SOLDER BATH. MEASUREMENT SHALL BE MADE AT ROOM TEMPERATURE AFTER 1~2 HOURS OF COOLING TIME.	CHANGE IN CAPACITANCE : $\pm 5\%$ OF INITIAL VALUE TANGENT OF LOSS ANGLE : LEAKAGE CURRENT : APPEARANCE : THERE SHALL BE NO EVIDENCE OF MECHANICAL DAMAGE		
		CONVECTION REFLOW PREHEAT : 150~190°C FOR 130 SEC. PEAK TEMPERATURE : 245±5°C FOR 10 SEC. METHOD : SAMPLES SHALL BE PASSED REFLOW 2 TIMES. MEASUREMENT SHALL BE MADE AT ROOM TEMPERATURE AFTER 3~4 HOURS OF COOLING TIME.	Change in capacitance: ±10% of initial value Tangent of loss angle: Leakage Current :		
14	RESISTANCE TO CLEAN TEST	IMMERSION CLEANING THE CAPACITOR SHALL BE CLEANED AT ROOM TEMPERATURE FOR 60sec. USING ISOPROPYL ALCOHOL	THERE SHALL BE NO EVIDENCE OF MECHANICAL DAMAGE. AND MARKING SHALL BE LEGIBLE. ELECTRICAL CHARACTERISTICS SHALL SATISFY THE INITIAL REQUIREMENT.		
15	VIBRATION	FREQUENCY : 10 to 55 to 10Hz (in 1 min.) MAX AMPLITUDE : 1.5 mm. DIRECTION OF VIBRATION : IN DIRECTION OF X,Y AND Z AXES TIME : 2 HOURS EACH DIRECTION AND 6 HOURS IN TOTAL DURING THE LAST 30 min. OF VIBRATION IN EACH DIRECTION, THE CAPACITANCE SHALL BE MEASURED 3 TO 5 TIMES. FOR OTHER PROCEDURES REFER TO IEC Pub. 68-2-6. MOUNTING METHOD SOLDER ALUMINA BOARD	CHANGE IN CAPACITANCE : WITHIN : ±5% OF THE INITIAL VALUE TANGENT OF LOSS ANGLE : LEAKAGE CURRENT : APPEARANCE : THERE SHALL BE NO EVIDENCE OF MECHANICAL DAMAGE		



NO	ITEMS		TEST CONDIT	ION		PERFORMANCE
16	MOISTURE RESISTANCE	TEMPERATUR HUMIDITY OF ELECTRICAL AFTER BEINC FOR 1~2 HC	TOR SHALL BE STURE OF 40 ± 2 °C ANI 90% TO 95% FOR MEASUREMENTS BOARD AT ROOM URS. FOR OTHER C Pub. 68-2-2.	CHANGE IN CAPACITANCE : WITHIN : ±10% OF THE INITIAL VALUE TANGENT OF LOSS ANGLE : LEAKAGE CURRENT :		
17	LOAD LIFE		RE VOLTAGE RATED VOLT, DERATED VOLTAGE TOR SHALL BE PL		CHANGE IN CAPACITANCE : WITHIN : ±10% OF THE INITIAL VALUE TANGENT OF LOSS ANGLE : LEAKAGE CURRENT :	
		ELECTRICAL MEASUREMENTS SHALL BE MADE AFTER BEING STORED AT ROOM TEMPERATURE FOR 1~2 HOURS.				
18	STORAGE AT LOW TEMPERATURE	TEMPERATUR WITHOUT LO ELECTRICAL	MEASUREMENTS	ELECTRICAL CHARACTERISTICS SHALL SATISFY THE INITIAL REQUIREMENT.		
		STEP	TEMPERATURE		TIME	
	Thermal Shock	1	-55 0 ℃ -3 ℃	3	0 ±3 MIN	CHANGE IN CAPACITANCE :
		2	25 ± 5℃	1	5 \pm 2 MIN	WITHIN : ±10% OF THE INITIAL VALUE
40		3	125	З	0 ±3 MIN	TANGENT OF LOSS ANGLE :
19		4	$25 \pm 5^{\circ}$ C	1	5 ±2 MIN	LEAKAGE CURRENT :
		THE CAPACITOR SHALL BE SUBJECTED TO EACH SPECIFIED TEMPERATURE FOR EACH SPECIFIED TIME IN THE TABLE ABOVE THESE 4 STEP CONSTITUTES ONE CYCLES SHALL BE PERFORMED CONTINUOUSLY				