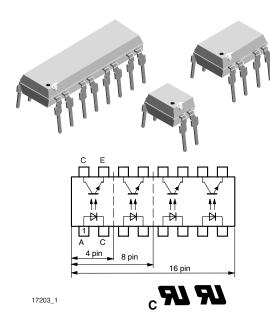


Vishay Semiconductors

Optocoupler, Phototransistor Output



DESCRIPTION

In the K817P, K827PH, K847PH parts each channel consist of a phototransistor optically coupled to a gallium arsenide infrared-emitting diode in a 4 pin (single); 8 pin (dual); 16 pin (quad) plastic dual inline package.

The elements are mounted on one leadframe providing a fixed distance between input and output for highest safety requirements.

FEATURES

- Endstackable to 2.54 mm (0.1") spacing
- DC isolation test voltage 5000 V_{RMS}
- · Low coupling capacitance of typical 0.3 pF
- Current transfer ratio (CTR) selected into groups



- · Low temperature coefficient of CTR
- Wide ambient temperature range
- Available in single, dual and quad channel packages
- Lead (Pb)-free component
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC

APPLICATIONS

- · Programmable logic controllers
- Modems
- · Answering machines
- · General applications

AGENCY APPROVALS

- UL1577, file no. E76222 system code U, double protection
- CSA 93751

ORDER INFORMATION				
PART	REMARKS			
K817P	CTR 50 % to 600 %, DIP-4			
K817P1	CTR 40 % to 80 %, DIP-4			
K817P2	CTR 63 % to 125 %, DIP-4			
K817P3	CTR 100 % to 200 %, DIP-4			
K817P4	CTR 160 % to 320 %, DIP-4			
K817P5	CTR 50 % to 150 %, DIP-4			
K817P6	CTR 100 % to 300 %, DIP-4			
K817P7	CTR 80 % to 160 %, DIP-4			
K817P8	CTR 130 % to 260 %, DIP-4			
K817P9	CTR 200 % to 400 %, DIP-4			
K827PH	CTR 50 % to 600 %, DIP-8			
K847PH	CTR 50 % to 600 %, DIP-16			

Note

K827PH and K847PH are marked as K827P and K847P respectively.

K817P, K827PH, K847PH

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ABSOLUTE MAXIMUM RATINGS (1)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
INPUT						
Reverse voltage		V_{R}	6	V		
Forward current		I _F	60	mA		
Forward surge current	t _P ≤ 10 μs	I _{FSM}	1.5	Α		
Power dissipation		P _{diss}	100	mW		
Junction temperature		Tj	125	°C		
OUTPUT						
Collector emitter voltage		V_{CEO}	70	V		
Emitter collector voltage		V _{ECO}	7	V		
Collector current		I _C	50	mA		
Collector peak current	$t_p/T = 0.5, t_p \le 10 \text{ ms}$	I _{CM}	100	mA		
Power dissipation		P _{diss}	150	mW		
Junction temperature		T _j	125	°C		
COUPLER						
AC isolation test voltage (RMS)	t = 1 min	V_{ISO}	5000	V_{RMS}		
Total power dissipation		P _{tot}	250	mW		
Operating ambient temperature range		T _{amb}	- 40 to + 100	°C		
Storage temperature range		T _{stg}	- 55 to + 125	°C		
Soldering temperature (2)	2 mm from case, $t \le 10 \text{ s}$	T _{sld}	260	°C		

Notes

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

⁽²⁾ Refer to wave profile for soldering conditions for through hole devices.

ELECTRICAL CHARACTERISTICS							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
INPUT							
Forward voltage	I _F = 50 mA	V _F		1.25	1.6	V	
Junction capacitance	$V_R = 0 V, f = 1 MHz$	Cj		50		pF	
OUTPUT							
Collector emitter voltage	I _C = 100 μA	V _{CEO}	70			V	
Emitter collector voltage	I _E = 100 μA	V _{ECO}	7			V	
Collector dark current	$V_{CE} = 20 \text{ V}, I_F = 0, E = 0$	I _{CEO}			100	nA	
COUPLER							
Collector emitter saturation voltage	I _F = 10 mA, I _C = 1 mA	V _{CEsat}			0.3	V	
Cut-off frequency	$I_F = 10 \text{ mA}, V_{CE} = 5 \text{ V},$ $R_L = 100 \Omega$	f _c		100		kHz	
Coupling capacitance	f = 1 MHz	C _k		0.3		pF	

Note

 T_{amb} = 25 $^{\circ}C,$ unless otherwise specified.

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

 $^{^{(1)}}$ T_{amb} = 25 $^{\circ}$ C, unless otherwise specified.



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CURRENT TRANSFER RATIO							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
	V _{CE} = 5 V, I _F = 5 mA	K817P	CTR	50		600	%
		K827PH	CTR	50		600	%
		K847PH	CTR	50		600	%
	V _{CE} = 5 V, I _F = 10 mA	K817P1	CTR	40		80	%
		K817P2	CTR	63		125	%
1.7		K817P3	CTR	100		200	%
I _C /I _F		K817P4	CTR	160		320	%
	V _{CE} = 5 V, I _F = 5 mA	K817P5	CTR	50		150	%
		K817P6	CTR	100		300	%
		K817P7	CTR	80		160	%
		K817P8	CTR	130		260	%
		K817P9	CTR	200		400	%

SWITCHING CHARACTERISTICS						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Delay time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega$ (see figure 1)	t _d		3		μs
Rise time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega$ (see figure 1)	t _r		3		μs
Fall time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega$ (see figure 1)	t _f		4.7		μs
Storage time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega$ (see figure 1)	t _s		0.3		μs
Turn-on time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega$ (see figure 1)	t _{on}		6		μs
Turn-off time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega$ (see figure 1)	t _{off}		5		μs
Turn-on time	$V_S = 5 \text{ V}, I_F = 10 \text{ mA}, R_L = 1 \text{ k}\Omega$ (see figure 2)	t _{on}		9		μs
Turn-off time	$V_S = 5 \text{ V}, I_F = 10 \text{ mA}, R_L = 1 \text{ k}\Omega$ (see figure 2)	t _{off}		18		μs

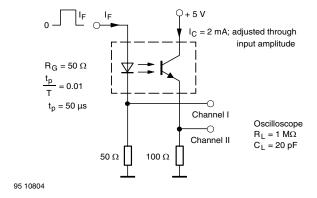


Fig. 1 - Test Circuit, Non-Saturated Operation

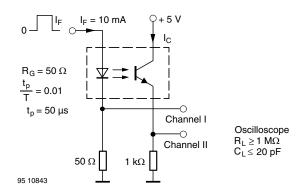


Fig. 2 - Test Circuit, Saturated Operation

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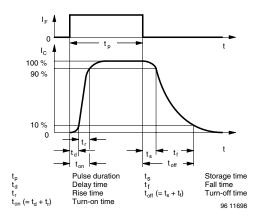


Fig. 3 - Switching Times

TYPICAL CHARACTERISTICS

T_{amb} = 25 °C, unless otherwise specified

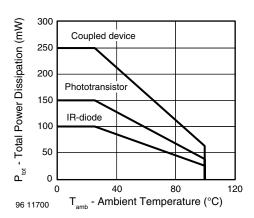


Fig. 4 - Total Power Dissipation vs. Ambient Temperature

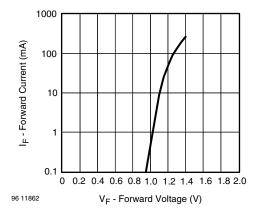


Fig. 5 - Forward Current vs. Forward Voltage

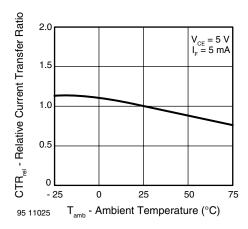


Fig. 6 - Relative Current Transfer Ratio vs. Ambient Temperature

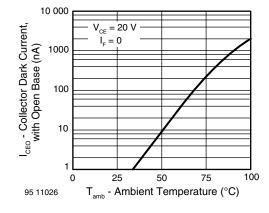


Fig. 7 - Collector Dark Current vs. Ambient Temperature



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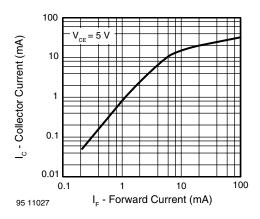


Fig. 8 - Collector Current vs. Forward Current

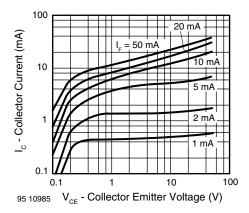


Fig. 9 - Collector Current vs. Collector Emitter Voltage

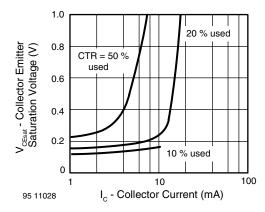


Fig. 10 - Collector Emitter Saturation Voltage vs. Collector Current

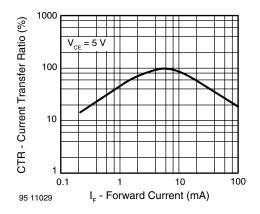


Fig. 11 - Current Transfer Ratio vs. Forward Current

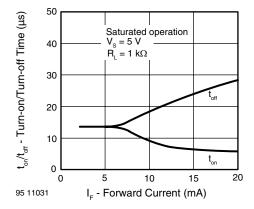


Fig. 12 - Turn-on/Turn-off Time vs. Forward Current

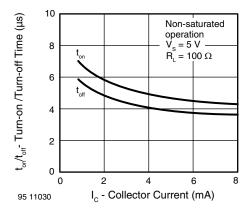


Fig. 13 - Turn-on/off Time vs. Collector Current

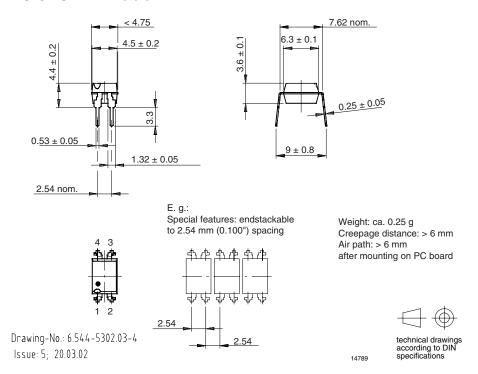
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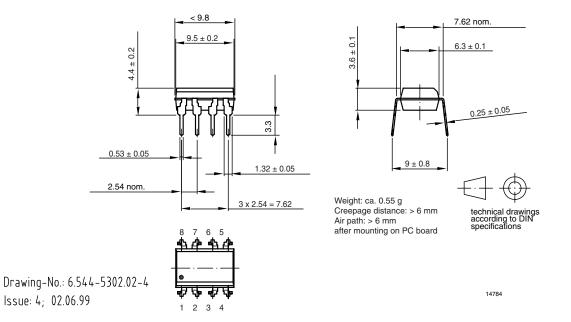
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PACKAGE DIMENSIONS in millimeters



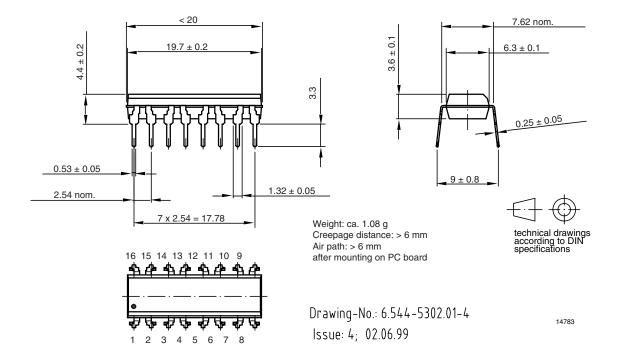






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OZONE DEPLETING SUBSTANCES POLICY STATEMENT

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively.
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA.
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

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Vishay

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