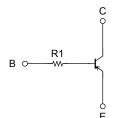
TOSHIBA Transistor Silicon PNP Epitaxial Type (PCT process) (Bias Resistor Built-in Transistor)

RN2972FS, RN2973FS

Switching, Inverter Circuit, Interface Circuit and Driver Circuit Applications

- Two devices are incorporated into a fine pitch small mold (6-pin) package.
- Incorporating a bias resistor into a transistor reduces parts count. Reducing the parts count enables the manufacture of ever more compact equipment and lowers assembly cost.
- Complementary to RN1972FS, RN1973FS

Equivalent Circuit and Bias Resistor Values



Absolute Maximum Ratings (Ta = 25°C) (Q1, Q2 common)

Characteristics	Symbol	Rating	Unit
Collector-base voltage	V _{CBO}	-20	V
Collector-emitter voltage	V _{CEO}	-20	V
Emitter-base voltage	V _{EBO}	-5	V
Collector current	Ι _C	-50	mA
Collector power dissipation	P _C (Note 1)	50	mW
Junction temperature	Tj	150	°C
Storage temperature range	T _{stg}	-55~150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba

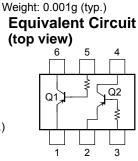
Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Total rating

Electrical Characteristics (Ta = 25°C) (Q1, Q2 common)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off curre	ent	I _{CBO}	$V_{CB}=-20~V,~I_{E}=0$	_	_	-100	nA
Emitter cut-off curren	t	I _{EBO}	$V_{EB}=-5~V,~I_C=0$	_	_	-100	nA
DC current gain		h _{FE}	$V_{CE} = -5 \text{ V}, \text{ I}_{C} = -1 \text{ mA}$	300	_	_	
Collector-emitter satu	ration voltage	V _{CE (sat)}	$I_C=-5\ m\text{A},\ I_B=-0.25\ m\text{A}$	_		-0.15	V
Collector output capa	citance	C _{ob}	$V_{CB}=-10~V,~I_{E}=0,~f=1~MHz$	_	1.2	_	pF
Input resistor	RN2972FS	- R1	_	17.6	22	26.4	kΩ
	RN2973FS			37.6	47	56.4	

1.0±0.05 0.8±0.05 0.1±0.05 0.1±0.05 0.15±0.05 6 0.7±0.05 Q g. B 5 35 1±0.05 02 õ \$ 1.EMIITTER1 (E1) 2.EMITTER2 (E2) 3.BASE2 (B2) 4.COLLECTOR2 (C2)5.BASE1 (B1) 6.COLLECTOR1 fS6 (C1) JEDEC _ JEITA TOSHIBA 2-1F1C

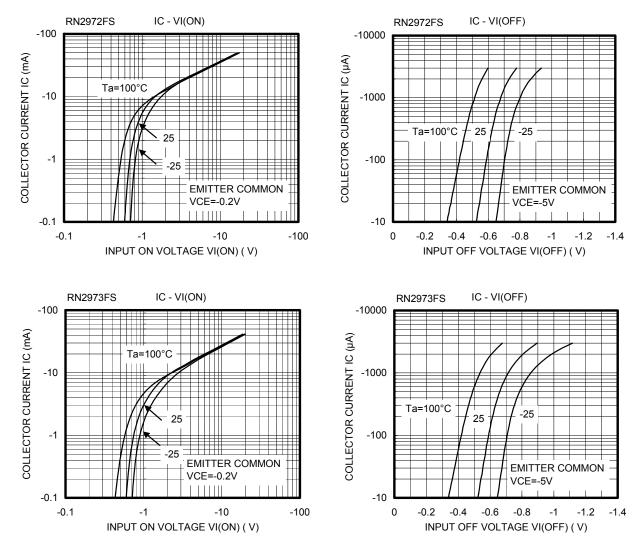




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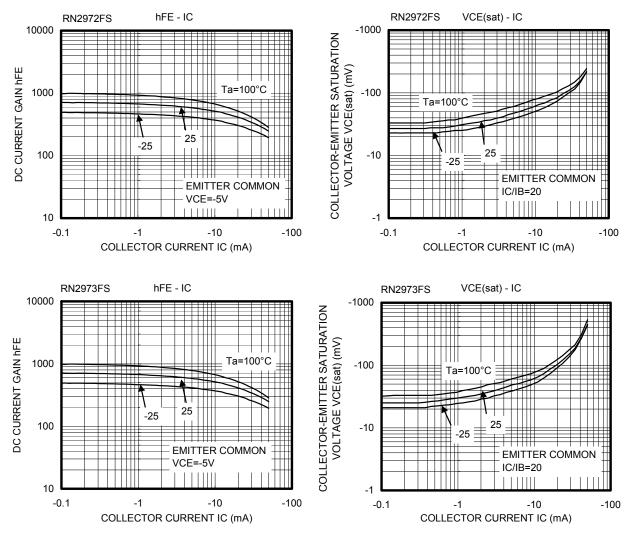
<u>TOSHIBA</u>

Q1, Q2 Common



<u>TOSHIBA</u>

Q1, Q2 Common



Type Name	Marking	
RN2972FS	6 5 4 Type name KH 1 2 3	
RN2973FS	6 5 4 Type name KJ 1 2 3	

Handling Precaution

When handling individual devices (which are not yet mounted on a circuit board), be sure that the environment is protected against electrostatic discharge. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.

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