

## P-Channel 30-V (D-S) MOSFET

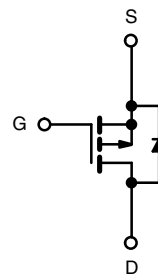
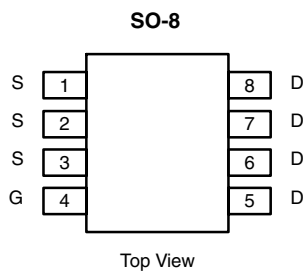
PRODUCT SUMMARY		
$V_{DS}$ (V)	$r_{DS(on)}$ ( $\Omega$ )	$I_D$ (A)
-30	0.02 @ $V_{GS} = -10$ V	-8.0
	0.035 @ $V_{GS} = -4.5$ V	-6.0

### FEATURES

- Lead (Pb)-Free Version is RoHS Compliant



**RoHS**  
COMPLIANT



P-Channel MOSFET

Ordering Information: Si4435DY-T1-REV A  
Si4435DY-T1-A-E3 (Lead (Pb)-Free)

ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ$ UNLESS OTHERWISE NOTED)			
Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	-30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current ( $T_J = 150^\circ\text{C}$ ) <sup>a</sup>	$I_D$	$T_A = 25^\circ\text{C}$	-8.0
		$T_A = 70^\circ\text{C}$	-6.4
Pulsed Drain Current	$I_{DM}$	-50	A
Continuous Source Current (Diode Conduction) <sup>a</sup>	$I_S$	-2.1	
Maximum Power Dissipation <sup>a</sup>	$P_D$	$T_A = 25^\circ\text{C}$	2.5
		$T_A = 70^\circ\text{C}$	1.6
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 to 150	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS			
Parameter	Symbol	Limit	Unit
Maximum Junction-to-Ambient <sup>a</sup>	$R_{thJA}$	50	$^\circ\text{C}/\text{W}$

**Notes**

a. Surface Mounted on FR4 Board,  $t \leq 10$  sec.

For SPICE model information via the Worldwide Web: <http://www.vishay.com/www/product/spice.htm>

SPECIFICATIONS (T <sub>J</sub> = 25 °C UNLESS OTHERWISE NOTED)						
Parameter	Symbol	Test Condition	Min	Typ <sup>a</sup>	Max	Unit
<b>Static</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250 μA	-1.0	-2.0	-3.0	V
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ±20 V			±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = -30 V, V <sub>GS</sub> = 0 V			-1	μA
		V <sub>DS</sub> = -30 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 70 °C			-5	
On-State Drain Current <sup>b</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> ≤ -5 V, V <sub>GS</sub> = -10 V	-40			A
		V <sub>DS</sub> ≤ -5 V, V <sub>GS</sub> = -4.5 V	-10			
Drain-Source On-State Resistance <sup>b</sup>	r <sub>DS(on)</sub>	V <sub>GS</sub> = -10 V, I <sub>D</sub> = -8.0 A		0.015	0.02	Ω
		V <sub>GS</sub> = -4.5 V, I <sub>D</sub> = -5.0 A		0.022	0.035	
Forward Transconductance <sup>b</sup>	g <sub>fs</sub>	V <sub>DS</sub> = -15 V, I <sub>D</sub> = -8.0 A		20		S
Diode Forward Voltage <sup>b</sup>	V <sub>SD</sub>	I <sub>S</sub> = -2.1 A, V <sub>GS</sub> = 0 V		-0.75	-1.2	V
<b>Dynamic<sup>a</sup></b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = -15 V, V <sub>GS</sub> = -10 V, I <sub>D</sub> = -4.6 A		47	60	nC
Gate-Source Charge	Q <sub>gs</sub>			9.5		
Gate-Drain Charge	Q <sub>gd</sub>			8		
Gate Resistance	R <sub>G</sub>			2.75	4.1	Ω
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = -15 V, R <sub>L</sub> = 15 Ω I <sub>D</sub> ≈ -1 A, V <sub>GEN</sub> = -10 V, R <sub>G</sub> = 6 Ω		16	30	ns
Rise Time	t <sub>r</sub>			17	30	
Turn-Off Delay Time	t <sub>d(off)</sub>			75	120	
Fall Time	t <sub>f</sub>			31	80	
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = -2.1 A, di/dt = 100 A/μs		40	80	

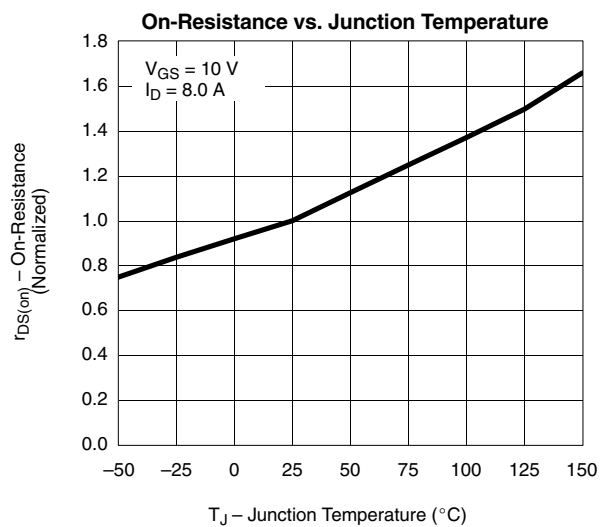
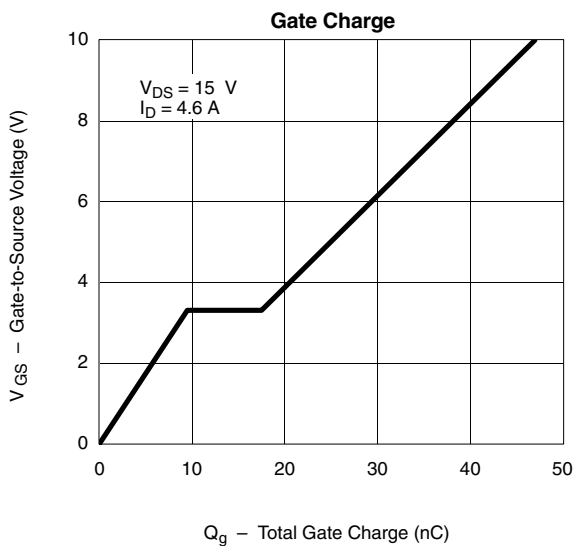
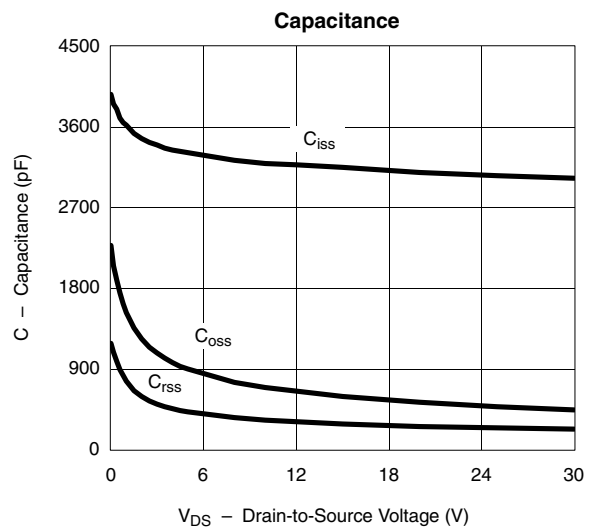
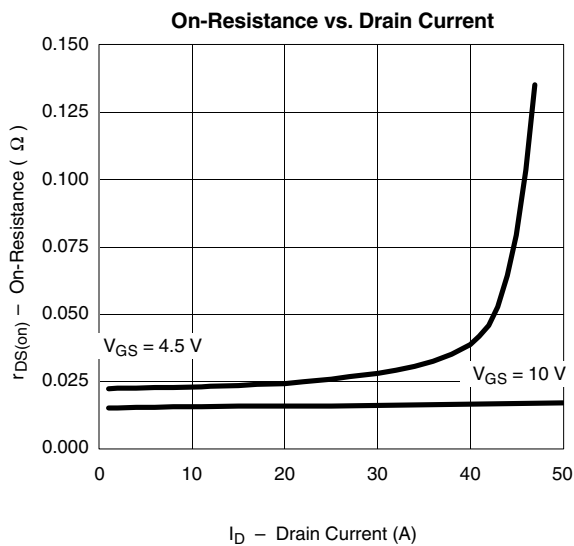
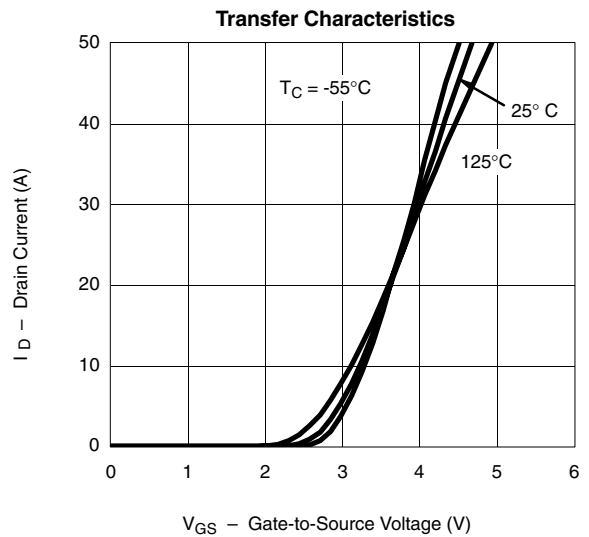
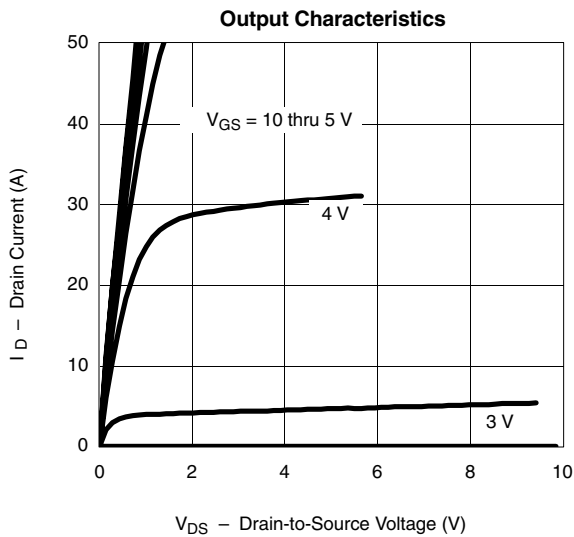
## Notes

- a. Guaranteed by design, not subject to production testing. Values shown are for Product Revision A.  
 b. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.

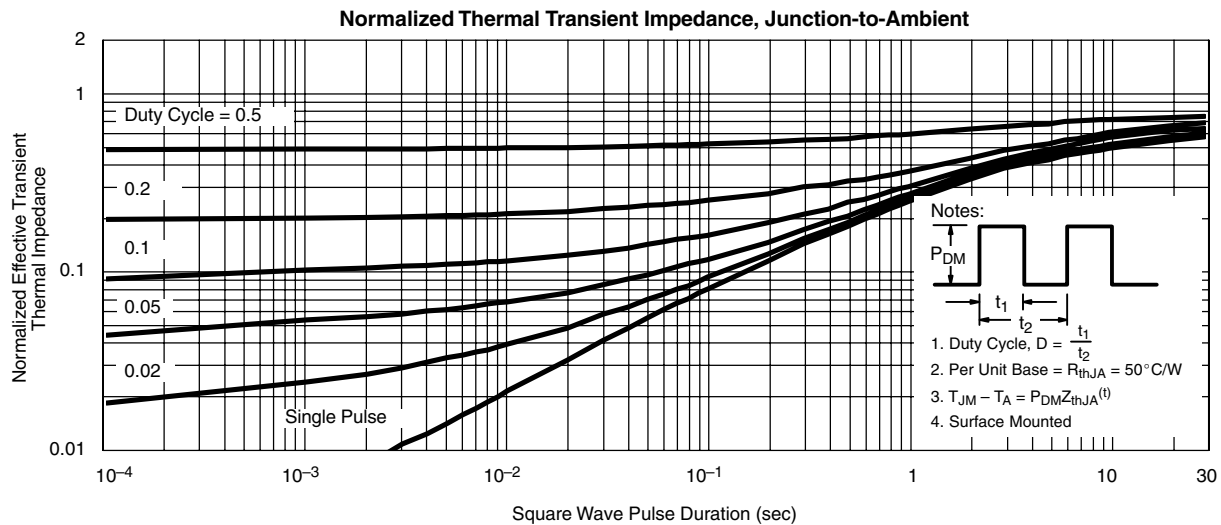
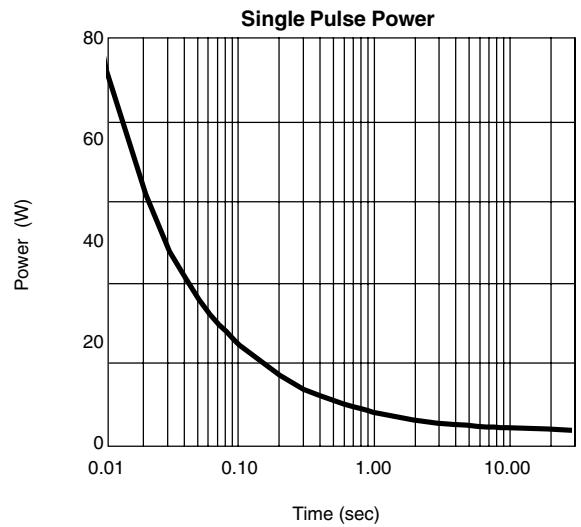
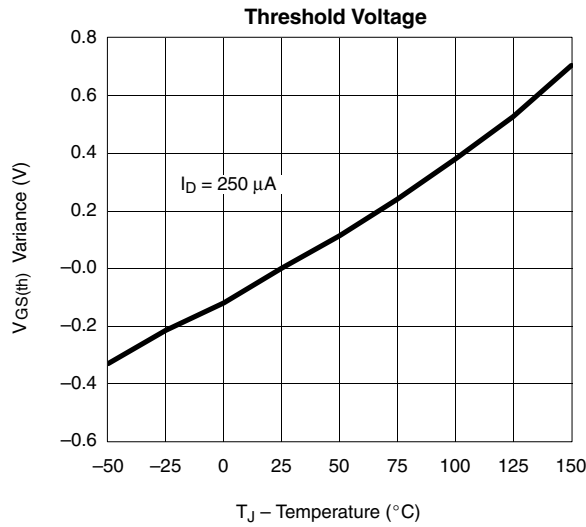
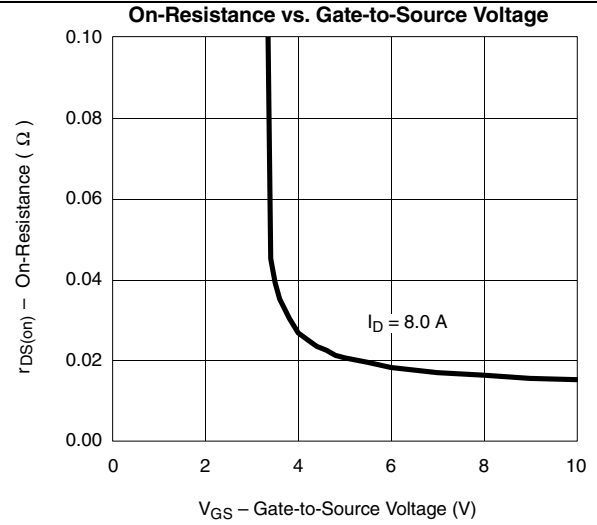
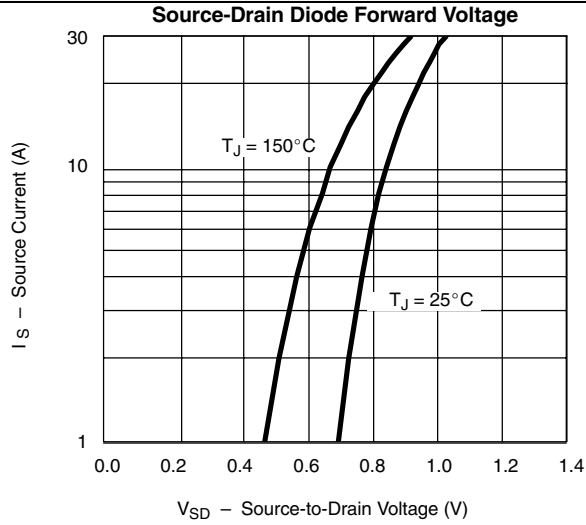
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



**TYPICAL CHARACTERISTICS, PRODUCT REVISION A (25°C UNLESS NOTED)**



### TYPICAL CHARACTERISTICS, PRODUCT REVISION A (25°C UNLESS NOTED)



Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <http://www.vishay.com/ppg?70149>.



## Notice

Specifications of the products displayed herein are subject to change without notice. Vishay Intertechnology, Inc., or anyone on its behalf, assumes no responsibility or liability for any errors or inaccuracies.

Information contained herein is intended to provide a product description only. No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document. Except as provided in Vishay's terms and conditions of sale for such products, Vishay assumes no liability whatsoever, and disclaims any express or implied warranty, relating to sale and/or use of Vishay products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright, or other intellectual property right.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications. Customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Vishay for any damages resulting from such improper use or sale.