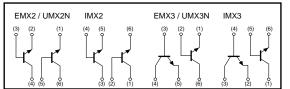
# General purpose (dual transistors)

## EMX2 / EMX3 / UMX2N / UMX3N / IMX2 / IMX3

#### Features

1) Two 2SC2412AK chips in a EMT or UMT or SMT package.

#### Equivalent circuits



#### Absolute maximum ratings (Ta=25°C)

	Parameter	Symbol Limits		Unit	
Collector-base voltage		Vсво	60	V	
Collector-emitter voltage		Vceo	50	V	
Emitter-base voltage		Vebo	7	V	
Collector current		lc	150	mA	
Collector power dissipation	EMX2 / EMX3 / UMX2N / UMX3N	ρ.	150(TOTAL)	mW *1 *2	
	IMX2 / IMX3	Pc	300(TOTAL)		
Junction temperature		Tj	150	°C	
Storage temperature		Tstg	-55~+150	°C	
*1 120mW per element	must not be exceeded				

\*2 200mW per element must not be exceeded.

#### •Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	60	-	-	V	Ic=50μA
Collector-emitter breakdown voltage	BVCEO	50	-	-	V	Ic=1mA
Emitter-base breakdown voltage	ВVево	7	-	-	V	Iε=50μA
Collector cutoff current	Ісво	-	-	0.1	μΑ	Vcb=60V
Emitter cutoff current	Іево	-	-	0.1	μΑ	VEB=7V
Collector-emitter saturation voltage	VCE(sat)	-	-	0.4	V	Ic/IB=50mA/5mA
DC current transfer ratio	hfe	120	-	560	-	Vce=6V, Ic=1mA
Transition frequency	fτ	-	180	-	MHz	Vce=12V, Ie=-2mA, f=100MHz *
Output capacitance	Cob	-	2	3.5	pF	Vcb=12V, IE=0mA, f=1kHz

\*Transition frequency of the device.

#### •Package, marking, and packaging specifications

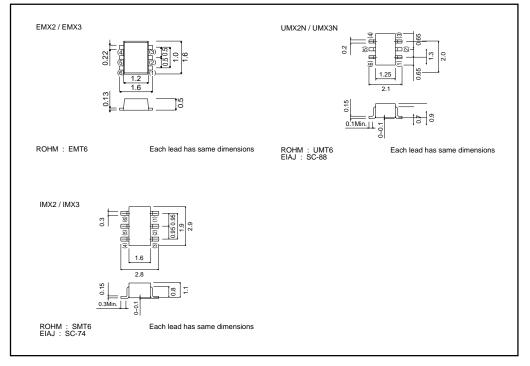
Туре	EMX2	EMX3	UMX2N	UMX3N	IMX2	IMX3
Package	EMT6	EMT6	UMT6	UMT6	SMT6	SMT6
Marking	X2	X3	X2	X3	X2	X3
Code	T2R	T2R	TR	TR	T108	T108
Basic ordering unit (pieces)	8000	8000	3000	3000	3000	3000

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### EMX2 / EMX3 / UMX2N / UMX3N / IMX2 / IMX3

#### Transistors

#### • External dimensions (Units : mm)



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