# 2981 AND 2982 

## 8-CHANNEL SOURCE DRIVERS



Note that the UDN2980A series (dual in-line package) and UDN2980LW series (small-outline IC package) are electrically identical and share a common terminal number assignment.


Recommended for high-side switching applications that benefit from separate logic and load grounds, these devices encompass load supply voltages to 50 V and output currents to -500 mA . These 8 -channel source drivers are useful for interfacing between low-level logic and high-current loads. Typical loads include relays, solenoids, lamps, stepper and/or servo motors, print hammers, and LEDs.

All devices may be used with 5 V logic systems - TTL, Schottky TTL, DTL, and 5 V CMOS. The UDN2981A, UDN2982A, and A2982SLW are electrically interchangeable, will withstand a maximum output off voltage of 50 V , and operate to a minimum of 5 V . All devices in this series integrate input current limiting resistors and output transient suppression diodes, and are activated by an active high input.

The suffix 'A' (all devices) indicates an 18-lead plastic dual in-line package with copper lead frame for optimum power dissipation. Under normal operating conditions, these devices will sustain 120 mA continuously for each of the eight outputs at an ambient temperature of $+50^{\circ} \mathrm{C}$ and a supply of 15 V .

The suffix 'LW' (UDN2982LW only) indicates an 18-lead surfacemountable wide-body SOIC package; the A2982SLW is provided in a 20-lead wide-body SOIC package with improved thermal characteristics.

The UDN2982A, UDN2982LW, and A2982SLW drivers are also available for operation over an extended temperature range to $-40^{\circ} \mathrm{C}$. To order, change the prefix 'UDN' to 'UDQ' or the suffix 'SLW' to 'ELW'. These packages are available in Pb (lead) free variants (suffix '-T'), with $100 \%$ matte-tin leadframe plating.

## FEATURES

- TTL, DTL, PMOS, or CMOS Compatible Inputs
- 500 mA Output Source Current Capability
- Transient-Protected Outputs
- Output Breakdown Voltage to 50 V
- DIP or SOIC Packaging

Always order by complete part number, e.g., UDN2981A-T. Note that all devices are not available in all package styles.

## 2981 THRU 2984 <br> 8-CHANNEL SOURCE DRIVERS

Selection Guide

| Part Number | Pb-free ${ }^{1}$ | Package | Packing | Ambient Temperature ( $\left.{ }^{\circ} \mathrm{C}\right)$ |
| :---: | :---: | :---: | :---: | :---: |
| A2982ELW-T | Yes | 20-pin SOICW | 37 per tube | -40 to 85 |
| A2982ELWTR-T | Yes | 20-pin SOICW | 1000 per reel | -40 to 85 |
| A2982SLW-T | Yes | 20-pin SOICW | 37 per tube | -20 to 85 |
| A2982SLWTR-T | Yes | 20-pin SOICW | 1000 per reel | -20 to 85 |
| UDN2981A-T | Yes | 18-pin DIP | 21 per tube | -20 to 85 |
| UDN2982A-T | Yes | 18-pin DIP | 21 per tube | -20 to 85 |
| UDN2982LW-T | Yes | 18-pin SOICW | 41 per tube | -20 to 85 |
| UDN2982LWTR-T | Yes | 18-pin SOICW | 1000 per reel | -20 to 85 |
| UDQ2982LW-T | Yes | 18-pin SOICW | 41 per tube | -40 to 85 |
| UDQ2982LWTR-T | Yes | 18-pin SOICW | 1000 per reel | -40 to 85 |

${ }^{1} \mathrm{~Pb}$-based variants are being phased out of the product line. Some variants cited in this footnote are in production but have been determined to be LAST TIME BUY or NOT FOR NEW DESIGN. This classification indicates that sale of this device is currently restricted to existing customer applications. The variants should not be purchased for new design applications because obsolescence in the near future is probable. Samples are no longer available. For LAST TIME BUY: status change: October 31, 2006. Deadline for receipt of LAST TIME BUY orders: April 27, 2007. These variants include: A2982ELW, A2982ELWTR, A2982SLW, A2982SLWTR, UDN2981A, and UDN2982A. For NOT FOR NEW DESIGN: status change: May 1, 2006. These variants include: UDN2982LW and UDN2982LWTR.

## 2981 and 2982 <br> 8-CHANNEL <br> SOURCE DRIVERS

One of Eight Drivers


115 Northeast C utoff, B ox 15036
Worcester, Massachusetts 01615-0036 (508) 853-5000
Copyright © 1977, 2004 Allegro MicroS ystems, Inc.

## ELECTRICAL CHARACTERISTICS at $\mathbf{T}_{\mathrm{A}} \mathbf{=} \boldsymbol{+ 2 5}^{\circ} \mathrm{C}$ (unless otherwise specified).

| Characteristic | Symbol | Applicable Devices | Test Conditions | Test <br> Fig. | Limits |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  | Min. | Typ. | Max. | Units |
| Output Leakage Current | $I_{\text {CEX }}$ | All | $\mathrm{V}_{\text {IN }}=0.4 \mathrm{~V}^{*}, \mathrm{~V}_{\mathrm{S}}=50 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=+70^{\circ} \mathrm{C}$ | 1 | - | - | 200 | $\mu \mathrm{A}$ |
| Output Sustaining Voltage | $\mathrm{V}_{\text {CE(SUS) }}$ | All | $\mathrm{I}_{\text {OUT }}=-45 \mathrm{~mA}$ | - | 35 | - | - | V |
| Collector-Emitter <br> Saturation Voltage | $\mathrm{V}_{\text {CE(SAT) }}$ | All | $\begin{aligned} & \mathrm{V}_{\text {IN }}=2.4 \mathrm{~V}, \mathrm{I}_{\text {OUT }}=-100 \mathrm{~mA} \\ & \mathrm{~V}_{\text {IN }}=2.4 \mathrm{~V}, \mathrm{I}_{\text {OUT }}=-225 \mathrm{~mA} \\ & \mathrm{~V}_{\text {IN }}=2.4 \mathrm{~V}, \mathrm{I}_{\text {OUT }}=-350 \mathrm{~mA} \end{aligned}$ | $\begin{aligned} & 2 \\ & 2 \\ & 2 \end{aligned}$ | $\begin{aligned} & - \\ & - \\ & \hline \end{aligned}$ | $\begin{aligned} & 1.6 \\ & 1.7 \\ & 1.8 \end{aligned}$ | $\begin{aligned} & 1.8 \\ & 1.9 \\ & 2.0 \end{aligned}$ | $\begin{aligned} & V \\ & V \\ & V \end{aligned}$ |
| Input Current | $\mathrm{I}_{\text {IN(ON) }}$ | UDN2981A | $\begin{aligned} & \mathrm{V}_{\mathbb{I N}}=2.4 \mathrm{~V} \\ & \mathrm{~V}_{\mathrm{IN}}=3.85 \mathrm{~V} \end{aligned}$ | $\begin{aligned} & 3 \\ & 3 \end{aligned}$ | — | $\begin{aligned} & 140 \\ & 310 \end{aligned}$ | $\begin{aligned} & 200 \\ & 450 \end{aligned}$ | $\mu \mathrm{A}$ $\mu \mathrm{A}$ |
|  |  | 2982† | $\begin{aligned} & \mathrm{V}_{\mathrm{IN}}=2.4 \mathrm{~V} \\ & \mathrm{~V}_{\mathrm{IN}}=12 \mathrm{~V} \end{aligned}$ | $\begin{aligned} & 3 \\ & 3 \end{aligned}$ |  | $\begin{aligned} & 140 \\ & 1.25 \\ & \hline \end{aligned}$ | $\begin{aligned} & 200 \\ & 1.93 \end{aligned}$ | $\mu \mathrm{A}$ <br> mA |
| Output Source Current (Outputs Open) | Iout | UDN2981A | $\mathrm{V}_{\text {IN }}=2.4 \mathrm{~V}, \mathrm{~V}_{\mathrm{CE}}=2.0 \mathrm{~V}$ | 2 | -350 | - | - | mA |
|  |  | 2982† | $\mathrm{V}_{\text {IN }}=2.4 \mathrm{~V}, \mathrm{~V}_{\mathrm{CE}}=2.0 \mathrm{~V}$ | 2 | -350 | - | - | mA |
| Supply Current <br> Leakage Current | $I_{S}$ | All | $\mathrm{V}_{\text {IN }}=2.4 \mathrm{~V}^{*}, \mathrm{~V}_{\mathrm{S}}=50 \mathrm{~V}$ | 4 | - | - | 10 | mA |
| Clamp Diode | $\mathrm{I}_{\mathrm{R}}$ | All | $\mathrm{V}_{\mathrm{R}}=50 \mathrm{~V}, \mathrm{~V}_{\mathrm{IN}}=0.4 \mathrm{~V}^{*}$ | 5 | - | - | 50 | $\mu \mathrm{A}$ |
| Clamp Diode | $V_{F}$ | All | $\mathrm{I}_{\mathrm{F}}=350 \mathrm{~mA}$ | 6 | - | 1.5 | 2.0 | V |
| Turn-On Delay | $\mathrm{t}_{\mathrm{ON}}$ | All | $\begin{aligned} & 0.5 \mathrm{E}_{\mathrm{IN}} \text { to } 0.5 \mathrm{E}_{\text {OUT }}, \mathrm{R}_{\mathrm{L}}=100 \Omega, \\ & \mathrm{~V}_{\mathrm{S}}=35 \mathrm{~V} \end{aligned}$ | - | - | 0.3 | 2.0 | $\mu \mathrm{s}$ |
| Turn-Off Delay | $\mathrm{t}_{\text {OFF }}$ | All | $0.5 \mathrm{E}_{\mathrm{IN}}$ to $0.5 \mathrm{E}_{\text {OUT }}, \mathrm{R}_{\mathrm{L}}=100 \Omega$, $\mathrm{V}_{\mathrm{S}}=35 \mathrm{~V}$, See Note | - | - | 2.0 | 10 | $\mu \mathrm{s}$ |
|  |  |  |  |  |  |  |  |  |

NOTES: Turn-off delay is influenced by load conditions. Systems applications well below the specified output loading may require timing considerations for some designs, i.e., multiplexed displays or when used in combination with sink drivers in a totem pole configuration.
All unused inputs must be connected to ground. Pulldown resistors ( $\approx 10 \mathrm{k} \Omega$ ) are recommended for inputs that are allowed to float while power is being applied to $\mathrm{V}_{\mathrm{S}}$.
Negative current is defined as coming out of (sourcing) the specified device terminal.

* All inputs simultaneously.
$\dagger$ Complete part number includes a prefix (A or UDN) and a suffix (A or SLW) as follows:
UDN2981A, UDN2982A, UDN2982LW, or A2982SLW.


## TEST FIGURES

Figure 1


Figure 2


Figure 5


Figure 4


Figure 3


> 2981 and 2982
> 8-CHANNEL SOURCE DRIVERS

## Allowable peak collector current as a function of duty cycle

Series UDN2980A


UDN2981A and UDN2982A



Dwg. No. A-11,108B

2981 and 2982
8-CHANNEL
SOURCE DRIVERS


Typical electrosensitive printer application


Dwg. No. A-11,113A

> 2981 and 2982
> 8-CHANNEL SOURCE DRIVERS

## UDN2981A and UDN2982A

Dimensions in Inches
(controlling dimensions)


Dimensions in Millimeters
(for reference only)


NOTES: 1. Exact body and lead configuration at vendor's option within limits shown.
2. Lead spacing tolerance is non-cumulative.
3. Lead thickness is measured at seating plane or below.
4. Supplied in standard sticks/tubes of 21 devices.

UDN2982LW
(add "TR" to part number for tape and reel) Dimensions in Inches
(for reference only)


Dwg. MA-008-18A in

## Dimensions in Millimeters

(controlling dimensions)


Dwg. MA-008-18A mm

NOTES: 1. Exact body and lead configuration at vendor's option within limits shown.
2. Lead spacing tolerance is non-cumetive.
3. Supplied in standard sticks/tubes of 41 devices or add "TR" to part number for tape and reel.


NOTES: 1. Exact body and lead configuration at vendor's option within limits shown.
2. Lead spacing tolerance is non-cumulative.
3. Supplied in standard sticks/tubes of 37 devices or add "TR" to part number for tape and reel.

## 2981 and 2982 <br> 8-CHANNEL <br> SOURCE DRIVERS

The products described here are manufactured under one or more U.S. patents or U.S. patents pending.

Allegro MicroSystems, Inc. reserves the right to make, from time to time, such departures from the detail specifications as may be required to permit improvements in the performance, reliability, or manufacturability of its products. Before placing an order, the user is cautioned to verify that the information being relied upon is current.

Allegro products are not authorized for use as critical components in life-support devices or systems without express written approval.

The information included herein is believed to be accurate and reliable. However, Allegro MicroSystems, Inc. assumes no responsibility for its use; nor for any infringement of patents or other rights of third parties which may result from its use.

