

EL2180C/EL2280C/EL2480C-Preliminary 250MHz / 3mA Current Mode Feedback Amplifiers

# Features

- Single, dual, and quad topologies
- 3mA supply current (per amplifier)
- 250MHz -3dB bandwidth
- 1200V/µs slew rate
- Tiny package package options (SOT23, LPP)
- Low cost
- Single- and dual-supply operation down to  $\pm 1.5V$
- + 0.05%/0.05° diff. gain/diff. phase into  $150\Omega$

# Applications

- Low power/battery applications
- HDSL amplifiers
- · Video amplifiers
- Cable drivers
- RGB amplifiers
- · Test equipment amplifiers
- · Current to voltage converters

#### **Ordering Information** Tape & Reel Part No Package Outline # EL2180CN 8-Pin PDIP MDP0031 EL2180CS 8-Pin SO MDP0027 EL2180CS-T MDP0027 8-Pin SO EL2180CS-T13 MDP0027 8-Pin SO 13' EL2180CW-T7 5-Pin SOT23 7" MDP0038 EL2180CW-T13 5-Pin SOT23 13 MDP0038 EL2280CN 8-Pin PDIP MDP0031 EL2280CS 8-Pin SO MDP0027 MDP0027 EL2280CS-T7 8-Pin SO EL2280CS-T13 8-Pin SO 13" MDP0027 EL2480CN 14-Pin PDIP MDP0031 EL2480CS 14-Pin SO MDP0027 EL2480CS-T7 14-Pin SO MDP0027 EL2480CS-T13 14-Pin SO 13' MDP0027 EL2480CL 24-Pin LPP MDP0046 24-Pin LPP MDP0046 EL2480CL-T7 EL2480CL-T13 24-Pin LPP 13' MDP0046

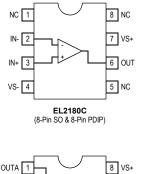
# **General Description**

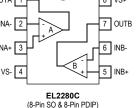
The EL2180C/EL2280C/EL2480C are single/dual/quad current-feedback operational amplifiers that achieve a -3dB bandwidth of 250MHz at a gain of +1 while consuming only 3mA of supply current per amplifier. They will operate with dual supplies ranging from  $\pm 1.5V$  to  $\pm 6V$  or from single supplies ranging from +3V to  $\pm 12V$ . In spite of their low supply current, the EL2480C and the EL2280C can output 55mA while swinging to  $\pm 4V$  on  $\pm 5V$  supplies. The EL2180C can output 100mA with similar output swings. These attributes make the EL2180C/EL2280C/EL2480C excellent choices for low power and/or low voltage cable driver, HDSL, or RGB applications.

For applications where board space is extremely critical, the EL2180C is available in the tiny 5-pin SOT23 package, with a footprint size 28% of an 8-pin SO. The EL2480C is also available in a 24-pin LPP package. All are specified for operation over the full  $-40^{\circ}$ C to  $+85^{\circ}$ C temperature range.

Single, dual, and triple versions are also available with the enable function (EL2186C, EL2286C, and EL2386C).

# **Connection Diagrams**





July 19, 200

Note: All information contained in this data sheet has been carefully checked and is believed to be accurate as of the date of publication; however, this data sheet cannot be a "controlled document". Current revisions, if any, to these specifications are maintained at the factory and are available upon your request. We recommend checking the revision level before finalization of your design documentation.

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250MHz / 3mA Current Mode Feedback Amplifiers

# Absolute Maximum Ratings (T<sub>A</sub> = 25°C)

| Supply Voltage between VS+ and GND  | +12.6V                               | Operating Junction Temperature |                 |
|-------------------------------------|--------------------------------------|--------------------------------|-----------------|
| Voltage between VS+ and VS-         | +12.6V                               | Plastic Packages               | 150°C           |
| Common-Mode Input Voltage           | V <sub>S</sub> - to V <sub>S</sub> + | Output Current (EL2180C)       | ±120mA          |
| Differential Input Voltage          | $\pm 6V$                             | Output Current (EL2280C)       | ±60mA           |
| Current into +IN or -IN             | ±7.5mA                               | Output Current (EL2480C)       | ±60mA           |
| Internal Power Dissipation          | See Curves                           | Storage Temperature Range      | -65°C to +150°C |
| Operating Ambient Temperature Range | -40°C to +85°C                       |                                |                 |

Important Note:

Al parameters having Min/Max specifications are guaranteed. Typ values are for information purposes only. Unless otherwise noted, all tests are at the specified temperature and are pulsed tests, therefore:  $T_J = T_C = T_A$ .

## **DC Electrical Characteristics**

 $V_S = \pm 5V$ ,  $R_L = 150\Omega$ ,  $T_A = 25^{\circ}C$  unless otherwise specified.

| Parameter           | Description                           | Conditions   | Min  | Тур  | Max | Unit  |
|---------------------|---------------------------------------|--|------|------|-----|-------|
| V <sub>OS</sub>     | Input Offset Voltage                  |  |      | 2.5  | 10  | mV    |
| TCVOS               | Average Input Offset Voltage Drift    | Measured from T <sub>MIN</sub> to T <sub>MAX</sub> |      | 5    |     | μV/°C |
| dV <sub>OS</sub>    | V <sub>OS</sub> Matching              | EL2280C, EL2480C only                              |      | 0.5  |     | mV    |
| +I <sub>IN</sub>    | +Input Current                        |  |      | 1.5  | 15  | μΑ    |
| d+I <sub>IN</sub>   | +I <sub>IN</sub> Matching             | EL2280C, EL2480C only                              |      | 20   |     | nA    |
| -I <sub>IN</sub>    | -Input Current                        |  |      | 16   | 40  | μΑ    |
| d-I <sub>IN</sub>   | -I <sub>IN</sub> Matching             | EL2280C, EL2480C only                              |      | 2    |     | μΑ    |
| CMRR                | Common Mode Rejection Ratio           | $V_{CM} = \pm 3.5 V$                               | 45   | 50   |     | dB    |
| -ICMR               | -Input Current Common Mode Rejection  | $V_{CM} = \pm 3.5 V$                               |      | 5    | 30  | μA/V  |
| PSRR                | Power Supply Rejection Ratio          | $V_S$ is moved from $\pm 4V$ to $\pm 6V$           | 60   | 70   |     | dB    |
| -IPSR               | -Input Current Power Supply Rejection | $V_S$ is moved from $\pm 4V$ to $\pm 6V$           |      | 1    | 15  | μA/V  |
| ROL                 | Transimpedance                        | $V_{OUT} = \pm 2.5 V$                              | 120  | 300  |     | kΩ    |
| +R <sub>IN</sub>    | +Input Resistance                     | $V_{CM} = \pm 3.5 V$                               | 0.5  | 2    |     | MΩ    |
| +CIN                | +Input Capacitance                    |  |      | 1.2  |     | pF    |
| CMIR                | Common Mode Input Range               |  | ±3.5 | ±4.0 |     | V     |
| Vo                  | Output Voltage Swing                  | $V_S = \pm 5$                                      | ±3.5 | ±4.0 |     | V     |
|                     |                                       | $V_S = +5$ Single-supply, high                     |      | 4.0  |     | V     |
|                     |                                       | $V_S = +5$ Single-supply, low                      |      | 0.3  |     | V     |
| I <sub>O</sub> Outŗ | Output Current                        | EL2180C only                                       | 80   | 100  |     | mA    |
|                     |                                       | EL2280C only, per amplifier                        | 50   | 55   |     | mA    |
|                     |                                       | EL2480C only, per amplifier                        | 50   | 55   |     | mA    |
| Is                  | Supply Current                        | Per amplifier                                      |      | 3    | 6   | mA    |

# **AC Electrical Characteristics**

 $V_S = \pm 5V$ ,  $R_F = R_G = 750\Omega$  for PDIP and SO packages,  $R_F = R_G = 560\Omega$  for SOT23-5 package,  $R_L = 150\Omega$ ,  $T_A = 25^{\circ}C$  unless otherwise specified

| Description        | Conditions   | Min   | Тур   | Max   | Unit  |
|--------------------|--|---|---|---|---|
| -3dB Bandwidth     | $A_{V} = +1$   |   | 250   |   | MHz   |
| -3dB Bandwidth     | $A_V = +2$   |   | 180   |   | MHz   |
| 0.1dB Bandwidth    | $A_V = +2$   |   | 50  |   | MHz   |
| Slew Rate          | $V_{OUT} = \pm 2.5 V, A_V = +2$                                  | 600   | 1200  |   | V/µs  |
| Rise and Fall Time | $V_{OUT} = \pm 500 \text{ mV}$                                   |   | 1.5   |   | ns  |
|                    | -3dB Bandwidth<br>-3dB Bandwidth<br>0.1dB Bandwidth<br>Slew Rate | -3dB Bandwidth $A_V = +1$ -3dB Bandwidth $A_V = +2$ 0.1dB Bandwidth $A_V = +2$ Slew Rate $V_{OUT} = \pm 2.5V, A_V = +2$ | -3dB Bandwidth $A_V = +1$ -3dB Bandwidth $A_V = +2$ 0.1dB Bandwidth $A_V = +2$ Slew Rate $V_{OUT} = \pm 2.5V, A_V = +2$ | -3dB Bandwidth $A_V = +1$ 250         -3dB Bandwidth $A_V = +2$ 180         0.1dB Bandwidth $A_V = +2$ 50         Slew Rate $V_{OUT} = \pm 2.5V, A_V = +2$ 600       1200 | -3dB Bandwidth $A_V = +1$ $A_V = 250$ -3dB Bandwidth $A_V = +2$ 180       0.1dB Bandwidth $A_V = +2$ 50       Slew Rate $V_{OUT} = \pm 2.5V, A_V = +2$ 600     1200 |

250MHz / 3mA Current Mode Feedback Amplifiers

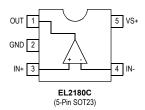
# **AC Electrical Characteristics**

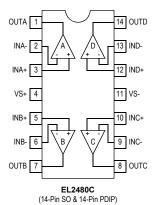
 $V_S=\pm 5V, R_F=R_G=750\Omega \text{ for PDIP and SO packages}, R_F=R_G=560\Omega \text{ for SOT23-5 package}, R_L=150\Omega, T_A=25^\circ C \text{ unless otherwise specified}$ 

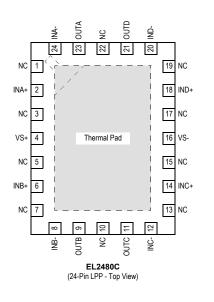
| Parameter | Description        | Conditions                                 | Min | Тур  | Max | Unit |
|-----------|--------------------|--|-----|------|-----|------|
| tpD       | Propagation Delay  | $V_{OUT} = \pm 500 \text{ mV}$             |     | 1.5  |     | ns   |
| OS        | Overshoot          | $V_{OUT} = \pm 500 \text{ mV}$             |     | 3.0  |     | %    |
| ts        | 0.1% Settling      | $V_{OUT} = \pm 2.5 V, A_V = -1$            |     | 15   |     | ns   |
| dG        | Differential Gain  | $A_V = +2, R_L = 150\Omega^{[1]}$          |     | 0.05 |     | %    |
| dP        | Differential Phase | $A_V = +2, R_L = 150\Omega$ <sup>[1]</sup> |     | 0.05 |     | 0    |
| dG        | Differential Gain  | $A_V = +1, R_L = 500\Omega$ <sup>[1]</sup> |     | 0.01 |     | %    |
| dP        | Differential Phase | $A_V = +1, R_L = 500\Omega$ <sup>[1]</sup> |     | 0.01 |     | 0    |
| Cs        | Channel Separation | EL2280C, EL2480C only, f = 5 MHz           |     | 85   |     | dB   |

1. DC offset from 0V to 0.714V, AC amplitude 286mV<sub>P-P</sub>, f = 3.58MHz

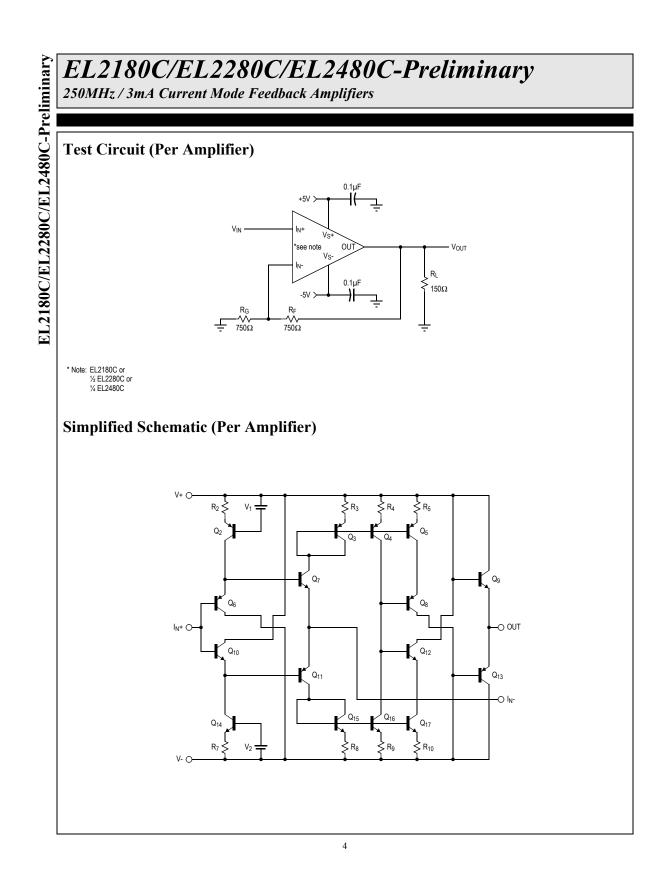
# **Connection Diagrams (Continued)**

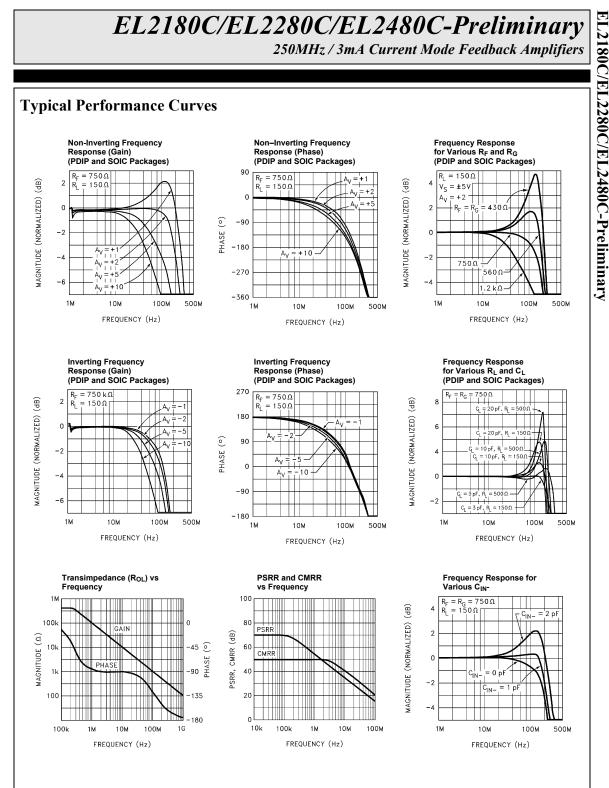


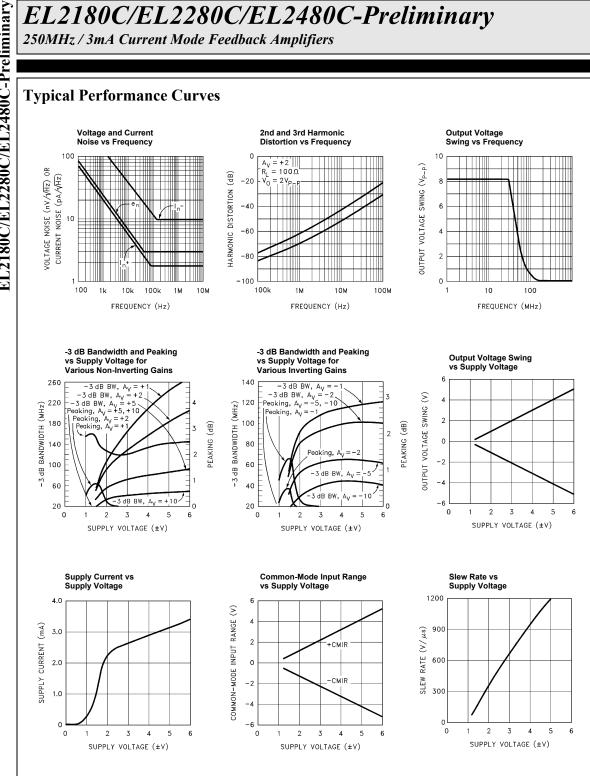


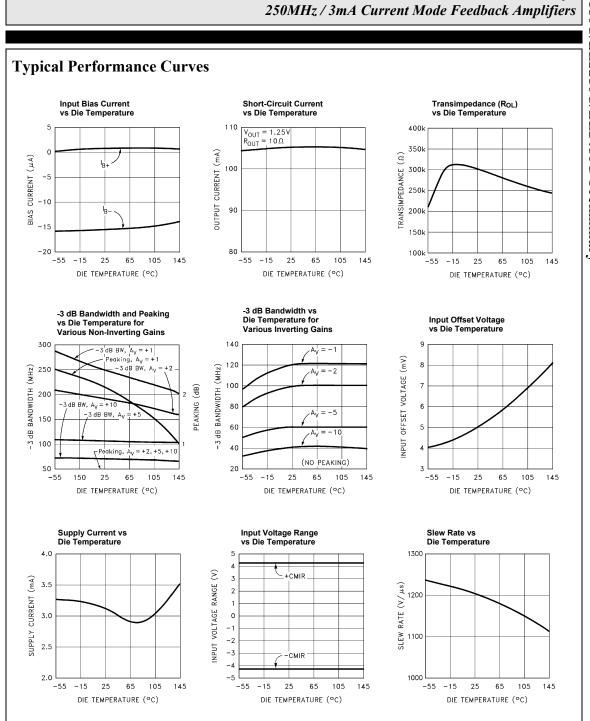


EL2180C/EL2280C/EL2480C-Preliminary

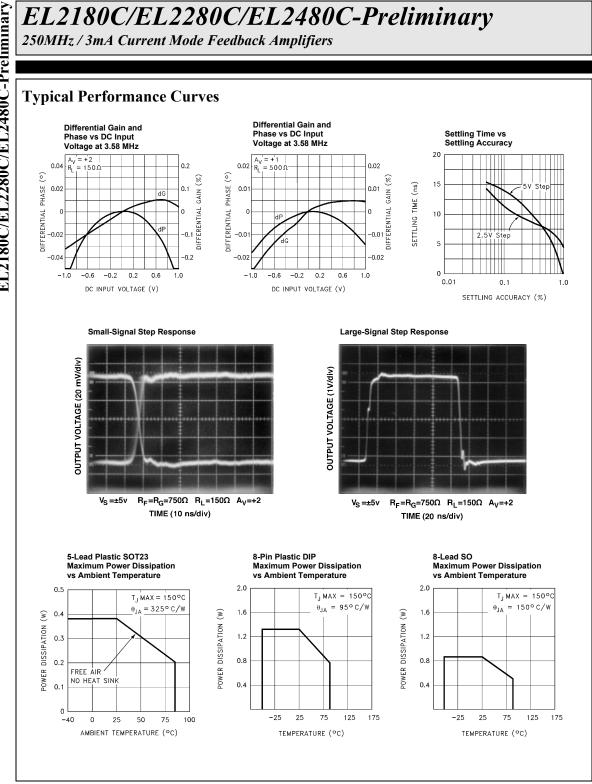


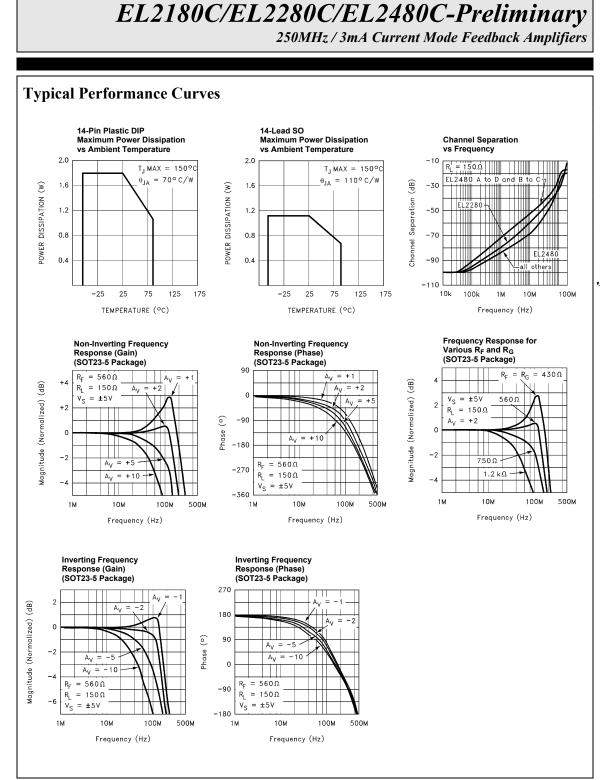






# EL2180C/EL2280C/EL2480C-Preliminary





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250MHz / 3mA Current Mode Feedback Amplifiers

# **Applications Information**

# **Product Description**

The EL2180C/EL2280C/EL2480C are current-feedback operational amplifiers that offer a wide -3dB bandwidth of 250MHz and a low supply current of 3mA per amplifier. All of these products also feature high output current drive. The EL2180C can output 100mA, while the EL2280C and the EL2480C can output 55mA per amplifier. The EL2180C/EL2280C/EL2480C work with supply voltages ranging from a single 3V to  $\pm 6V$  and they are also capable of swinging to within 1V of either supply on the input and the output. Because of their current-feedback topology, the EL2180C/EL2280C/ EL2480C do not have the normal gain-bandwidth product associated with voltage-feedback operational amplifiers. This allows their -3dB bandwidth to remain relatively constant as closed-loop gain is increased. This combination of high bandwidth and low power, together with aggressive pricing make the EL2180C/EL2280C/ EL2480C the ideal choice for many low-power/highbandwidth applications such as portable computing, HDSL, and video processing.

For applications where board space is extremely critical, the EL2180C is available in the tiny 5-pin SOT23 package, which has a footprint 28% the size of an 8-pin SO. The EL2480C is available in the 24-pin LPP package, offering board space savings and better power dissipation compared to the SO and PDIP packages. The EL2180C/EL2280C/EL2480C are each also available in industry-standard pinouts in PDIP and SO packages.

For single, dual, and triple applications with disable, consider the EL2186C (8-pin single), EL2286C (14-pin dual), and EL2386C (16-pin triple). If lower power is required, refer to the EL2170C/EL2176C family which provides singles, duals, and quads with 70MHz of bandwidth while consuming 1mA of supply current per amplifier.

# Power Supply Bypassing and Printed Circuit Board Layout

As with any high-frequency device, good printed circuit board layout is necessary for optimum performance. Ground plane construction is highly recommended. Lead lengths should be as short as possible. The power supply pins must be well bypassed to reduce the risk of oscillation. The combination of a  $4.7\mu$ F tantalum capacitor in parallel with a  $0.1\mu$ F capacitor has been shown to work well when placed at each supply pin.

For good AC performance, parasitic capacitance should be kept to a minimum especially at the inverting input (see the Capacitance at the Inverting Input section). Ground plane construction should be used, but it should be removed from the area near the inverting input to minimize any stray capacitance at that node. Carbon or Metal-Film resistors are acceptable with the Metal-Film resistors giving slightly less peaking and bandwidth because of their additional series inductance. Use of sockets, particularly for the SO package, should be avoided if possible. Sockets add parasitic inductance and capacitance which will result in some additional peaking and overshoot.

# **Capacitance at the Inverting Input**

Any manufacturer's high-speed voltage- or current-feedback amplifier can be affected by stray capacitance at the inverting input. For inverting gains this parasitic capacitance has little effect because the inverting input is a virtual ground, but for non-inverting gains this capacitance (in conjunction with the feedback and gain resistors) creates a pole in the feedback path of the amplifier. This pole, if low enough in frequency, has the same destabilizing effect as a zero in the forward openloop response. The use of large value feedback and gain resistors further exacerbates the problem by further lowering the pole frequency.

The experienced user with a large amount of PC board layout experience may find in rare cases that the EL2180C/EL2280C/EL2480C have less bandwidth than expected.

The reduction of feedback resistor values (or the addition of a very small amount of external capacitance at the inverting input, e.g. 0.5pF) will increase bandwidth as desired. Please see the curves for Frequency Response for Various  $R_F$  and  $R_G$ , and Frequency Response for Various  $C_{IN}$ -.

# EL2180C/EL2280C/EL2480C-Preliminary 250MHz / 3mA Current Mode Feedback Amplifiers

## **Feedback Resistor Values**

The EL2180C/EL2280C/EL2480C have been designed and specified at gains of +1 and +2 with  $R_F = 750\Omega$  in PDIP and SO packages and  $R_F = 560\Omega$  in 5-pin SOT23 package. These values of feedback resistors give 250MHz of -3dB bandwidth at  $A_V = +1$  with about 2.5dB of peaking, and 180MHz of -3dB bandwidth at  $A_V = +2$  with about 0.1dB of peaking. The 5-pin SOT23 package is characterized with a smaller value of feedback resistor, for a given bandwidth, to compensate for lower parasitics within both the package itself and the printed circuit board where it will be placed. Since the EL2180C/EL2280C/EL2480C are current-feedback amplifiers, it is also possible to change the value of RF to get more bandwidth. As seen in the curve of Frequency Response For Various R<sub>F</sub> and R<sub>G</sub>, bandwidth and peaking can be easily modified by varying the value of the feedback resistor.

Because the EL2180C/EL2280C/EL2480C are currentfeedback amplifiers, their gain-bandwidth product is not a constant for different closed-loop gains. This feature actually allows the EL2180C/EL2280C/EL2480C to maintain about the same -3dB bandwidth, regardless of closed-loop gain. However, as closed-loop gain is increased, bandwidth decreases slightly while stability increases. Since the loop stability is improving with higher closed-loop gains, it becomes possible to reduce the value of  $R_F$  below the specified 560 $\Omega$  and 750 $\Omega$  and still retain stability, resulting in only a slight loss of bandwidth with increased closed-loop gain.

# Supply Voltage Range and Single-Supply Operation

The EL2180C/EL2280C/EL2480C have been designed to operate with supply voltages having a span of greater than 3V, and less than 12V. In practical terms, this means that the EL2180C/EL2280C/EL2480C will operate on dual supplies ranging from  $\pm 1.5$ V to  $\pm 6$ V. With a single-supply, the EL2180C/EL2280C/EL2480C will operate from  $\pm 3$ V to  $\pm 12$ V.

As supply voltages continue to decrease, it becomes necessary to provide input and output voltage ranges that can get as close as possible to the supply voltages. The EL2180C/EL2280C/EL2480C have an input voltage range that extends to within 1V of either supply. So, for example, on a single +5V supply, the EL2180C/ EL2280C/EL2480C have an input range which spans from 1V to 4V. The output range of the EL2180C/EL2280C/EL2480C is also quite large, extending to within 1V of the supply rail. On a  $\pm$ 5V supply, the output is therefore capable of swinging from -4V to +4V. Single-supply output range is even larger because of the increased negative swing due to the external pull-down resistor to ground. On a single +5V supply, output voltage range is about 0.3V to 4V.

#### Video Performance

For good video performance, an amplifier is required to maintain the same output impedance and the same frequency response as DC levels are changed at the output. This is especially difficult when driving a standard video load of 150 $\Omega$ , because of the change in output current with DC level. Until the EL2180C/EL2280C/EL2480C, good Differential Gain could only be achieved by running high idle currents through the output transistors (to reduce variations in output impedance). These currents were typically comparable to the entire 3mA supply current of each EL2180C/EL2280C/EL2480C amplifier! Special circuitry has been incorporated in the EL2180C/EL2280C/EL2480C to reduce the variation of output impedance with current output. This results in dG and dP specifications of 0.05% and 0.05° while driving 150 $\Omega$  at a gain of +2.

Video Performance has also been measured with a  $500\Omega$  load at a gain of +1. Under these conditions, the EL2180C/EL2280C/EL2480C have dG and dP specifications of 0.01% and 0.01° respectively while driving  $500\Omega$  at  $A_V = +1$ .

#### **Output Drive Capability**

In spite of its low 3mA of supply current, the EL2180C is capable of providing a minimum of  $\pm 80$ mA of output current. Similarly, each amplifier of the EL2280C and the EL2480C is capable of providing a minimum of  $\pm 50$ mA. These output drive levels are unprecedented in amplifiers running at these supply currents. With a minimum  $\pm 80$ mA of output drive, the EL2180C is capable of driving 50 $\Omega$  loads to  $\pm 4$ V, making it an excellent choice for driving isolation transformers in telecommunications applications. Similarly, the  $\pm 50$ mA minimum

250MHz / 3mA Current Mode Feedback Amplifiers

output drive of each EL2280C and EL2480C amplifier allows swings of  $\pm 2.5V$  into  $50\Omega$  loads.

#### **Driving Cables and Capacitive Loads**

When used as a cable driver, double termination is always recommended for reflection-free performance. For those applications, the back-termination series resistor will decouple the EL2180C/EL2280C/EL2480C from the cable and allow extensive capacitive drive. However, other applications may have high capacitive loads without a back-termination resistor. In these applications, a small series resistor (usually between  $5\Omega$  and  $50\Omega$ ) can be placed in series with the output to eliminate most peaking. The gain resistor (R<sub>G</sub>) can then be chosen to make up for any gain loss which may be created by this additional resistor at the output. In many cases it is also possible to simply increase the value of the feedback resistor (R<sub>F</sub>) to reduce the peaking.

#### **Current Limiting**

The EL2180C/EL2280C/EL2480C have no internal current-limiting circuitry. If any output is shorted, it is possible to exceed the Absolute Maximum Ratings for output current or power dissipation, potentially resulting in the destruction of the device.

#### **Power Dissipation**

With the high output drive capability of the EL2180C/EL2280C/EL2480C, it is possible to exceed the 150°C Absolute Maximum junction temperature under certain very high load current conditions. Generally speaking, when  $R_L$  falls below about 25 $\Omega$ , it is important to calculate the maximum junction temperature ( $T_{JMAX}$ ) for the application to determine if powersupply voltages, load conditions, or package type need to be modified for the EL2180C/EL2280C/EL2480C to remain in the safe operating area. These parameters are calculated as follows:

$$T_{JMAX} = T_{MAX} + (\Theta_{JA} \times n \times PD_{MAX})$$

where:

T<sub>MAX</sub> = Maximum Ambient Temperature

 $\theta_{IA}$  = Thermal Resistance of the Package

n = Number of Amplifiers in the Package

PD<sub>MAX</sub> = Maximum Power Dissipation of Each Amplifier in the Package

PD<sub>MAX</sub> for each amplifier can be calculated as follows:

$$PD_{MAX} = (2 \times V_S \times I_{SMAX}) + (V_S - V_{OUTMAX}) \times \frac{V_{OUTMAX}}{R_r}$$

where:

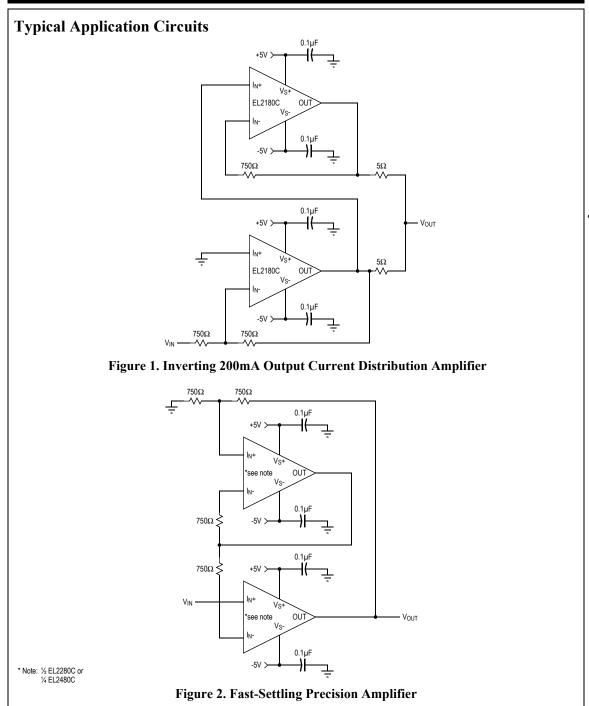
 $V_S =$  Supply Voltage

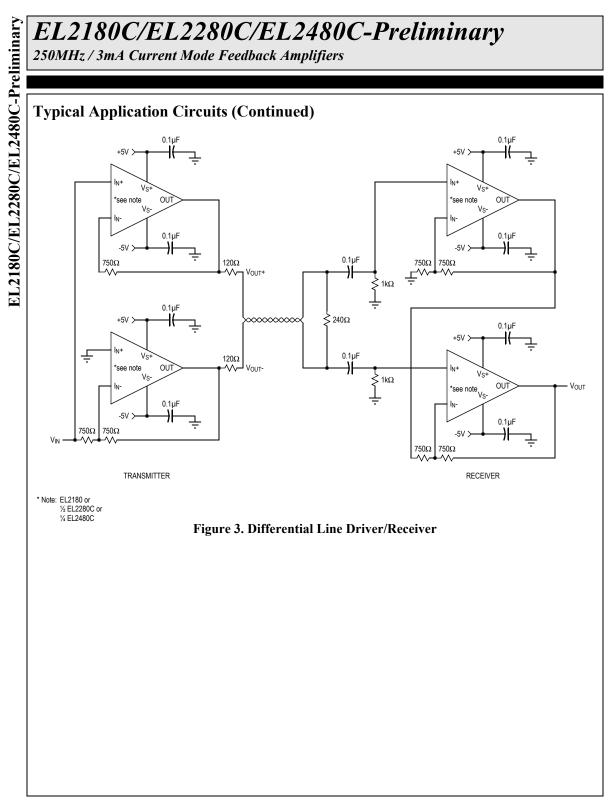
I<sub>SMAX</sub> = Maximum Supply Current of 1 Amplifier

V<sub>OUTMAX</sub> = Maximum Output Voltage of the Application

RL = Load Resistance

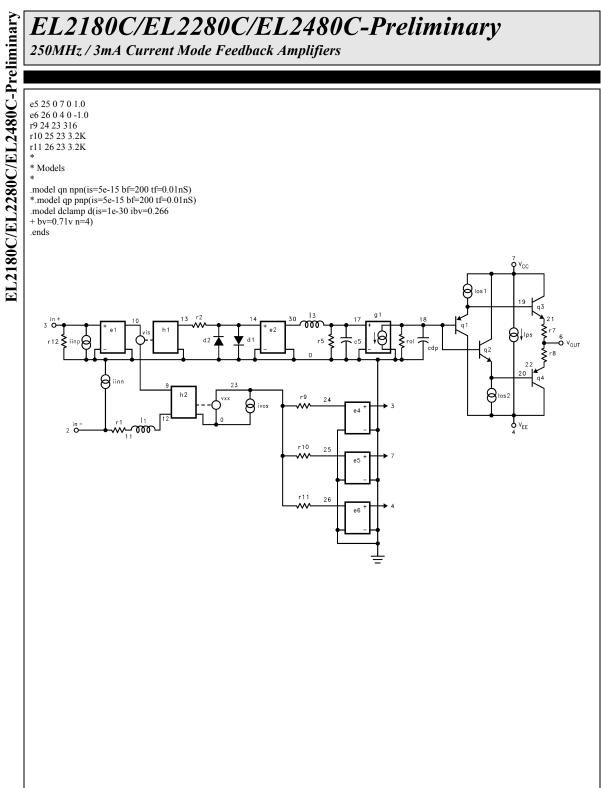
250MHz / 3mA Current Mode Feedback Amplifiers





250MHz / 3mA Current Mode Feedback Amplifiers

#### EL2180C/EL2280C/EL2480C Macromodel \* EL2180 Macromodel \* Revision A, March 1995 \* AC characteristics used: Rf = Rg = 750 ohms \* Connections: +input \* -input \* +Vsupply \* -Vsupply \* output \* 7 4 6 .subckt EL2180/el 3 2 \* Input Stage e1 10 0 3 0 1.0 vis 10 9 0V h2 9 12 vxx 1.0 r1 2 11 400 11 11 12 25nH iinp 3 0 1.5uA iinm 2 0 3uA r12 3 0 2Meg \* Slew Rate Limiting h1 13 0 vis 600 r2 13 14 1K d1 14 0 dclamp d2 0 14 dclamp \* High Frequency Pole $e2\;30\;0\;14\;0\;0.001666666666$ 13 30 17 150nH c5 17 0 0.8pF r5 17 0 165 \* Transimpedance Stage g1 0 18 17 0 1.0 rol 18 0 450K cdp 18 0 0.675pF \* Output Stage q1 4 18 19 qp q2 7 18 20 qn q3 7 19 21 qn q4 4 20 22 qp r7 21 6 4 r8 22 6 4 ios1 7 19 1mA ios2 20 4 1mA \* Supply Current ips 7 4 0.2mA \* Error Terms ivos 0 23 0.2mA vxx 23 0 0V e4 24 0 3 0 1.0



250MHz / 3mA Current Mode Feedback Amplifiers

# General Disclaimer

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July 19, 2001