

MITSUBISHI THYRISTOR MODULES  
**TM25RZ/EZ-M,-H**

MEDIUM POWER GENERAL USE  
 INSULATED TYPE

TM25RZ/EZ-M,-H



- **IT (AV)** Average on-state current ..... **25A**
- **IF (AV)** Average forward current ..... **25A**
- **VRRM** Repetitive peak reverse voltage  
..... **400/800V**
- **VDRM** Repetitive peak off-state voltage  
..... **400/800V**
- **MIX DOUBLE ARMS**
- **Insulated Type**
- **UL Recognized**

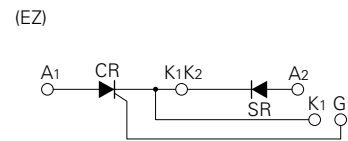
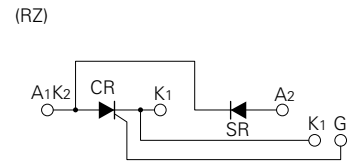
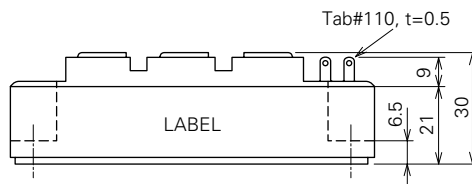
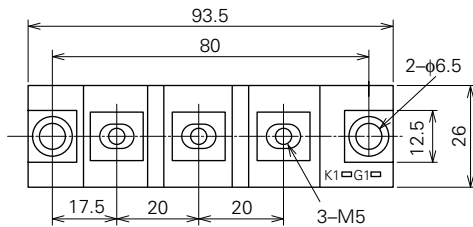
Yellow Card No. E80276 (N)  
 File No. E80271

**APPLICATION**

DC motor control, NC equipment, AC motor control, contactless switches,  
 electric furnace temperature control, light dimmers

**OUTLINE DRAWING & CIRCUIT DIAGRAM**

Dimensions in mm



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## ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Voltage class		Unit
		M	H	
VRRM	Repetitive peak reverse voltage	400	800	V
VRSM	Non-repetitive peak reverse voltage	480	960	V
VR (DC)	DC reverse voltage	320	640	V
VDRM	Repetitive peak off-state voltage	400	800	V
VDSM	Non-repetitive peak off-state voltage	480	960	V
VD (DC)	DC off-state voltage	320	640	V

Symbol	Parameter	Conditions	Ratings	Unit
IT (RMS), IF (RMS)	RMS current		39	A
IT (AV), IF (AV)	Average current	Single-phase, half-wave 180° conduction, Tc=93°C	25	A
ITSM, IFSM	Surge (non-repetitive) current	One half cycle at 60Hz, peak value	500	A
I <sup>2</sup> t	I <sup>2</sup> t for fusing	Value for one cycle of surge current	1.0 × 10 <sup>3</sup>	A <sup>2</sup> s
di/dt	Critical rate of rise of on-state current	VD=1/2VDRM, IG=0.5A, Tj=125°C	100	A/μs
PGM	Peak gate power dissipation		5.0	W
PG (AV)	Average gate power dissipation		0.5	W
VFGM	Peak gate forward voltage		10	V
VRGM	Peak gate reverse voltage		5.0	V
IFGM	Peak gate forward current		2.0	A
Tj	Junction temperature		-40~125	°C
Tstg	Storage temperature		-40~125	°C
Viso	Isolation voltage	Charged part to case	2500	V
—	Mounting torque	Main terminal screw M5	1.47~1.96	N·m
			15~20	kg·cm
		Mounting screw M6	1.96~2.94	N·m
—	Weight	Typical value	20~30	kg·cm
			160	g

## ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
IRRM	Repetitive peak reverse current	Tj=125°C, VRRM applied	—	—	4.0	mA
IDRM	Repetitive peak off-state current	Tj=125°C, VDRM applied	—	—	4.0	mA
VTM, VFM	Forward voltage	Tj=125°C, ITM=IFM=75A, instantaneous meas.	—	—	1.5	V
dv/dt	Critical rate of rise of off-state voltage	Tj=125°C, VD=2/3VDRM	500	—	—	V/μs
VGT	Gate trigger voltage	Tj=25°C, VD=6V, RL=2Ω	—	—	3.0	V
VGD	Gate non-trigger voltage	Tj=125°C, VD=1/2VDRM	0.25	—	—	V
IGT	Gate trigger current	Tj=25°C, VD=6V, RL=2Ω	10	—	50	mA
Rth (j-c)	Thermal resistance	Junction to case (per 1/2 module)	—	—	0.8	°C/W
Rth (c-f)	Contact thermal resistance	Case to fin, conductive grease applied (per 1/2 module)	—	—	0.2	°C/W
—	Insulation resistance	Measured with a 500V megohmmeter between main terminal and case	10	—	—	MΩ

Note: Items of the above table applies to the Thyristor part and the Diode part as circled in the following tables.

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## MAXIMUM RATINGS

Item	VRRM	VRSM	VR (DC)	VDRM	VDSM	VD (DC)	IT (RMS)	IT (AV)	ITSM	$I^2_t$	di/dt
							IF (RMS)	IF (AV)	IFSM		
Thyristor	○	○	○	○	○	○	○	○	○	○	○
Diode	○	○	○	—	—	—	○	○	○	○	—

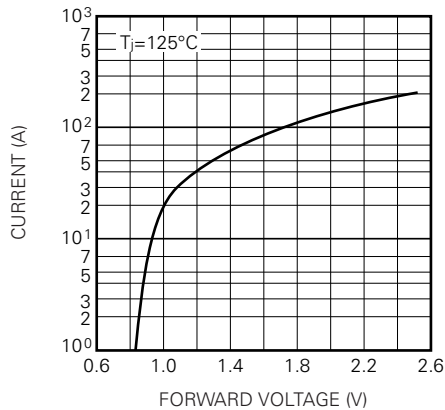
Item	PGM	PG (AV)	VFGM	IFGM	T <sub>j</sub>	T <sub>stg</sub>
Thyristor	○	○	○	○	○	○
Diode	—	—	—	—	○	○

## ELECTRICAL CHARACTERISTICS

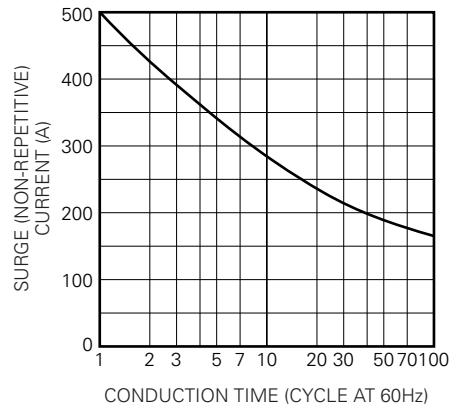
Item	IRRMS	IDRM	V <sub>TM</sub>	dv/dt	VGT	VGD	IGT	R <sub>th (j-c)</sub>	R <sub>th (c-f)</sub>
			V <sub>FM</sub>						
Thyristor	○	○	○	○	○	○	○	○	○
Diode	○	—	○	—	—	—	—	○	○

## PERFORMANCE CURVES

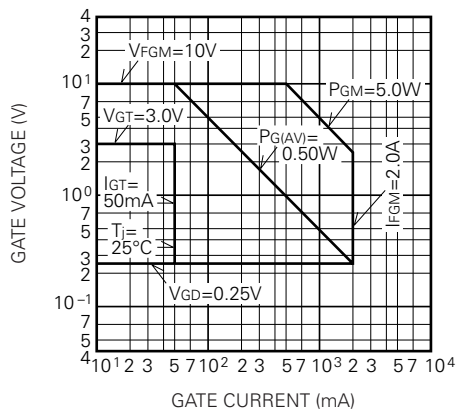
MAXIMUM FORWARD CHARACTERISTIC



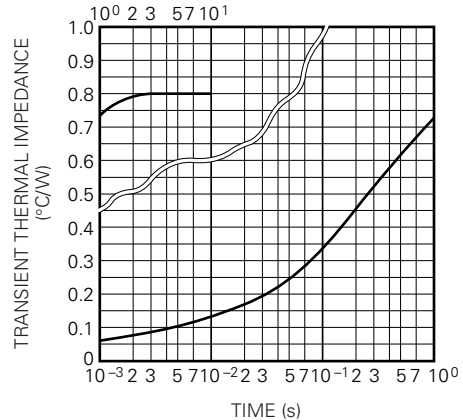
RATED SURGE (NON-REPETITIVE) CURRENT



GATE CHARACTERISTICS

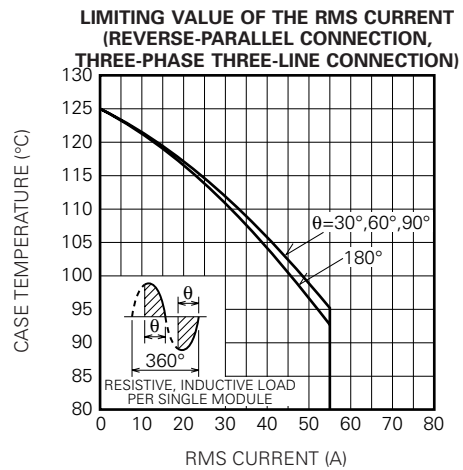
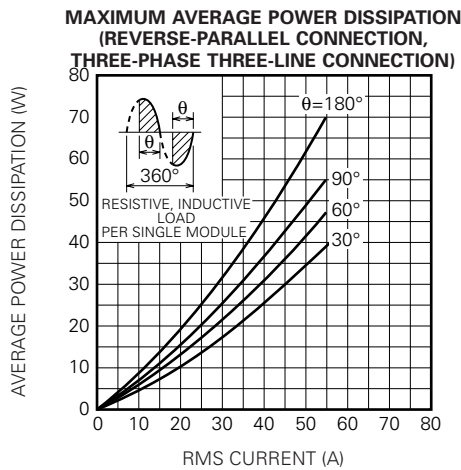
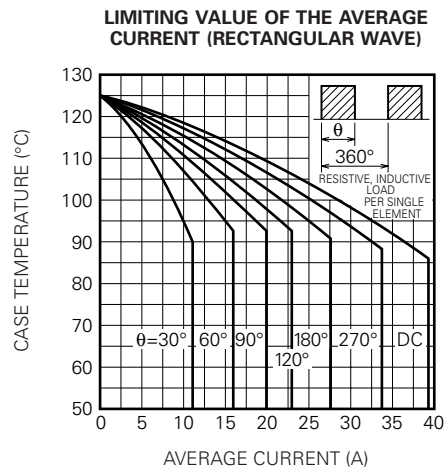
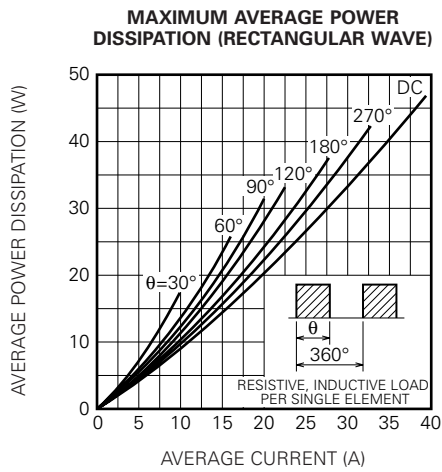
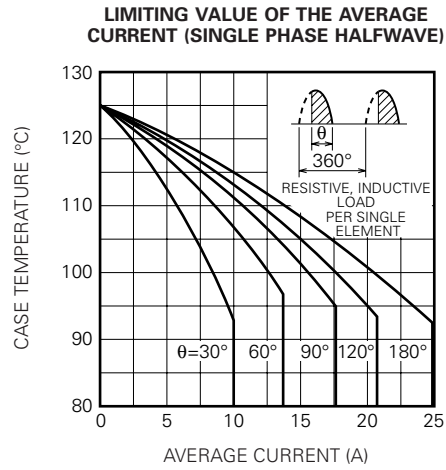
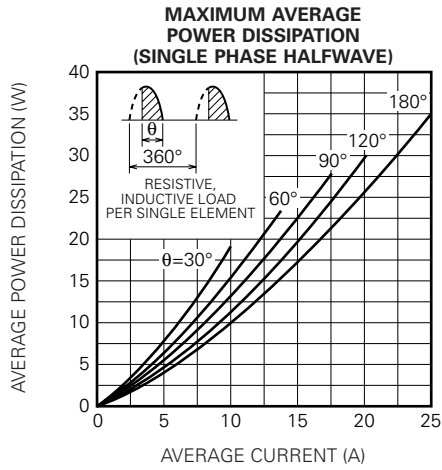


MAXIMUM TRANSIENT THERMAL IMPEDANCE (JUNCTION TO CASE)



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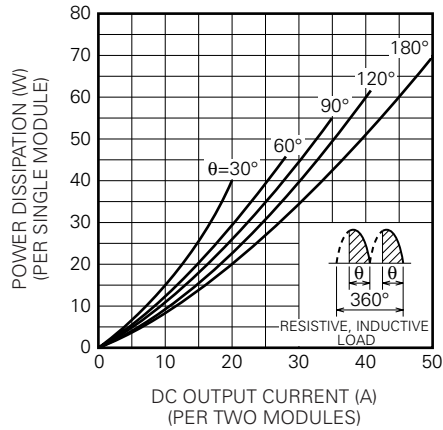
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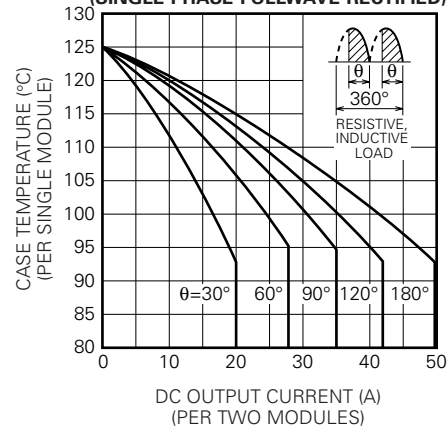
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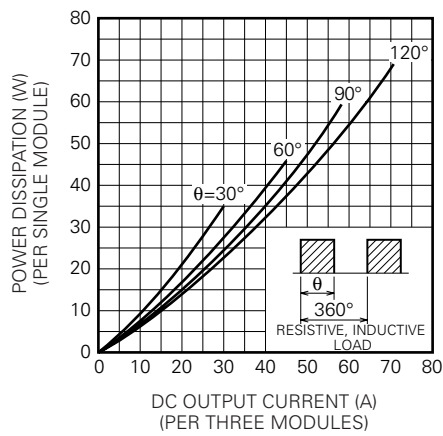
**MAXIMUM POWER DISSIPATION  
(SINGLE PHASE FULLWAVE RECTIFIED)**



**LIMITING VALUE OF THE DC OUTPUT CURRENT  
(SINGLE PHASE FULLWAVE RECTIFIED)**



**MAXIMUM POWER DISSIPATION  
(THREE-PHASE FULLWAVE RECTIFIED)**



**LIMITING VALUE OF THE DC OUTPUT CURRENT  
(THREE-PHASE FULLWAVE RECTIFIED)**

