May 2000

$\mathcal{N}ational\ Semiconductor$

LM4050
Precision Micropower Shunt Voltage Reference

General Description

Ideal for space critical applications, the LM4050 precision voltage reference is available in the sub-miniature (3 mm x 1.3 mm) SSOT-23 surface-mount package. The LM4050's design eliminates the need for an external stabilizing capacitor while ensuring stability with any capacitive load, thus making the LM4050 easy to use. Further reducing design effort is the availability of several fixed reverse breakdown voltages: 2.500V, 4.096V, 5.000V, 8.192V, and 10.000V. The minimum operating current increases from 60 µA for the LM4050-2.5 to 100 µA for the LM4050-10.0. All versions have a maximum operating current of 15 mA.

The LM4050 utilizes fuse and zener-zap reverse breakdown voltage trim during wafer sort to ensure that the prime parts have an accuracy of better than ±0.1% (A grade) at 25˚C. Bandgap reference temperature drift curvature correction and low dynamic impedance ensure stable reverse breakdown voltage accuracy over a wide range of operating temperatures and currents.

All grades and voltage options of the LM4050 operate between −40˚C and +85˚C. Selected parts can operate in the extended temperature range, from −40˚C and +125˚C.

Features

- Small packages: SSOT-23
- No output capacitor required
- Tolerates capacitive loads
- Fixed reverse breakdown voltages of 2.500V, 4.096V, 5.000V, 8.192V, and 10.000V

Key Specifications (LM4050-2.5)

- Output voltage tolerance
  (A grade, 25˚C)   ±0.1% (max)
- Low output noise
  (10 Hz to 10 kHz)   41 µV$_{rms}$(typ)
- Wide operating current range   60 µA to 15 mA
- Industrial temperature range   −40˚C to +85˚C
- Extended temperature range   −40˚C to +125˚C
- Low temperature coefficient   50 ppm/˚C (max)

Applications

- Portable, Battery-Powered Equipment
- Data Acquisition Systems
- Instrumentation
- Process Control
- Energy Management
- Product Testing
- Automotive
- Precision Audio Components

Connection Diagrams

SSOT-23

DS101045-1
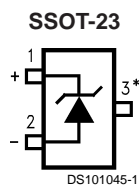
*This pin must be left floating or connected to pin 2.

Top View
See NS Package Number MF03A

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Ordering Information

Industrial Temperature Range (–40 ˚C to +85 ˚C)

Reverse Breakdown Voltage Tolerance at 25˚C and Average Reverse Breakdown Voltage Temperature Coefficient	LM4050 Supplied as 1000 Units, Tape and Reel	LM4050 Supplied as 3000 Units, Tape and Reel
±0.1%, 50 ppm/˚C max (A grade)	LM4050AIM3-2.5	LM4050AIM3X-2.5
	LM4050AIM3-4.1	LM4050AIM3X-4.1
	LM4050AIM3-5.0	LM4050AIM3X-5.0
	LM4050AIM3-8.2	LM4050AIM3X-8.2
	LM4050AIM3-10	LM4050AIM3X-10
±0.2%, 50 ppm/˚C max (B grade)	LM4050BIM3-2.5	LM4050BIM3X-2.5
	LM4050BIM3-4.1	LM4050BIM3X-4.1
	LM4050BIM3-5.0	LM4050BIM3X-5.0
	LM4050BIM3-8.2	LM4050BIM3X-8.2
	LM4050BIM3-10	LM4050BIM3X-10
±0.5%, 50 ppm/˚C max (C grade)	LM4050CIM3-2.5	LM4050CIM3X-2.5
	LM4050CIM3-4.1	LM4050CIM3X-4.1
	LM4050CIM3-5.0	LM4050CIM3X-5.0
	LM4050CIM3-8.2	LM4050CIM3X-8.2
	LM4050CIM3-10	LM4050CIM3X-10

Extended Temperature Range (–40 ˚C to +125 ˚C)

Reverse Breakdown Voltage Tolerance at 25˚C and Average Reverse Breakdown Voltage Temperature Coefficient	LM4050 Supplied as 1000 Units, Tape and Reel	LM4050 Supplied as 3000 Units, Tape and Reel
±0.5%, 50 ppm/˚C max (C grade)	LM4050CEM3-2.5	LM4050CEM3X-2.5

SSOT-23 Package Marking Information

Only three fields of marking are possible on the SSOT-23's small surface. This table gives the meaning of the three fields.

Part Marking	Field Definition
RCA	First Field:
RDA	R = Reference
REA	Second Field:
RFA	C = 2.500V Voltage Option
RGA	D = 4.096V Voltage Option
RCB	E = 5.000V Voltage Option
RDB	F = 8.192V Voltage Option
REB	G = 10.000V Voltage Option
RFB	
RGB	Third Field:
RCC	A–C = Initial Reverse Breakdown Voltage or Reference Voltage Tolerance
RDC	A = ±0.1%, B = ±0.2%, C = +0.5%,
REC	
RFC	
RGC	

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Reverse Current	20 mA
Forward Current	10 mA
Power Dissipation (T_A = 25˚C) (Note 3)	
M3 Package	280 mW
Storage Temperature (Note 2)	−65˚C to +150˚C
Lead Temperature	
M3 Package	
Vapor phase (60 seconds)	+215˚C
Infrared (15 seconds)	+220˚C
ESD Susceptibility	
Human Body Model (Note 4)	2 kV

Machine Model (Note 4)   200V

See AN-450 "Surface Mounting Methods and Their Effect on Product Reliability" for other methods of soldering surface mount devices.

Operating Ratings (Notes 1, 3)

Temperature Range	($T_{min} \leq T_A \leq T_{max}$)
Industrial Temperature Range	−40˚C $\leq T_A \leq$ +85˚C
Extended temperature Range	−40˚C $\leq T_A \leq$ +125˚C
Reverse Current	
LM4050-2.5	60 µA to 15 mA
LM4050-4.1	68 µA to 15 mA
LM4050-5.0	74 µA to 15 mA
LM4050-8.2	91 µA to 15 mA
LM4050-10.0	100 µA to 15 mA

LM4050-2.5
Electrical Characteristics

Boldface limits apply for T_A = T_J = T_{MIN} to T_{MAX}; all other limits T_A = T_J = 25˚C. The grades A, B and C designate initial Reverse Breakdown Voltage tolerances of ±0.1%, ±0.2%, and 0.5% respectively.

Symbol	Parameter	Conditions	Typical (Note 5)	LM4050AIM3 Limits (Note 6)	LM4050BIM3 Limits (Note 6)	LM4050CIM3 LM4050CEM3 Limits (Note 6)	Units (Limit)
V_R	Reverse Breakdown Voltage	I_R = 100 µA	2.500				V
	Reverse Breakdown Voltage Tolerance (Note 7)	I_R = 100 µA		±2.5	±5.0	±13	mV (max)
		Industrial Temp. Range Devices		**±11**	**±14**	**±21**	mV (max)
		Extended Temp. Range Devices				**±25**	mV (max)
I_{RMIN}	Minimum Operating Current		41				µA
				60	60	60	µA (max)
				65	**65**	**65**	µA (max)
$\Delta V_R/\Delta T$	Average Reverse Breakdown Voltage Temperature Coefficient (Note 7)	I_R = 10 mA	±20				ppm/˚C
		I_R = 1 mA	±15				ppm/˚C
		I_R = 100 µA	±15	**±50**	**±50**	**±50**	ppm/˚C (max)
$\Delta V_R/\Delta I_R$	Reverse Breakdown Voltage Change with Operating Current Change (Note 8)	$I_{RMIN} \leq I_R \leq$ 1 mA	0.3				mV
				0.8	0.8	0.8	mV (max)
				1.2	**1.2**	**1.2**	mV (max)
		1 mA $\leq I_R \leq$ 15 mA	2.3				mV
				6.0	6.0	6.0	mV (max)
				8.0	**8.0**	**8.0**	mV (max)
Z_R	Reverse Dynamic Impedance	I_R = 1 mA, f = 120 Hz, I_{AC} = 0.1 I_R	0.3				Ω
e_N	Wideband Noise	I_R = 100 µA 10 Hz $\leq$ f $\leq$ 10 kHz	41				µV$_{rms}$
ΔV_R	Reverse Breakdown Voltage Long Term Stability	t = 1000 hrs T = 25˚C ±0.1˚C I_R = 100 µA	120				ppm
V_{HYST}	Output Hysteresis	ΔT = −40˚C to 125˚C	0.7				mV

LM4050-4.1
Electrical Characteristics (Industrial Temperature Range)

Boldface limits apply for T$_A$ = T$_J$ = T$_{MIN}$ to T$_{MAX}$; all other limits T$_A$ = T$_J$ = 25˚C. The grades A, B and C designate initial Reverse Breakdown Voltage tolerances of ±0.1%, ±0.2%, and 0.5% respectively.

Symbol	Parameter	Conditions	Typical (Note 5)	LM4050AIM3 Limits (Note 6)	LM4050BIM3 Limits (Note 6)	LM4050CIM3 Limits (Note 6)	Units (Limit)
V$_R$	Reverse Breakdown Voltage	I$_R$ = 100 µA	4.096				V
	Reverse Breakdown Voltage Tolerance (Note 7)	I$_R$ = 100 µA		±4.1 **±18**	±8.2 **±22**	±21 **±34**	mV (max) mV (max)
I$_{RMIN}$	Minimum Operating Current		52	68 **73**	68 **73**	68 **73**	µA µA (max) µA (max)
ΔV$_R$/ΔT	Average Reverse Breakdown Voltage Temperature Coefficient(Note 7)	I$_R$ = 10 mA / I$_R$ = 1 mA / I$_R$ = 100 µA	±30 / ±20 / ±20	**±50**	**±50**	**±50**	ppm/˚C ppm/˚C ppm/˚C (max)
ΔV$_R$/ΔI$_R$	Reverse Breakdown Voltage Change with Operating Current Change (Note 8)	I$_{RMIN}$ ≤ I$_R$ ≤ 1 mA	0.2	0.9 **1.2**	0.9 **1.2**	0.9 **1.2**	mV mV (max) mV (max)
		1 mA ≤ I$_R$ ≤ 15 mA	2.0	7.0 **10.0**	7.0 **10.0**	7.0 **10.0**	mV mV (max) mV (max)
Z$_R$	Reverse Dynamic Impedance	I$_R$ = 1 mA, f = 120 Hz, I$_{AC}$ = 0.1 I$_R$	0.5				Ω
e$_N$	Wideband Noise	I$_R$ = 100 µA, 10 Hz ≤ f ≤ 10 kHz	93				µV$_{rms}$
ΔV$_R$	Reverse Breakdown Voltage Long Term Stability	t = 1000 hrs, T = 25˚C ±0.1˚C, I$_R$ = 100 µA	120				ppm
V$_{HYST}$	Output Hysteresis	ΔT = −40˚C to 125˚C	1.148				mV

LM4050-5.0
Electrical Characteristics (Industrial Temperature Range)

Boldface limits apply for $T_A = T_J = T_{MIN}$ to T_{MAX}; all other limits $T_A = T_J = 25°C$. The grades A, B and C designate initial Reverse Breakdown Voltage tolerances of ±0.1%, ±0.2% and 0.5% respectively.

Symbol	Parameter	Conditions	Typical (Note 5)	LM4050AIM3 Limits (Note 6)	LM4050BIM3 Limits (Note 6)	LM4050CIM3 Limits (Note 6)	Units (Limit)
V_R	Reverse Breakdown Voltage	$I_R = 100\ \mu A$	5.000				V
	Reverse Breakdown Voltage Tolerance (Note 7)	$I_R = 100\ \mu A$		±5.0	±10	±25	mV (max)
				±22	**±27**	**±42**	mV (max)
I_{RMIN}	Minimum Operating Current		56				μA
				74	74	74	μA (max)
				80	**80**	**80**	μA (max)
$\Delta V_R/\Delta T$	Average Reverse Breakdown Voltage Temperature Coefficient (Note 7)	$I_R = 10\ mA$	±30				ppm/°C
		$I_R = 1\ mA$	±20				ppm/°C
		$I_R = 100\ \mu A$	±20	**±50**	**±50**	**±50**	ppm/°C (max)
$\Delta V_R/\Delta I_R$	Reverse Breakdown Voltage Change with Operating Current Change (Note 8)	$I_{RMIN} \leq I_R \leq 1\ mA$	0.2				mV
				1.0	1.0	1.0	mV (max)
				1.4	**1.4**	**1.4**	mV (max)
		$1\ mA \leq I_R \leq 15\ mA$	2.0				mV
				8.0	8.0	8.0	mV (max)
				12.0	**12.0**	**12.0**	mV (max)
Z_R	Reverse Dynamic Impedance	$I_R = 1\ mA$, $f = 120\ Hz$, $I_{AC} = 0.1\ I_R$	0.5				Ω / Ω (max)
e_N	Wideband Noise	$I_R = 100\ \mu A$ 10 Hz ≤ f ≤ 10 kHz	93				μV_{rms}
ΔV_R	Reverse Breakdown Voltage Long Term Stability	t = 1000 hrs T = 25°C ±0.1°C $I_R = 100\ \mu A$	120				ppm
V_{HYST}	Output Hysteresis	$\Delta T = -40°C$ to $125°C$	1.4				mV

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LM4050-8.2
Electrical Characteristics (Industrial Temperature Range)

Boldface limits apply for $T_A = T_J = T_{MIN}$ to T_{MAX}; all other limits $T_A = T_J = 25°C$. The grades A, B and C designate initial Reverse Breakdown Voltage tolerances of ±0.1% and ±0.2% and 0.5% respectively.

Symbol	Parameter	Conditions	Typical (Note 5)	LM4050AIM3 Limits (Note 6)	LM4050BIM3 Limits (Note 6)	LM4050CIM3 Limits (Note 6)	Units (Limit)
V_R	Reverse Breakdown Voltage	I_R = 150 µA	8.192				V
	Reverse Breakdown Voltage Tolerance (Note 7)	I_R = 150 µA		±8.2 **±35**	±16 **±43**	±41 **±68**	mV (max) mV (max)
I_{RMIN}	Minimum Operating Current		74	91 **95**	91 **95**	91 **95**	µA µA (max) µA (max)
$\Delta V_R/\Delta T$	Average Reverse Breakdown Voltage Temperature Coefficient(Note 7)	I_R = 10 mA I_R = 1 mA I_R = 150 µA	±40 ±20 ±20	**±50**	**±50**	**±50**	ppm/°C ppm/°C ppm/°C (max)
$\Delta V_R/\Delta I_R$	Reverse Breakdown Voltage Change with Operating Current Change (Note 8)	$I_{RMIN} \leq I_R \leq 1$ mA	0.6	1.3 **2.5**	1.3 **2.5**	1.3 **2.5**	mV mV (max) mV (max)
		1 mA $\leq I_R \leq$ 15 mA	7.0	10.0 **18.0**	10.0 **18.0**	10.0 **18.0**	mV mV (max) mV (max)
Z_R	Reverse Dynamic Impedance	I_R = 1 mA, f = 120 Hz, I_{AC} = 0.1 I_R	0.6				Ω
e_N	Wideband Noise	I_R = 150 µA 10 Hz $\leq$ f $\leq$ 10 kHz	150				µV$_{rms}$
ΔV_R	Reverse Breakdown Voltage Long Term Stability	t = 1000 hrs T = 25°C ±0.1°C I_R = 150 µA	120				ppm
V_{HYST}	Output Hysteresis	ΔT = −40°C to 125°C	2.3				mV

LM4050-10.0
Electrical Characteristics (Industrial Temperature Range)

Boldface limits apply for $T_A = T_J = T_{MIN}$ to T_{MAX}; all other limits $T_A = T_J = 25°C$. The grades A, B and C designate initial Reverse Breakdown Voltage tolerances of ±0.1% and ±0.2% and 0.5% respectively.

Symbol	Parameter	Conditions	Typical (Note 5)	LM4050AIM3 Limits (Note 6)	LM4050BIM3 Limits (Note 6)	LM4050CIM3 Limits (Note 6)	Units (Limit)
V_R	Reverse Breakdown Voltage	I_R = 150 µA	10.00				V
	Reverse Breakdown Voltage Tolerance (Note 7)	I_R = 150 µA		±10 **±43**	±20 **±53**	±50 **±83**	mV (max) mV (max)
I_{RMIN}	Minimum Operating Current		80	100 **103**	100 **103**	100 **103**	µA µA (max) µA (max)
$\Delta V_R/\Delta T$	Average Reverse Breakdown Voltage Temperature Coefficient (Note 7)	I_R = 10 mA I_R = 1 mA I_R = 150 µA	±40 ±20 ±20	**±50**	**±50**	**±50**	ppm/°C ppm/°C ppm/°C (max)
$\Delta V_R/\Delta I_R$	Reverse Breakdown Voltage Change with Operating Current Change (Note 8)	$I_{RMIN} \leq I_R \leq 1$ mA	0.8	1.5 **3.5**	1.5 **3.5**	1.5 **3.5**	mV mV (max) mV (max)
		1 mA $\leq I_R \leq 15$ mA	8.0	12.0 **23.0**	12.0 **23.0**	12.0 **23.0**	mV mV (max) mV (max)
Z_R	Reverse Dynamic Impedance	I_R = 1 mA, f = 120 Hz, I_{AC} = 0.1 I_R	0.7				Ω
e_N	Wideband Noise	I_R = 150 µA 10 Hz $\leq$ f $\leq$ 10 kHz	150				µV$_{rms}$
ΔV_R	Reverse Breakdown Voltage Long Term Stability	t = 1000 hrs T = 25°C ±0.1°C I_R = 150 µA	120				ppm
V_{HYST}	Output Hysteresis	$\Delta T = -40°C$ to 125°C	2.8				mV

Note 1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not guarantee specific performance limits. For guaranteed specifications and test conditions, see the Electrical Characteristics. The guaranteed specifications apply only for the test conditions listed. Some performance characteristics may degrade when the device is not operated under the listed test conditions.

Note 2: If parts are exposed to temperatures outside the specific operating temperature range, the output may shift due to hysteresis.

Note 3: The maximum power dissipation must be derated at elevated temperatures and is dictated by T_{Jmax} (maximum junction temperature), θ_{JA} (junction to ambient thermal resistance), and T_A (ambient temperature). The maximum allowable power dissipation at any temperature is $PD_{max} = (T_{Jmax} - T_A)/\theta_{JA}$ or the number given in the Absolute Maximum Ratings, whichever is lower. For the LM4050, $T_{Jmax} = 125°C$, and the typical thermal resistance (θ_{JA}), when board mounted, is 326°C/W for the SSOT-23 package.

Note 4: The human body model is a 100 pF capacitor discharged through a 1.5 kΩ resistor into each pin. The machine model is a 200 pF capacitor discharged directly into each pin.

Note 5: Typicals are at $T_J = 25°C$ and represent most likely parametric norm.

Note 6: Limits are 100% production tested at 25°C. Limits over temperature are guaranteed through correlation using Statistical Quality Control (SQC) methods. The limits are used to calculate National's AOQL.

Note 7: The boldface (over-temperature) limit for Reverse Breakdown Voltage Tolerance is defined as the room temperature Reverse Breakdown Voltage Tolerance ±[($\Delta V_R/\Delta T$)(maxΔT)(V_R)]. Where, $\Delta V_R/\Delta T$ is the V_R temperature coefficient, maxΔT is the maximum difference in temperature from the reference point of 25°C to T_{MIN} or T_{MAX}, and V_R is the reverse breakdown voltage. The total over-temperature tolerance for the different grades in the industrial temperature range where maxΔT = 65°C is shown below:

A-grade: ±0.425% = ±0.1% ±50 ppm/°C x 65°C

B-grade: ±0.525% = ±0.2% ±50 ppm/°C x 65°C

C-grade: ±0.825% = ±0.5% ±50 ppm/°C x 65°C

Therefore, as an example, the A-grade LM4050-2.5 has an over-temperature Reverse Breakdown Voltage tolerance of ±2.5V x 0.425% = ±11 mV.

Note 8: Load regulation is measured on pulse basis from no load to the specified load current. Output changes due to die temperature change must be taken into account separately.
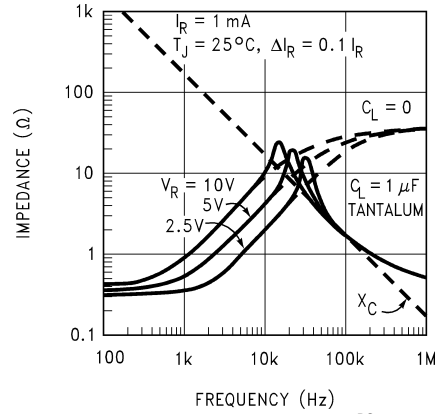
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Typical Performance Characteristics

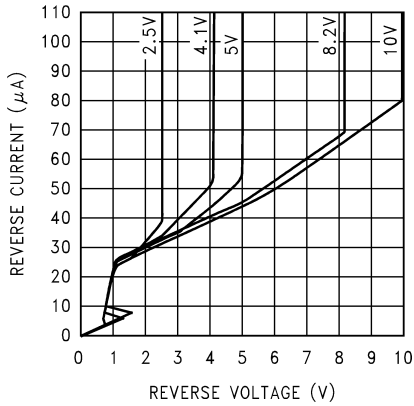**Output Impedance vs Frequency**
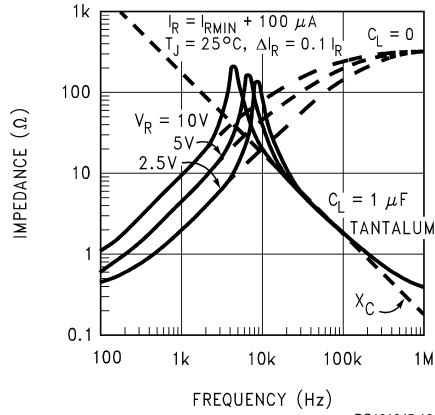
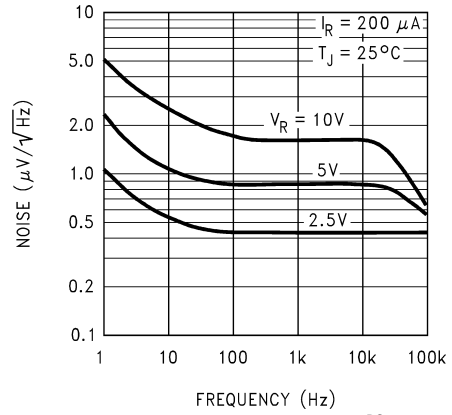
DS101045-10

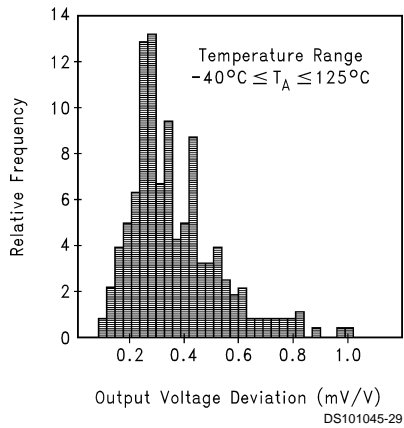**Output Impedance vs Frequency**

DS101045-11

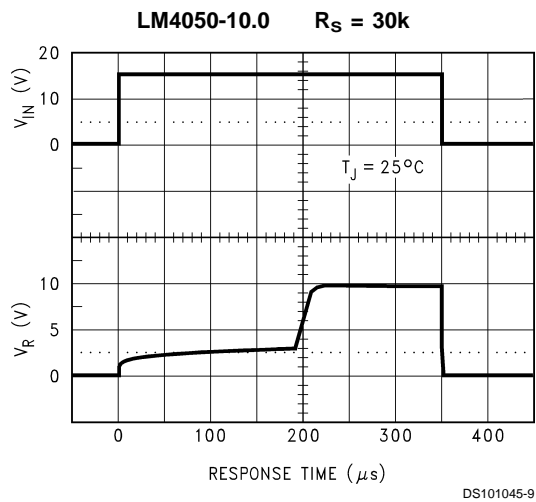**Reverse Characteristics and Minimum Operating Current**
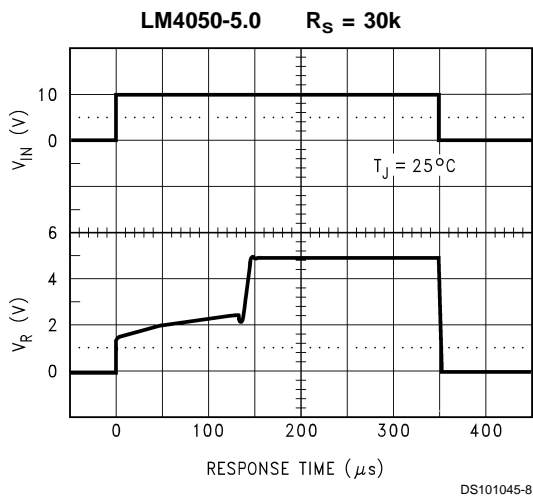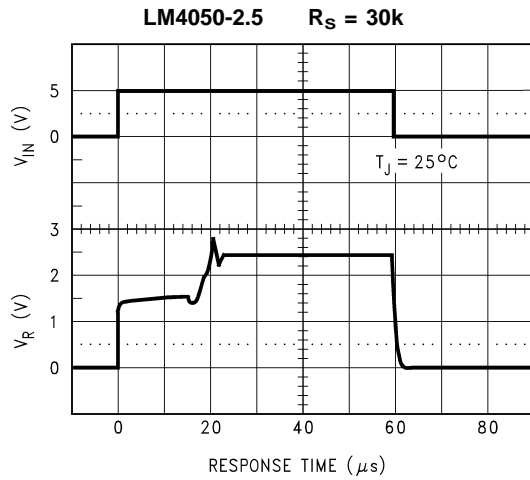

DS101045-12

Noise Voltage vs Frequency

DS101045-13

Thermal Hysteresis

DS101045-29

Start-Up Characteristics

Test Circuit 1 Hz rate

DS101045-5

LM4050-2.5   R_S = 30k

V_{IN} (V)

V_R (V)

$T_J = 25°C$

RESPONSE TIME (μs)

DS101045-7

LM4050-5.0   R_S = 30k

V_{IN} (V)

V_R (V)

$T_J = 25°C$

RESPONSE TIME (μs)

DS101045-8

LM4050-10.0   R_S = 30k

V_{IN} (V)

V_R (V)

$T_J = 25°C$

RESPONSE TIME (μs)

DS101045-9
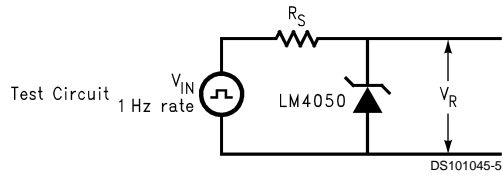
Functional Block Diagram

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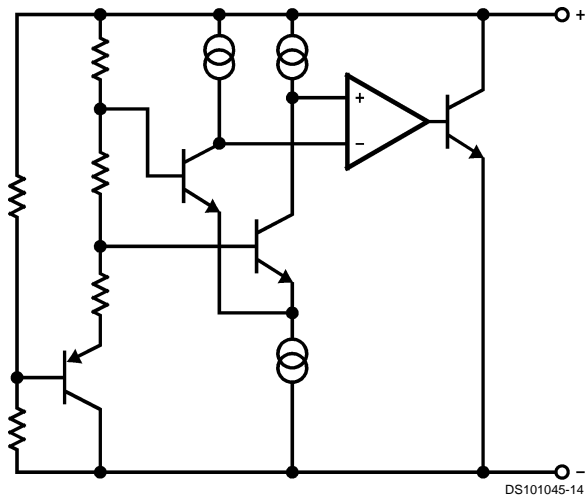
Applications Information

The LM4050 is a precision micro-power curvature-corrected bandgap shunt voltage reference. For space critical applications, the LM4050 is available in the sub-miniature SSOT-23 surface-mount package. The LM4050 has been designed for stable operation without the need of an external capacitor connected between the "+" pin and the "−" pin. If, however, a bypass capacitor is used, the LM4050 remains stable. Reducing design effort is the availability of several fixed reverse breakdown voltages: 2.500V, 4.096V, 5.000V, 8.192V, and 10.000V. The minimum operating current increases from 60 µA for the LM4050-2.5 to 100 µA for the LM4050-10.0. All versions have a maximum operating current of 15 mA.

LM4050s in the SSOT-23 packages have a parasitic Schottky diode between pin 2 (−) and pin 3 (Die attach interface contact). Therefore, pin 3 of the SSOT-23 package must be left floating or connected to pin 2.

The 4.096V version allows single +5V 12-bit ADCs or DACs to operate with an LSB equal to 1 mV. For 12-bit ADCs or DACs that operate on supplies of 10V or greater, the 8.192V version gives 2 mV per LSB.

In a conventional shunt regulator application (*Figure 1*) , an external series resistor (R_S) is connected between the supply voltage and the LM4050. R_S determines the current that flows through the load (I_L) and the LM4050 (I_Q). Since load current and supply voltage may vary, R_S should be small enough to supply at least the maximum guaranteed I_{RMIN}

(spec. table) to the LM4050 even when the supply voltage is at its minimum and the load current is at its maximum value. When the supply voltage is at its maximum and I_L is at its minimum, R_S should be large enough so that the current flowing through the LM4050 is less than 15 mA.

R_S is determined by the supply voltage, (V_S), the load and operating current, (I_L and I_Q), and the LM4050's reverse breakdown voltage, V_R.

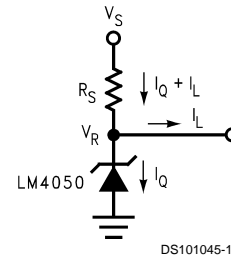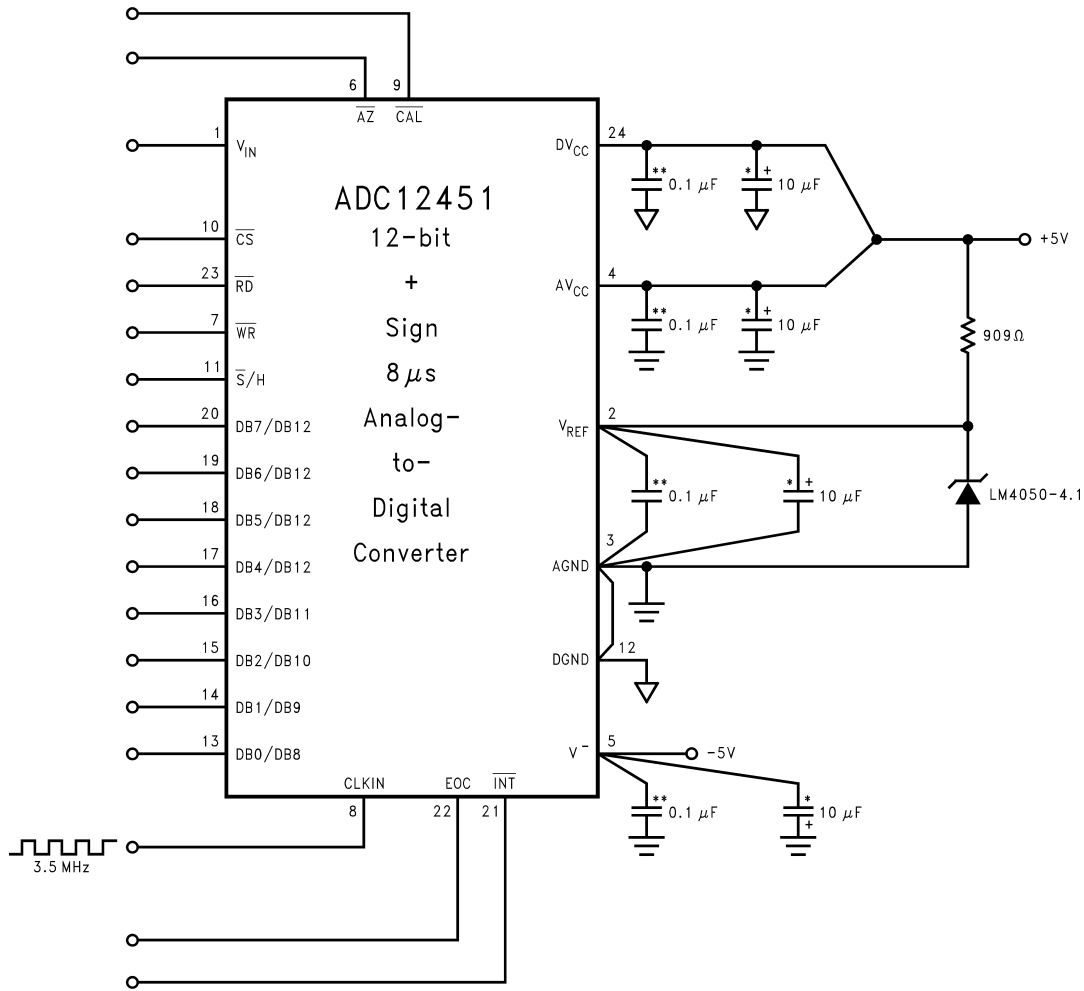$$R_S = \frac{V_S - V_R}{I_L + I_Q}$$

Typical Applications

DS101045-15

FIGURE 1. Shunt Regulator
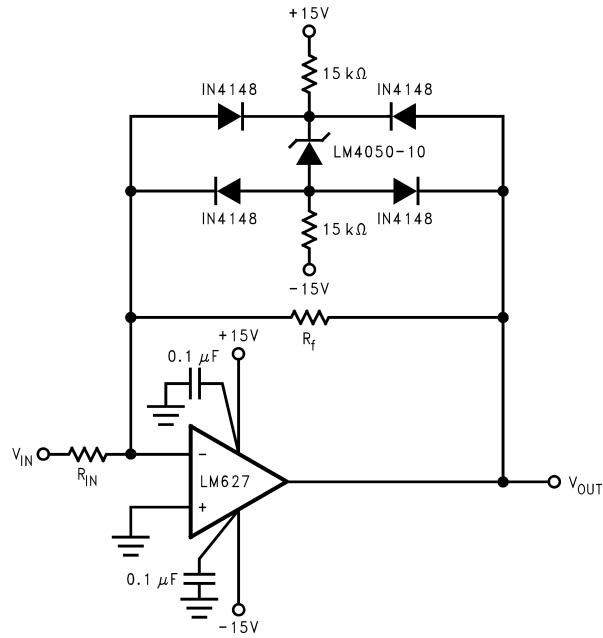
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Typical Applications (Continued)

DS101045-16

**Ceramic monolithic
*Tantalum

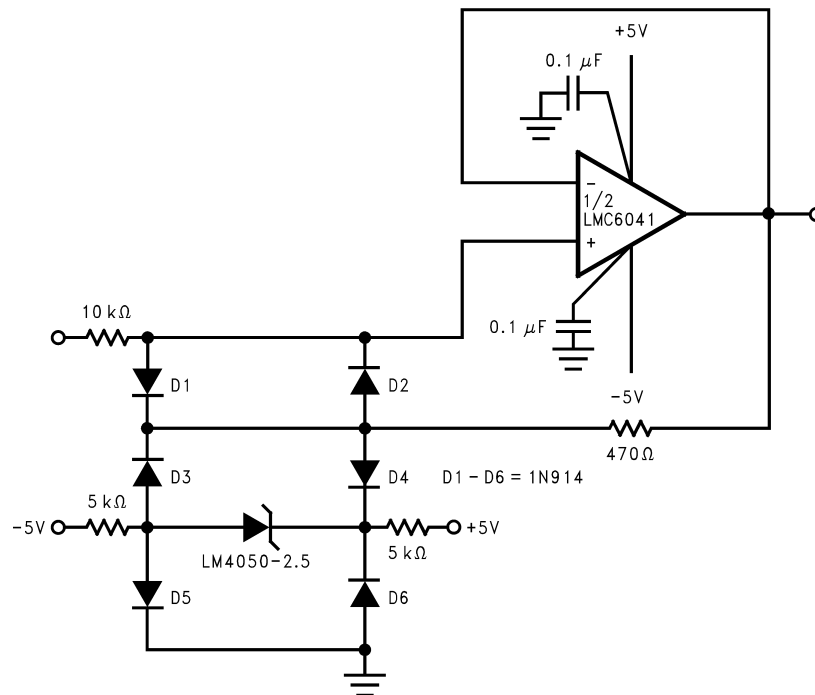**FIGURE 2. LM4050-4.1's Nominal 4.096 breakdown voltage gives ADC12451 1 mV/LSB**

Typical Applications (Continued)

FIGURE 3. Bounded amplifier reduces saturation-induced delays and can prevent succeeding stage damage.
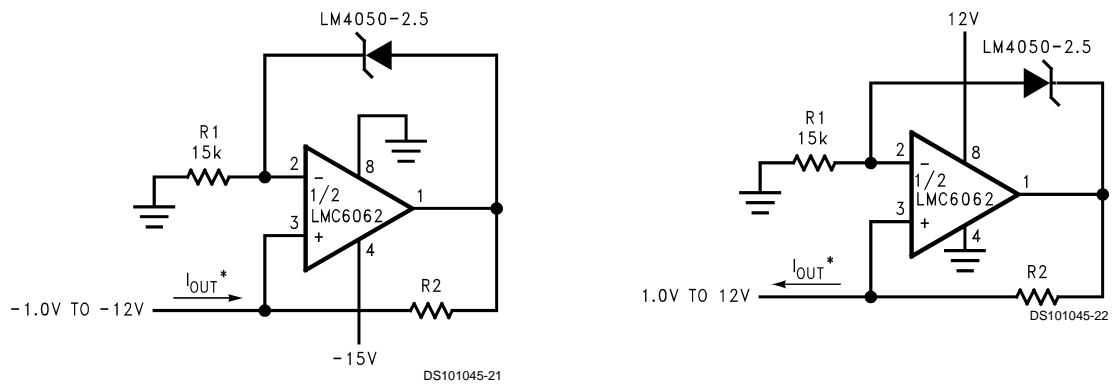**Nominal clamping voltage is ±11.5V (LM4050's reverse breakdown voltage +2 diode V_F).**

FIGURE 4. Protecting Op Amp input. The bounding voltage is ±4V with the LM4050-2.5
(LM4050's reverse breakdown voltage + 3 diode V_F).

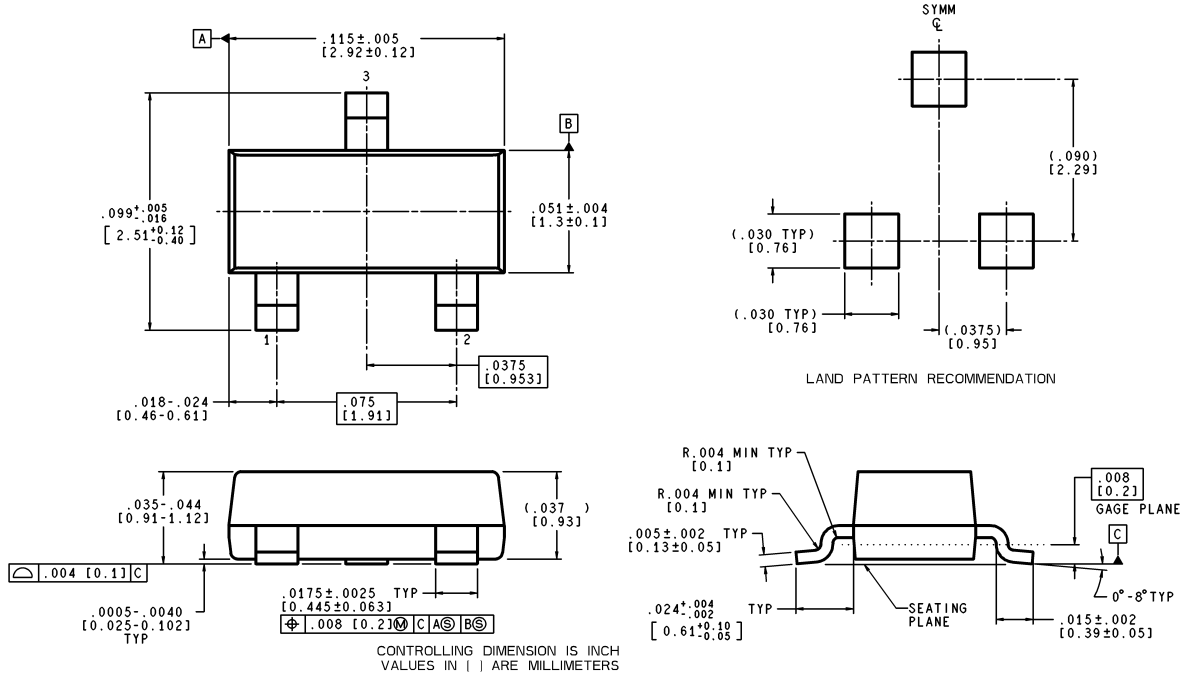# Typical Applications (Continued)
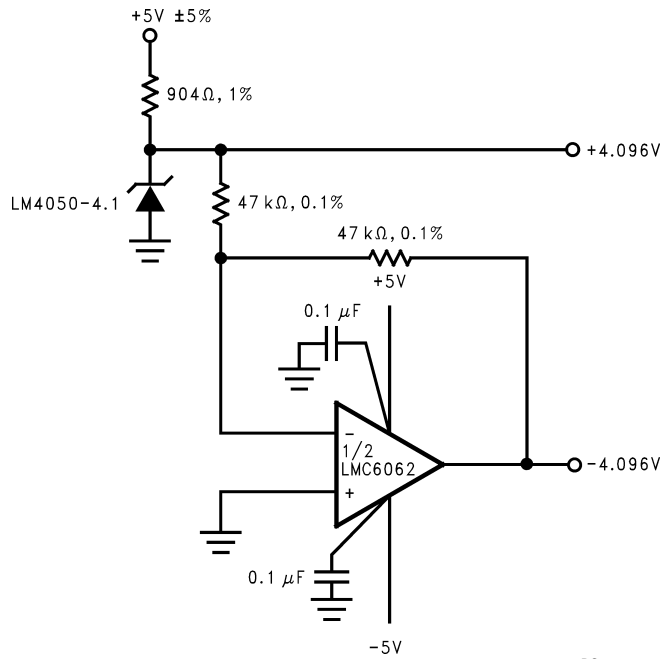
FIGURE 5. Precision ±4.096V Reference

$$I_{OUT} = \frac{2.5V}{R2}$$

FIGURE 6. Precision 1 µA to 1 mA Current Sources

Physical Dimensions inches (millimeters) unless otherwise noted

.115±.005
[2.92±0.12]

.099 +.005 -.016
[2.51 +0.12 -0.40]

.051±.004
[1.3±0.1]

.0375
[0.953]

.018-.024
[0.46-0.61]

.075
[1.91]

(.090)
[2.29]

(.030 TYP)
[0.76]

(.030 TYP)
[0.76]

(.0375)
[0.95]

LAND PATTERN RECOMMENDATION

.035-.044
[0.91-1.12]

(.037)
[0.93]

.004 [0.1] C

.0005-.0040
[0.025-0.102]
TYP

.0175±.0025 TYP
[0.445±0.063]

.008 [0.2] M C A S B S

R.004 MIN TYP
[0.1]

R.004 MIN TYP
[0.1]

.005±.002 TYP
[0.13±0.05]

.024 +.004 -.002
[0.61 +0.10 -0.05] TYP

.008
[0.2]
GAGE PLANE

0°-8° TYP

.015±.002
[0.39±0.05]

SEATING PLANE

CONTROLLING DIMENSION IS INCH
VALUES IN [ ] ARE MILLIMETERS

MF03A (Rev A)

Plastic Surface Mount Package (M3)
NS Package Number MF03A
(JEDEC Registration TO-236AB)

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