



Digital-to-Analog Converters Selection Table

Generic Part #	Resolution (Bits)	Settling Time (ns typ)	# DACs Per Pkg	Output (I or V)	Data Interface	FS Out Range	REF (Int/Ext)	Power Supply (V nom)	Power Dissipation (mW max)
12-Bit, 10 ns to 100 ns, Sorted by Resolution, Settling Time and Number of DACs/Package									
AD9742	12	11	1	I	Par	Diff (0 to 2 mA), Diff (0 to 20 mA)	Int/Ext	Range-Single (+2.7 to +3.6), Single (+3), Single (+3.3)	135
AD9753	12	11	1	I	Par	Diff (0 to 2 mA), Diff (0 to 20 mA)	Int/Ext	Single (+3)	330
AD9773	12	11	2	I	Par	Diff (0 to 2 mA), Diff (0 to 20 mA)	Int/Ext	Single (3.1 to 3.5)	1600
AD9762	12	35	1	I	Par	Diff (0 to 2 mA), Diff (0 to 20 mA)	Int/Ext	Range-Single (+2.7 to +5.5), Single (+3), Single (+3.3), Single (+5)	160
AD9752	12	35	1	I	Par	Diff-Uni (20 mA)	Int/Ext	Single (+5)	220
AD568	12	35	1	I or V	Unbuff'd Par	Uni 10.24 mA, Bip 5.12 mA, Uni 1.024 V, Bip 0.512 V	Int	Dual (+15, -15)	625
AD9765	12	35	2	I	Par	Diff (0 to 2 mA), Diff (0 to 20 mA)	Int/Ext	Range-Single (+3.0 to +5.5), Single (+3), Single (+3.3), Single (+5)	410

Last Updated: 4/2004