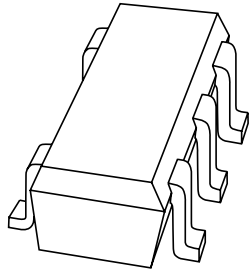


# DATA SHEET



## **BZA800A-series** Quadruple ESD transient voltage suppressor

Preliminary specification

2000 Apr 18

# Quadruple ESD transient voltage suppressor

## BZA800A-series

### FEATURES

- ESD rating >8 kV, according to IEC1000-4-2
- SOT353 (SC-88A) surface mount package
- Common anode configuration

### APPLICATIONS

- Computers and peripherals
- Audio and video equipment
- Communication systems
- Medical equipment.

### DESCRIPTION

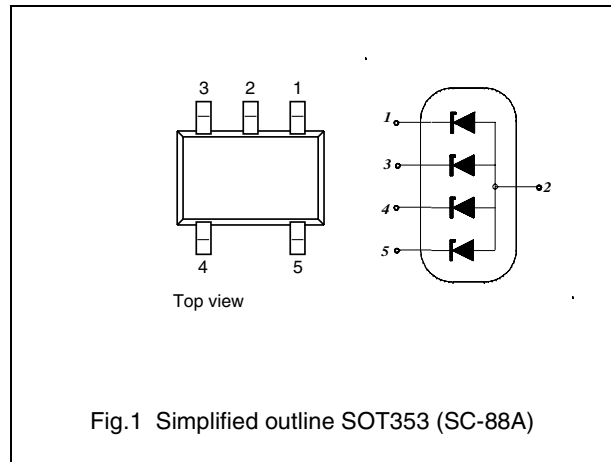
Monolithic transient voltage suppressor diode in a five lead SOT353 (SC-88A) package for 4-bit wide ESD transient suppression.

### MARKING

TYPE NUMBER	MARKING CODE
BZA856A	Z1
BZA862A	Z2
BZA868A	Z3
BZA820A	Z4

### PINNING

PIN	DESCRIPTION
1	cathode 1
2	common anode
3	cathode 2
4	cathode 3
5	cathode 4



## Quadruple ESD transient voltage suppressor

## BZA800A-series

**LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
<b>Per diode</b>					
$I_Z$	working current	$T_a = 25\text{ }^\circ\text{C}$	–	note 1	mA
$I_F$	continuous forward current	$T_a = 25\text{ }^\circ\text{C}$	–	200	mA
$I_{FSM}$	non-repetitive peak forward current	$t_p = 1\text{ ms}$ ; square pulse	–	3.75	A
$P_{tot}$	total power dissipation	$T_a = 25\text{ }^\circ\text{C}$	–	335	mW
$P_{ZSM}$	non repetitive peak reverse power dissipation BZA856A, BZA862A, BZA868A BZA820A	square pulse; $t_p = 1\text{ ms}$ ; see Fig.3	–	24	W
			–	17	W
$T_{stg}$	storage temperature		–65	+150	$^\circ\text{C}$
$T_j$	junction temperature		–	150	$^\circ\text{C}$

**Notes**

- DC working current limited by  $P_{tot\text{ max}}$ .

**THERMAL CHARACTERISTICS**

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th\ j-a}$	thermal resistance from junction to ambient	all diodes loaded	370	K/W

**ELECTRICAL CHARACTERISTICS** $T_j = 25\text{ }^\circ\text{C}$  unless otherwise specified.

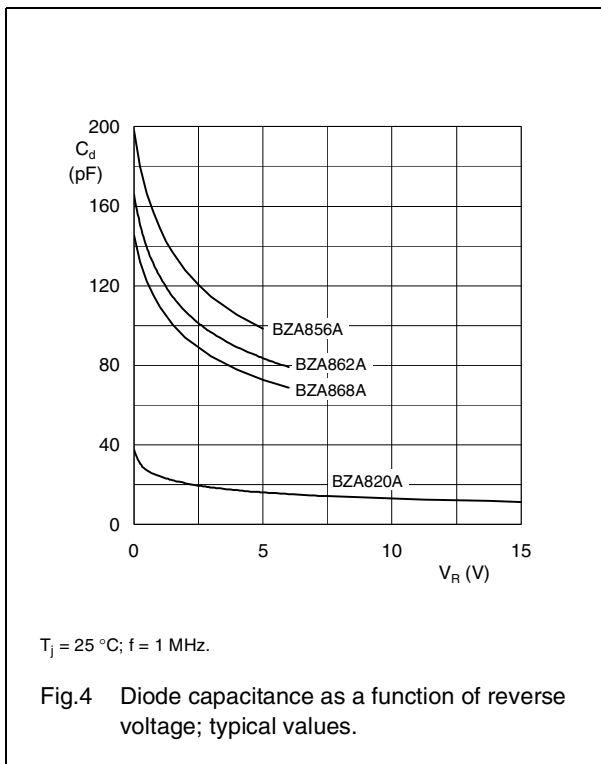
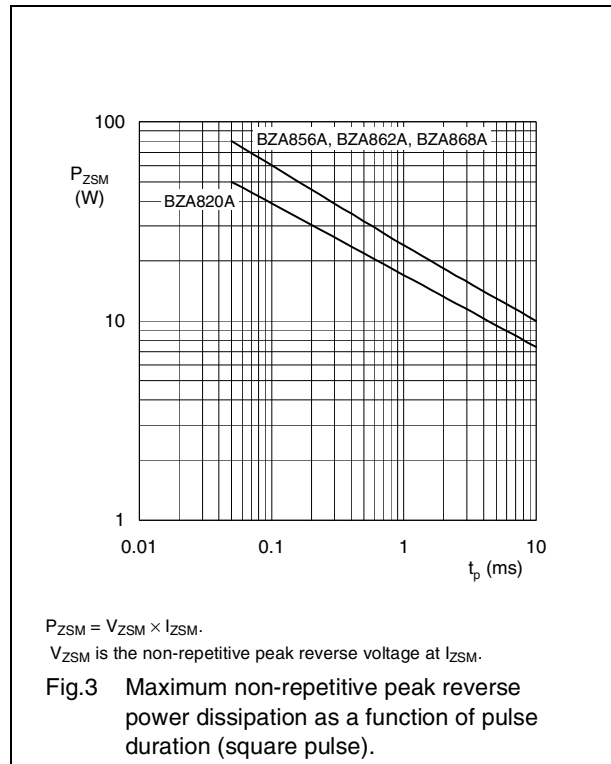
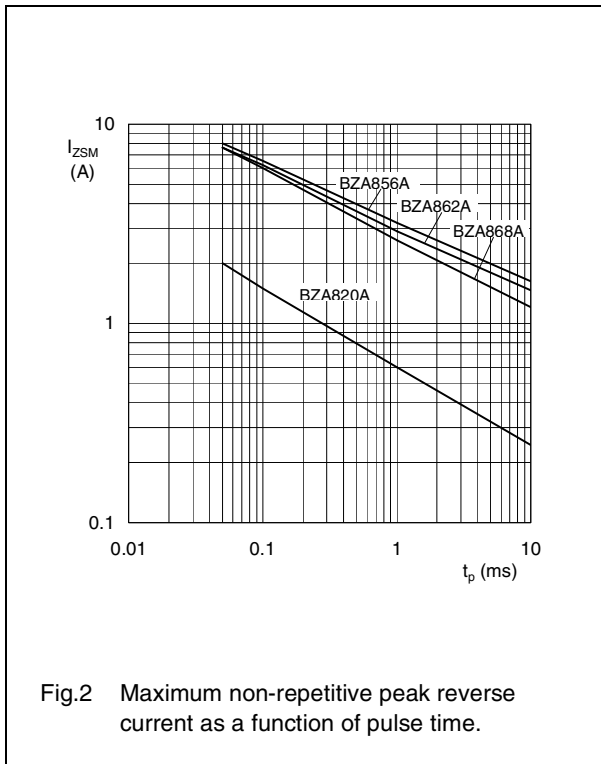
SYMBOL	PARAMETER	CONDITIONS	MAX.	UNIT
$V_F$	forward voltage	$I_F = 200\text{ mA}$	1.3	V
$I_R$	reverse current			
	BZA856A	$V_R = 3\text{ V}$	2000	nA
	BZA862A	$V_R = 4\text{ V}$	700	nA
	BZA868A	$V_R = 4.3\text{ V}$	200	nA
	BZA820A	$V_R = 15\text{ V}$	100	nA

 $T_j = 25\text{ }^\circ\text{C}$  unless otherwise specified

TYPE	WORKING VOLTAGE $V_Z$ (V) at $I_Z = 1\text{ mA}$			DIFFERENTIAL RESISTANCE $r_{dif}$ ( $\Omega$ ) at $I_Z = 1\text{ mA}$	TEMP. COEFF. $S_Z$ (mV/K) at $I_Z = 1\text{ mA}$	DIODE CAP. $C_d$ (pF) at $f = 1\text{ MHz}$ ; $V_R = 0\text{ V}$	NON-REPETITIVE PEAK REVERSE CURRENT $I_{ZSM}$ (A) at $t_p = 1\text{ ms}$ ; $T_{amb} = 25\text{ }^\circ\text{C}$
	MIN.	TYP.	MAX.	MAX.	TYP.	MAX.	MAX.
BZA856A	5.32	5.6	5.88	400	–0.2	240	3.2
BZA862A	5.89	6.2	6.51	300	1.8	200	2.9
BZA868A	6.46	6.8	7.14	200	3	180	2.6
BZA820A	19	20	21	125	16	50	0.6

Quadruple ESD transient voltage suppressor

BZA800A-series



Quadruple ESD transient voltage suppressor

BZA800A-series

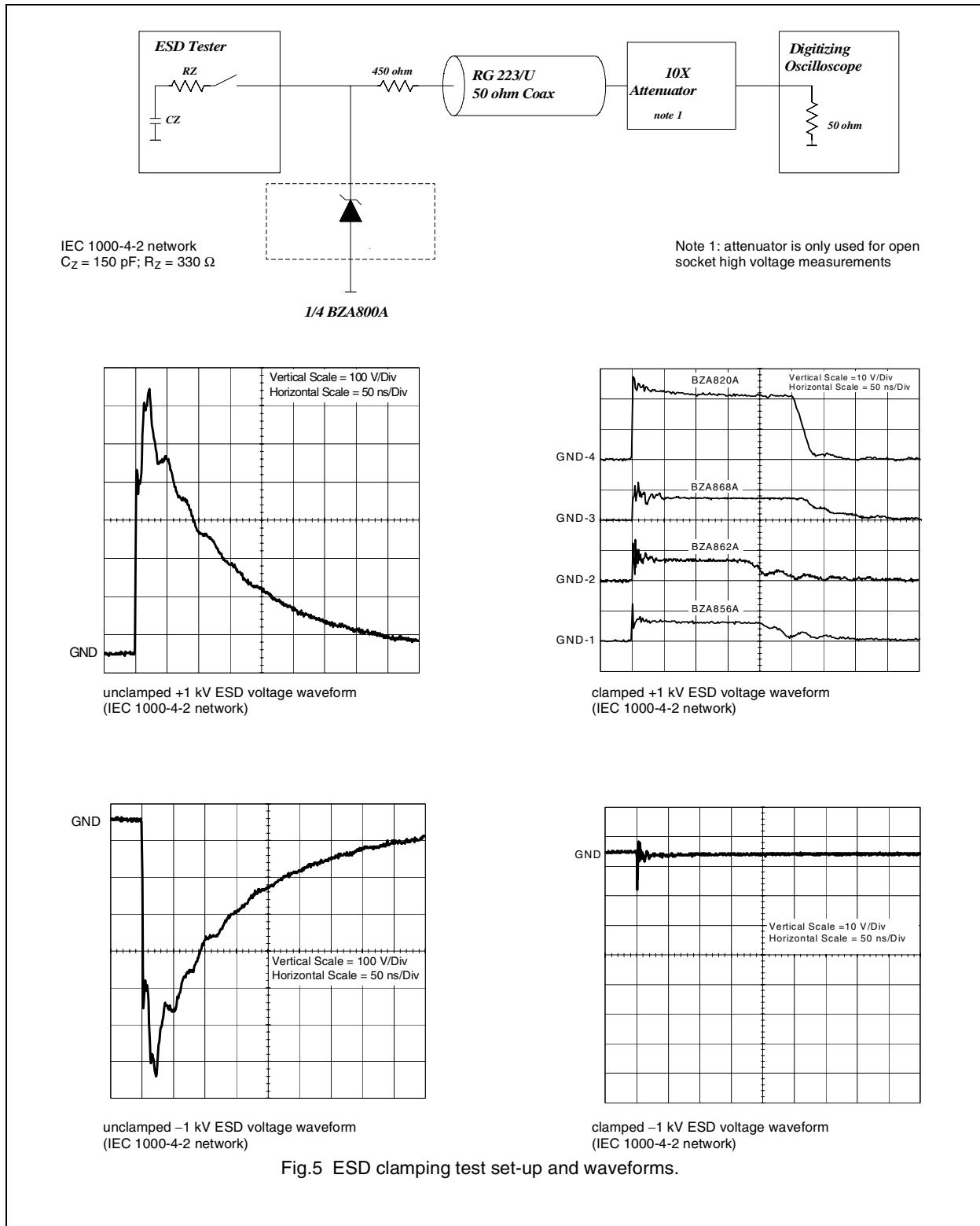


Fig.5 ESD clamping test set-up and waveforms.

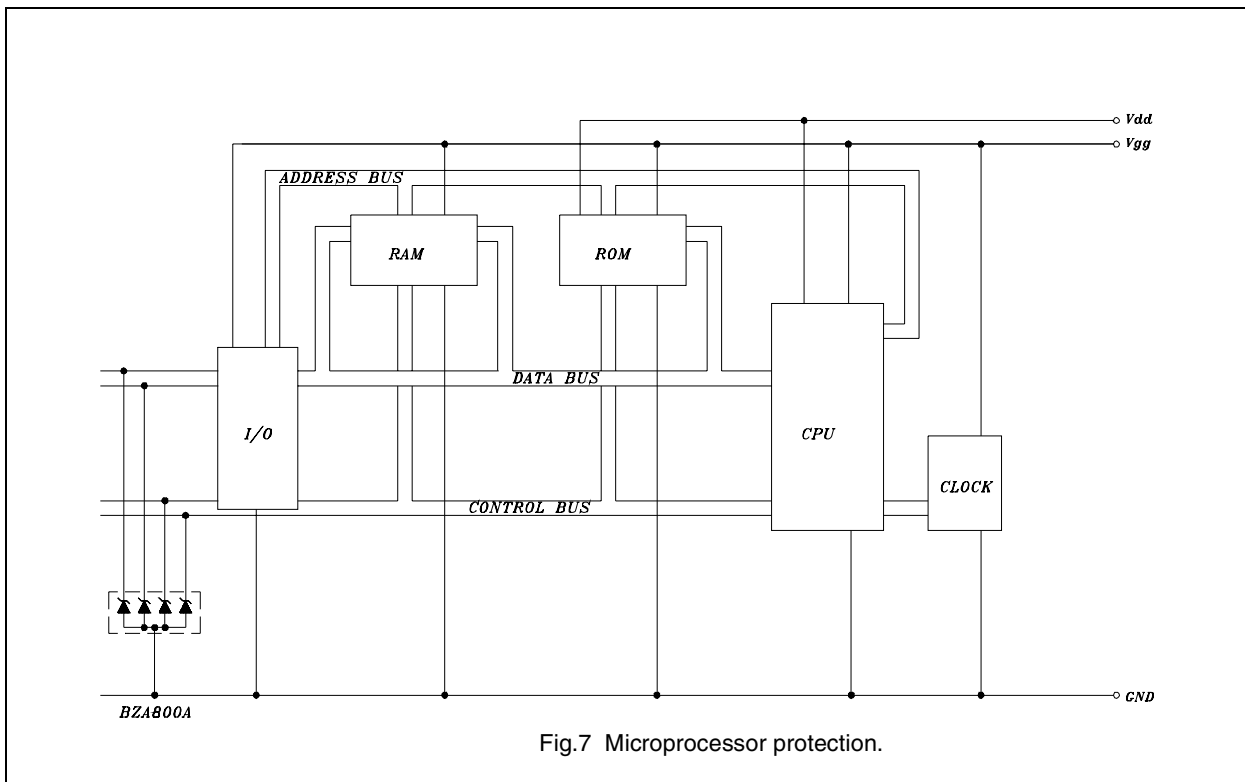
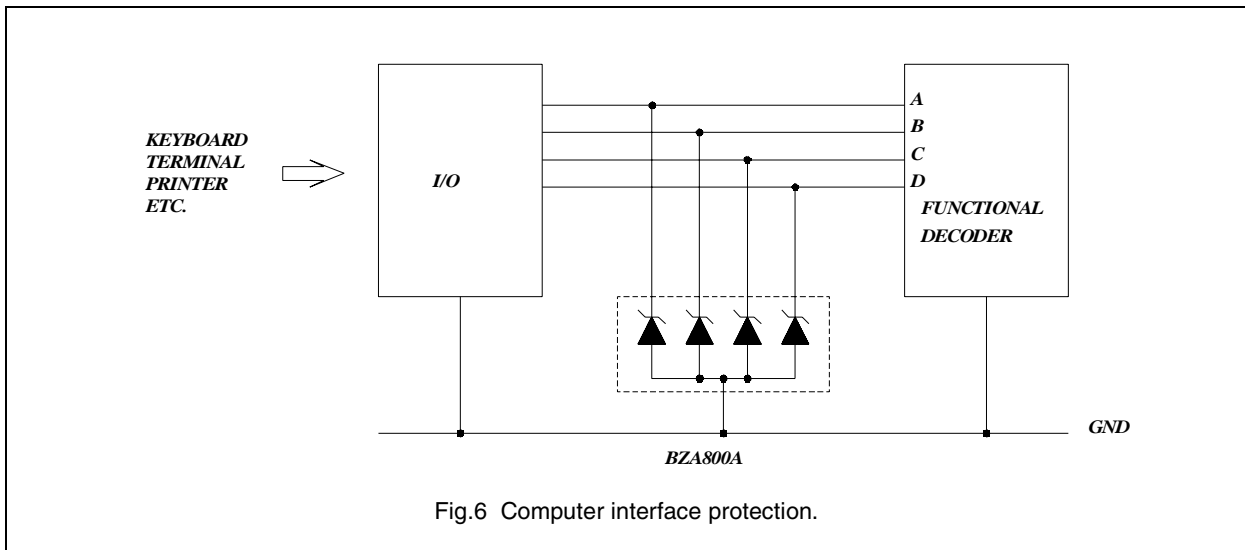
# Quadruple ESD transient voltage suppressor

# BZA800A-series

## APPLICATION INFORMATION

### Typical common anode application

A quadruple transient suppressor in a SOT353 package makes it possible to protect four separate lines using only one package. Two simplified examples are shown in Figs 6 and 7.



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## Quadruple ESD transient voltage suppressor

## BZA800A-series

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### Device placement and printed-circuit board layout

Circuit board layout is of extreme importance in the suppression of transients. The clamping voltage of the BZA800A is determined by the peak transient current and the rate of rise of that current ( $di/dt$ ). Since parasitic inductances can further add to the clamping voltage ( $V = L di/dt$ ) the series conductor lengths on the printed-circuit board should be kept to a minimum. This includes the lead length of the suppression element.

In addition to minimizing conductor length the following printed-circuit board layout guidelines are recommended:

1. Place the suppression element close to the input terminals or connectors.
2. Keep parallel signal paths to a minimum.
3. Avoid running protection conductors in parallel with unprotected conductors.
4. Minimize all printed-circuit board loop areas including power and ground loops.
5. Minimize the length of the transient return path to ground.
6. Avoid using shared transient return paths to a common ground point.

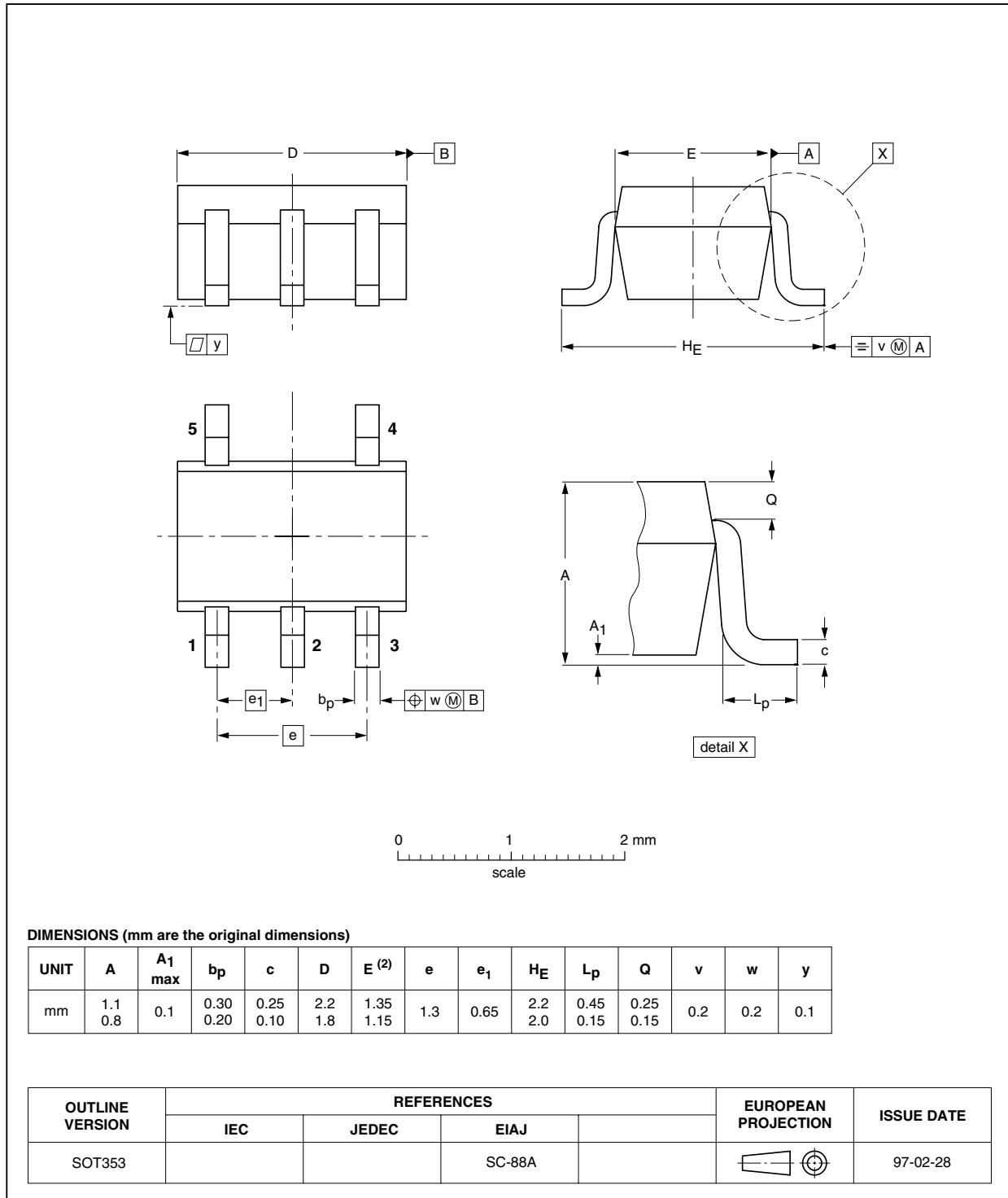
Quadruple ESD transient voltage suppressor

BZA800A-series

PACKAGE OUTLINE

Plastic surface mounted package; 5 leads

SOT353





## Quadruple ESD transient voltage suppressor

## BZA800A-series

## DATA SHEET STATUS

DATA SHEET STATUS	PRODUCT STATUS	DEFINITIONS <sup>(1)</sup>
Objective specification	Development	This data sheet contains the design target or goal specifications for product development. Specification may change in any manner without notice.
Preliminary specification	Qualification	This data sheet contains preliminary data, and supplementary data will be published at a later date. Philips Semiconductors reserves the right to make changes at any time without notice in order to improve design and supply the best possible product.
Product specification	Production	This data sheet contains final specifications. Philips Semiconductors reserves the right to make changes at any time without notice in order to improve design and supply the best possible product.

## Note

1. Please consult the most recently issued data sheet before initiating or completing a design.

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These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

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