BGU7005

SiGe:C Low Noise Amplifier MMIC for GPS, GLONASS and Galileo

Rev. 4 — 6 May 2011

Product data sheet

1. Product profile

1.1 General description

The BGU7005 is a Low Noise Amplifier (LNA) for GNSS receiver applications in a plastic leadless 6-pin, extremely small SOT886 package. The BGU7005 requires only one external matching inductor and one external decoupling capacitor.

The BGU7005 adapts itself to the changing environment resulting from co-habitation of different radio systems in modern cellular handsets. It has been designed for low power consumption and optimal performance when jamming signals from co-existing cellular transmitters are present. At low jamming power levels it delivers 16.5 dB gain at a noise figure of 0.85 dB. During high jamming power levels, resulting for example from a cellular transmit burst, it temporarily increases its bias current to improve sensitivity.

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Therefore care should be taken during transport and handling.

1.2 Features and benefits

- Covers full GNSS L1 band, from 1559 MHz to 1610 MHz
- Noise figure (NF) = 0.85 dB
- Gain 16.5 dB
- High input 1 dB compression point P_{i(1dB)} of -11 dBm
- High out of band IP3_i of 9 dBm
- Supply voltage 1.5 V to 2.85 V
- Power-down mode current consumption < 1 μA</p>
- Optimized performance at low supply current of 4.5 mA
- Integrated matching for the output
- Requires only one input matching inductor and one supply decoupling capacitor
- Input and output DC decoupled
- ESD protection on all pins (HBM > 2 kV)
- Integrated temperature stabilized bias for easy design
- Small 6-pin leadless package 1 mm × 1.45 mm × 0.5 mm
- 110 GHz transit frequency SiGe:C technology



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1.3 Applications

- LNA for GPS in automotive applications like Toll Collection and Emergency Call.
- LNA for GPS in smart phones, feature phones, tablet PCs, Personal Navigation Devices, Digital Still Cameras, Digital Video Cameras, RF Front End modules, complete GPS chipset modules and theft protection (laptop, ATM).

1.4 Quick reference data

Table 1. Quick reference data

f = 1559 MHz to 1610 MHz; V_{CC} = 1.8 V; P_i < -40 dBm; T_{amb} = 25 °C; input matched to 50 Ω using a 5.6 nH inductor; unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{CC}	supply voltage	RF input AC coupled		1.5	-	2.85	V
I _{CC}	supply current	$V_{ENABLE} \ge 0.8 \text{ V}$					
		P _i < -40 dBm		3.2	4.5	5.7	mΑ
		$P_i = -20 \text{ dBm}$		8.1	11.6	14.4	mΑ
Gp	power gain	$P_i < -40$ dBm, no jammer		14	16.5	19	dB
		P _i = −20 dBm, no jammer		15	17.5	20	dB
NF	noise figure	$P_i < -40$ dBm, no jammer	[1]	-	0.85	1.2	dB
		P _i < -40 dBm, no jammer	[2]	-	0.9	1.3	dB
		P _i = −20 dBm, no jammer		-	1.2	1.6	dB
$P_{i(1dB)}$	input power at 1 dB	f = 1575 MHz				5.7 14.4 19 20 1.2 1.3	
P _{i(1dB)} ii	gain compression	$V_{CC} = 1.5 \text{ V}$		-15	-12	-	dBm
		V _{CC} = 1.8 V		-14	-11	-	dBm
		V _{CC} = 2.85 V		-11	-8	-	dBm
IP3 _i	input third-order intercept point	f = 1.575 GHz					
		V _{CC} = 1.5 V	[3]	5	8	-	dBm
		V _{CC} = 1.8 V	[3]	5	9	-	dBm
		V _{CC} = 2.85 V	[3]	5	12	-	dBm

^[1] PCB losses are subtracted.

2. Pinning information

Table 2. Pinning

Pin	Description	Simplified outline	Graphic symbol
1	GND		
2	GND	1 2 3	4 5
3	RF_IN		3—6
4	V _{CC}		
5	ENABLE		2 1 sym129
6	RF_OUT	6 5 4 bottom view	J

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^[2] Including PCB losses.

^[3] $f_1 = 1713 \text{ MHz}$; $f_2 = 1851 \text{ MHz}$; $P_1 = P_2 = -30 \text{ dBm}$.

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3. Ordering information

Table 3. Ordering information

Type number	Package						
	Name	Description	Version				
BGU7005	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 \times 1.45 \times 0.5 mm	SOT886				

4. Marking

Table 4. Marking codes

Type number	Marking code
BGU7005	AC

5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CC}	supply voltage	RF input AC coupled	-0.5	3.1	V
V_{ENABLE}	voltage on pin ENABLE	$V_{CC} \geq 2.5 \ V$	-0.5	3.1	V
		V _{CC} < 2.5 V	<u>[2]</u> −0.5	$V_{CC} + 0.6$	V
V_{RF_IN}	voltage on pin RF_IN	DC			
		$V_{CC} \ge 3.0 \text{ V}$	<u>[3]</u> −0.5	3.6	V
		V _{CC} < 3.0 V	[2][3] -0.5	$V_{CC} + 0.6$	V
V_{RF_OUT}	voltage on pin RF_OUT	DC			
		$V_{CC} \ge 1.8 \text{ V}$	<u>[3]</u> −0.5	3.6	V
		V _{CC} < 1.8 V	[2][3] -0.5	V _{CC} + 1.8	V
Pi	input power		-	0	dBm
P _{tot}	total power dissipation	T _{sp} ≤ 130 °C	[1]	55	mW
T _{stg}	storage temperature		-65	150	°C
Tj	junction temperature		-	150	°C

^[1] T_{sp} is the temperature at the soldering point of the emitter lead.

6. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Тур	Unit
$R_{th(j-sp)}$	thermal resistance from junction to solder point		225	K/W

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^[2] Due to internal ESD diode protection, the applied voltage should not exceed the specified maximum in order to avoid excess current.

^[3] The RF input and RF output are AC coupled through internal DC blocking capacitor.

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7. Characteristics

Table 7. Characteristics

f = 1559 MHz to 1610 MHz; V_{CC} = 1.8 V; $V_{ENABLE} \ge 0.8$ V; P_i < -40 dBm; T_{amb} = 25 °C; input matched to 50 Ω using a 5.6 nH inductor; unless otherwise specified.

		A 11/1	F	_		
Symbol	Parameter	Conditions		Тур		Unit
V _{CC}	supply voltage	RF input AC coupled	1.5	-	2.85	V
I _{CC}	supply current	V _{ENABLE} ≥ 0.8 V				
		P _i < -40 dBm	3.2	4.5	5.7	mA
		$P_i = -20 \text{ dBm}$	8.1	11.6	14.4	mA
		V _{ENABLE} ≤ 0.35 V	-	-	1	μΑ
T_{amb}	ambient temperature		-40	+25	+85	°C
G_p	power gain	T _{amb} = 25 °C				
		P _i < −40 dBm, no jammer	14	16.5	19	dB
		$P_i = -20 \text{ dBm}$, no jammer	15	17.5	20	dB
		$P_{jam} = -20 \text{ dBm}; f_{jam} = 850 \text{ MHz}$	15	17.5	20	dB
		$P_{jam} = -20 \text{ dBm}$; $f_{jam} = 1850 \text{ MHz}$	15	17.5	20	dB
		$-40~^{\circ}C \le T_{amb} \le +85~^{\circ}C$				
		P _i < −40 dBm, no jammer	13	-	20	dB
		$P_i = -20$ dBm, no jammer	14	-	21	dB
		$P_{jam} = -20 \text{ dBm}; f_{jam} = 850 \text{ MHz}$	14	-	21	dB
		$P_{jam} = -20 \text{ dBm}$; $f_{jam} = 1850 \text{ MHz}$	14	-	21	dB
RLin	input return loss	$P_i < -40 \text{ dBm}$	5	8	-	dB
		$P_i = -20 \text{ dBm}$	6	10	-	dB
RLout	output return loss	$P_i < -40 \text{ dBm}$	10	20	-	dB
		$P_i = -20 \text{ dBm}$	10	14	-	dB
ISL	isolation		20	23	-	dB
NF	noise figure	T _{amb} = 25 °C				
		P _i < −40 dBm, no jammer	[1] -	0.85	1.2	dB
		P _i < -40 dBm, no jammer	[2] _	0.9	1.3	dB
		P _i = −20 dBm, no jammer	-	1.2	1.6	dB
		$P_{jam} = -20 \text{ dBm}$; $f_{jam} = 850 \text{ MHz}$	-	1.1	1.5	dB
		$P_{jam} = -20 \text{ dBm}; f_{jam} = 1850 \text{ MHz}$	-	1.3	1.7	dB
		-40 °C ≤ T _{amb} ≤ +85 °C				
		P _i < −40 dBm, no jammer	-	-	1.7	dB
		P _i = −20 dBm, no jammer	-	-	1.9	dB
		$P_{jam} = -20 \text{ dBm}$; $f_{jam} = 850 \text{ MHz}$	-	-	1.8	dB
		$P_{iam} = -20 \text{ dBm}; f_{iam} = 1850 \text{ MHz}$	-	-	2.0	dB
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Table 7. Characteristics ...continued

f = 1559 MHz to 1610 MHz; V_{CC} = 1.8 V; $V_{ENABLE} \ge 0.8$ V; P_i < -40 dBm; T_{amb} = 25 °C; input matched to 50 Ω using a 5.6 nH inductor; unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
P _{i(1dB)}	input power at 1 dB gain compression	f = 1575 MHz					
		V _{CC} = 1.5 V		-15	-12	-	dBm
		V _{CC} = 1.8 V		-14	-11	-	dBm
		V _{CC} = 2.85 V		-11	-8	-	dBm
		f = 806 MHz to 928 MHz					
		V _{CC} = 1.5 V	<u>[3]</u>	-15	-12	-	dBm
		V _{CC} = 1.8 V	<u>[3]</u>	-14	-11	-	dBm
		V _{CC} = 2.85 V	[3]	-14	-11	-	dBm
		f = 1612 MHz to 1909 MHz					
		V _{CC} = 1.5 V	<u>[3]</u>	-13	-10	-	dBm
		V _{CC} = 1.8 V	<u>[3]</u>	-12	-9	-	dBm
		V _{CC} = 2.85 V	<u>[3]</u>	-10	-7	-	dBm
IP3 _i	input third-order intercept point	f = 1.575 GHz					
		V _{CC} = 1.5 V	<u>[4]</u>	5	8	-	dBm
		V _{CC} = 1.8 V	<u>[4]</u>	5	9	-	dBm
		V _{CC} = 2.85 V	<u>[4]</u>	5	12	-	dBm
t _{on}	turn-on time		<u>[5]</u>	-	-	2	μS
t _{off}	turn-off time		<u>[5]</u>	-	-	1	μS
K	Rollett stability factor			1	-	-	

^[1] PCB losses are subtracted.

Table 8. ENABLE (pin 5)

 $-40~^{\circ}\text{C} \le T_{amb} \le +85~^{\circ}\text{C}$; 1.5 V \le V_{CC} \le 2.85 V

V _{ENABLE} (V)	State
≤ 0.35	OFF
≥ 0.8	ON

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^[2] Including PCB losses.

^[3] Out of band.

^[4] $f_1 = 1713 \text{ MHz}$; $f_2 = 1851 \text{ MHz}$; $P_1 = P_2 = -30 \text{ dBm}$.

^[5] Within 10 % of the final gain.

BGU7005 NXP Semiconductors

Application information

8.1 GNSS LNA

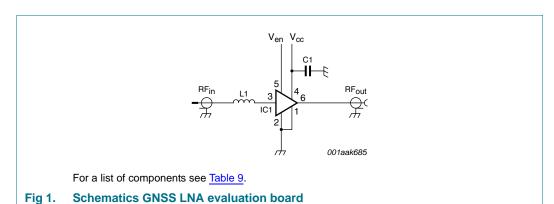
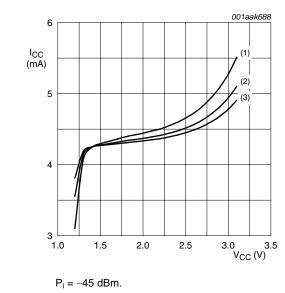


Table 9. List of components For schematics see Figure 1.

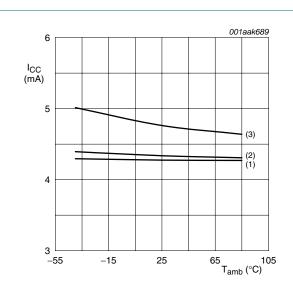
Component	Description	Value	Supplier	Remarks
C1	decoupling capacitor	1 nF	various	
IC1	BGU7005	-	NXP	
L1	high quality matching inductor	5.6 nH	Murata LQW15A	





- (2) $T_{amb} = +25 \, ^{\circ}C$
- (3) $T_{amb} = +85 \, ^{\circ}C$

Supply current as a function of supply voltage; Fig 2. typical values



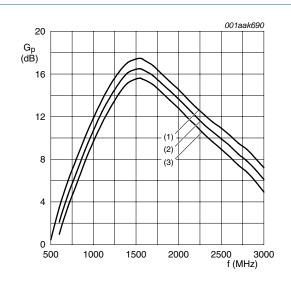
 $P_i = -45 \text{ dBm}.$

- (1) $V_{CC} = 1.5 \text{ V}$
- (2) $V_{CC} = 1.8 \text{ V}$
- (3) $V_{CC} = 2.85 \text{ V}$

Supply current as a function of ambient Fig 3. temperature; typical values

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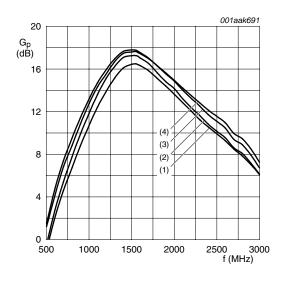
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$$V_{CC} = 1.8 \text{ V}; P_i = -45 \text{ dBm}.$$

- (1) $T_{amb} = -40 \, ^{\circ}C$
- (2) $T_{amb} = +25 \, ^{\circ}C$
- (3) $T_{amb} = +85 \, ^{\circ}C$

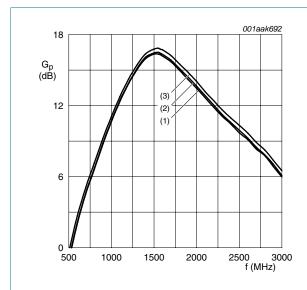
Fig 4. Power gain as a function of frequency; typical values



$$V_{CC} = 1.8 \text{ V}; T_{amb} = 25 ^{\circ}\text{C}.$$

- (1) $P_i = -45 \text{ dBm}$
- (2) $P_i = -30 \text{ dBm}$
- (3) $P_i = -20 \text{ dBm}$
- (4) $P_i = -15 \text{ dBm}$

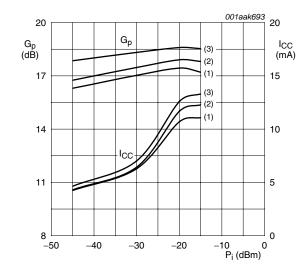
Fig 5. Power gain as a function of frequency; typical values



 $P_i = -45 \text{ dBm}; T_{amb} = 25 \text{ }^{\circ}\text{C}.$

- (1) $V_{CC} = 1.5 \text{ V}$
- (2) $V_{CC} = 1.8 \text{ V}$
- (3) $V_{CC} = 2.85 \text{ V}$

Fig 6. Power gain as a function of frequency; typical values



 $T_{amb} = 25 \, ^{\circ}C; f = 1575 \, MHz.$

- (1) $V_{CC} = 1.5 \text{ V}$
- (2) $V_{CC} = 1.8 \text{ V}$
- (3) $V_{CC} = 2.85 \text{ V}$

Fig 7. Power gain as a function of input power; typical values

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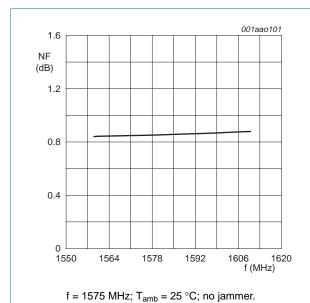
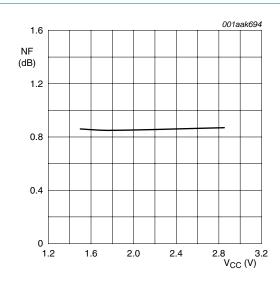


Fig 8. Noise figure as a function of supply voltage; typical values



f = 1575 MHz; $T_{amb} = 25 \,^{\circ}\text{C}$; no jammer.

Fig 9. Noise figure as a function of ambient temperature; typical values

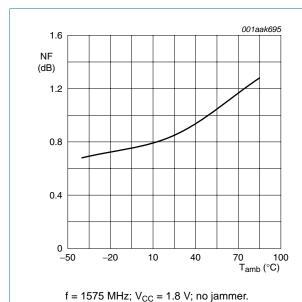
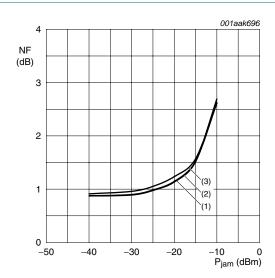


Fig 10. Noise figure as a function of ambient temperature; typical values

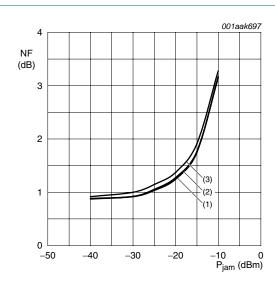


 f_{jam} = 850 MHz; T_{amb} = 25 °C; f = 1575 MHz.

- (1) $V_{CC} = 1.5 \text{ V}$
- (2) $V_{CC} = 1.8 \text{ V}$
- (3) $V_{CC} = 2.85 \text{ V}$

Fig 11. Noise figure as a function of jamming power; typical values

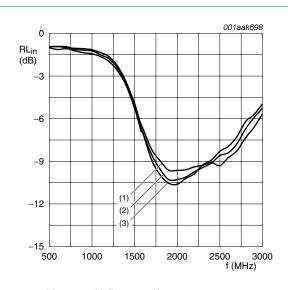
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 $f_{jam} {=}~1850~MHz; \, T_{amb} {=}~25~^{\circ}C; \, f {=}~1575~MHz.$

- (1) $V_{CC} = 1.5 \text{ V}$
- (2) $V_{CC} = 1.8 \text{ V}$
- (3) $V_{CC} = 2.85 \text{ V}$

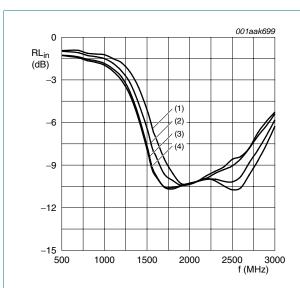
Fig 12. Noise figure as a function of jamming power; typical values



 $V_{CC} = 1.8 \text{ V}; P_i = -45 \text{ dBm}.$

- (1) $T_{amb} = -40 \, ^{\circ}C$
- (2) $T_{amb} = +25 \, ^{\circ}C$
- (3) $T_{amb} = +85 \, ^{\circ}C$

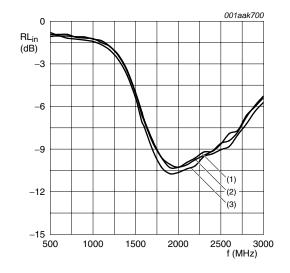
Fig 13. Input return loss as a function of frequency; typical values



 $V_{CC} = 1.8 \text{ V}; T_{amb} = 25 ^{\circ}\text{C}.$

- (1) $P_i = -45 \text{ dBm}$
- (2) $P_i = -30 \text{ dBm}$
- (3) $P_i = -20 \text{ dBm}$
- (4) $P_i = -15 \text{ dBm}$

Fig 14. Input return loss as a function of frequency; typical values

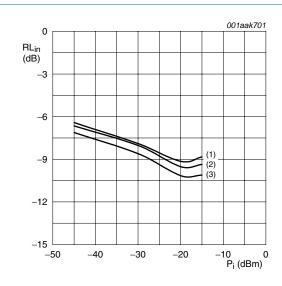


 $P_i = -45 \text{ dBm}$; $T_{amb} = 25 \text{ }^{\circ}\text{C}$.

- (1) $V_{CC} = 1.5 \text{ V}$
- (2) $V_{CC} = 1.8 \text{ V}$
- (3) $V_{CC} = 2.85 \text{ V}$

Fig 15. Input return loss as a function of frequency; typical values

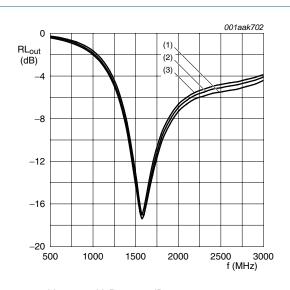
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$$T_{amb}$$
 = 25 °C; f = 1575 MHz.

- (1) $V_{CC} = 1.5 \text{ V}$
- (2) $V_{CC} = 1.8 \text{ V}$
- (3) $V_{CC} = 2.85 \text{ V}$

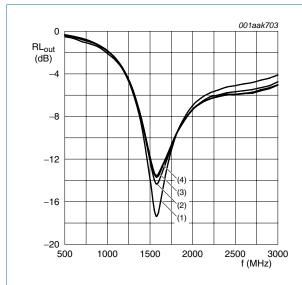
Fig 16. Input return loss as a function of input power; typical values



$$V_{CC} = 1.8 \text{ V}; P_i = -45 \text{ dBm}.$$

- (1) $T_{amb} = -40 \, ^{\circ}C$
- (2) $T_{amb} = +25 \, ^{\circ}C$
- (3) $T_{amb} = +85 \, ^{\circ}C$

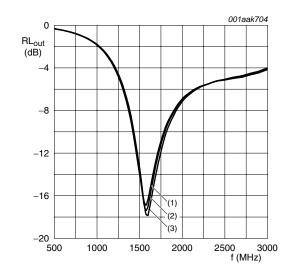
Fig 17. Output return loss as a function of frequency; typical values



 $V_{CC} = 1.8 \text{ V}; T_{amb} = 25 ^{\circ}\text{C}.$

- (1) $P_i = -45 \text{ dBm}$
- (2) $P_i = -30 \text{ dBm}$
- (3) $P_i = -20 \text{ dBm}$
- (4) $P_i = -15 \text{ dBm}$

Fig 18. Output return loss as a function of frequency; typical values

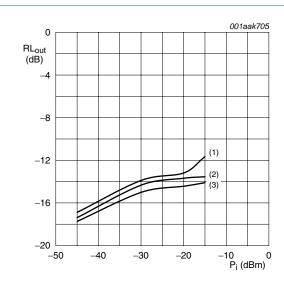


 $P_i = -45 \text{ dBm}$; $T_{amb} = 25 \text{ }^{\circ}\text{C}$.

- (1) $V_{CC} = 1.5 \text{ V}$
- (2) $V_{CC} = 1.8 \text{ V}$
- (3) $V_{CC} = 2.85 \text{ V}$

Fig 19. Output return loss as a function of frequency; typical values

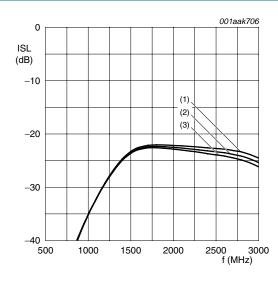
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$$T_{amb}$$
 = 25 °C; f = 1575 MHz.

- (1) $V_{CC} = 1.5 \text{ V}$
- (2) $V_{CC} = 1.8 \text{ V}$
- (3) $V_{CC} = 2.85 \text{ V}$

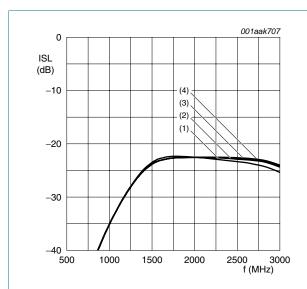
Fig 20. Output return loss as a function of input power; typical values



$$V_{CC} = 1.8 \text{ V}; P_i = -45 \text{ dBm}.$$

- (1) $T_{amb} = -40 \, ^{\circ}C$
- (2) $T_{amb} = +25 \, ^{\circ}C$
- (3) $T_{amb} = +85 \, ^{\circ}C$

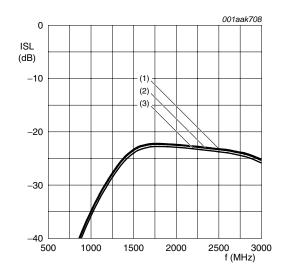
Fig 21. Isolation as a function of frequency; typical values



 $V_{CC} = 1.8 \text{ V}; T_{amb} = 25 \,^{\circ}\text{C}.$

- (1) $P_i = -45 \text{ dBm}$
- (2) $P_i = -30 \text{ dBm}$
- (3) $P_i = -20 \text{ dBm}$
- (4) $P_i = -15 \text{ dBm}$

Fig 22. Isolation as a function of frequency; typical values



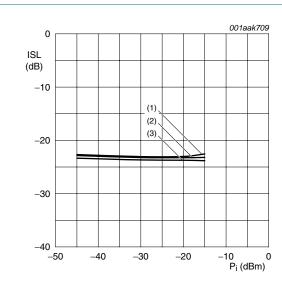
 $P_i = -45 \text{ dBm}$; $T_{amb} = 25 \text{ }^{\circ}\text{C}$.

- (1) $V_{CC} = 1.5 \text{ V}$
- (2) $V_{CC} = 1.8 \text{ V}$
- (3) $V_{CC} = 2.85 \text{ V}$

Fig 23. Isolation as a function of frequency; typical values

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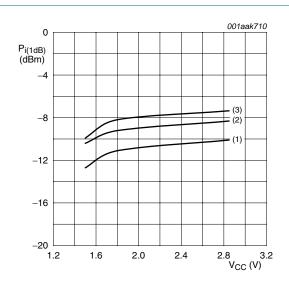
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$$T_{amb}$$
 = 25 °C; f = 1575 MHz.

- (1) $V_{CC} = 1.5 \text{ V}$
- (2) $V_{CC} = 1.8 \text{ V}$
- (3) $V_{CC} = 2.85 \text{ V}$

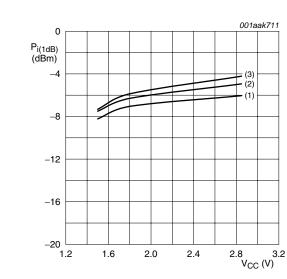
Fig 24. Isolation as a function of input power; typical values



f = 850 MHz.

- (1) $T_{amb} = -40 \, ^{\circ}C$
- (2) $T_{amb} = +25 \, ^{\circ}C$
- (3) $T_{amb} = +85 \, ^{\circ}C$

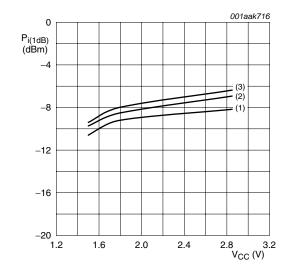
Fig 25. Input power at 1 dB gain compression as a function of supply voltage; typical values



f = 1850 MHz.

- (1) $T_{amb} = -40 \, ^{\circ}C$
- (2) $T_{amb} = +25 \, ^{\circ}C$
- (3) $T_{amb} = +85 \, ^{\circ}C$

Fig 26. Input power at 1 dB gain compression as a function of supply voltage; typical values

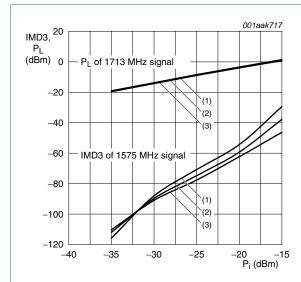


f = 1575 MHz.

- (1) $T_{amb} = -40 \, ^{\circ}C$
- (2) $T_{amb} = +25 \, ^{\circ}C$
- (3) $T_{amb} = +85 \, ^{\circ}C$

Fig 27. Input power at 1 dB gain compression as a function of supply voltage; typical values

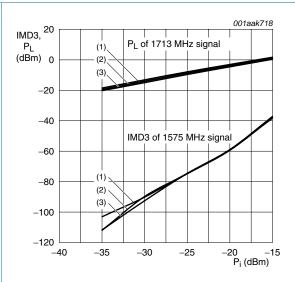
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f = 1575 MHz; f_1 = 1713 MHz; f_2 = 1851 MHz; T_{amb} = 25 °C.

- (1) $V_{CC} = 1.5 \text{ V}$
- (2) $V_{CC} = 1.8 \text{ V}$
- (3) $V_{CC} = 2.85 \text{ V}$

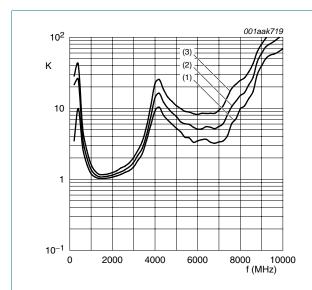
Fig 28. Third order intermodulation distortion and output power as function of input power; typical values



 $f = 1575 \text{ MHz}; f_1 = 1713 \text{ MHz}; f_2 = 1851 \text{ MHz}; V_{CC} = 1.8 \text{ V}.$

- (1) $T_{amb} = -40 \, ^{\circ}C$
- (2) $T_{amb} = +25 \, ^{\circ}C$
- (3) $T_{amb} = +85 \, ^{\circ}C$

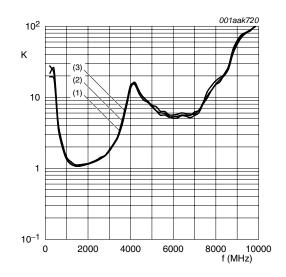
Fig 29. Third order intermodulation distortion and output power as function of input power; typical values



 $T_{amb} = 25 \, ^{\circ}C; P_i = -45 \, dBm.$

- (1) $V_{CC} = 1.5 \text{ V}$
- (2) $V_{CC} = 1.8 \text{ V}$
- (3) $V_{CC} = 2.85 \text{ V}$

Fig 30. Rollett stability factor as a function of frequency; typical values



 $V_{CC} = 1.8 \text{ V}; P_i = -45 \text{ dBm}.$

- (1) $T_{amb} = -40 \, ^{\circ}C$
- (2) $T_{amb} = +25 \, ^{\circ}C$
- (3) $T_{amb} = +85 \, ^{\circ}C$

Fig 31. Rollett stability factor as a function of frequency; typical values

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8.2 GPS front-end

The GPS LNA is typically used in a GPS front-end. A GPS front-end application circuit and its characteristics is provided here.

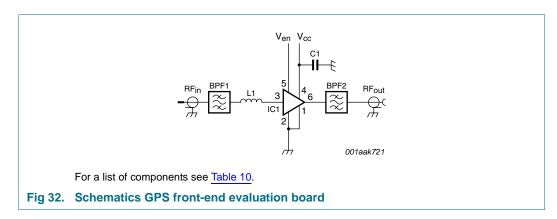


Table 10. List of components For schematics see Figure 32.

Component	Description	Value	Supplier	Remarks
BPF1, BPF2	GPS SAW filter	-	Murata SAFEA1G57KE0F00	Alternatives from Epcos:
				• B9444
				Alternatives from Murata:
				 SAFEA1G57KH0F00
				 SAFEA1G57KB0F00
				Alternatives from Fujitsu:
				 FAR-F6KA-1G5754-L4AA
				 FAR-F6KA-1G5754-L4AJ
C1	decoupling capacitor	1 nF	Various	
IC1	BGU7005	-	NXP	
L1	high quality matching inductor	5.6 nH	Murata LQW15A	

8.3 Characteristics GPS front-end

Table 11. Characteristics GPS front-end

f = 1575 MHz; $V_{CC} = 1.8$ V; $V_{ENABLE} \ge 0.8$ V; power at LNA input $P_i < -40$ dBm; $T_{amb} = 25$ °C; input and output matched to 50 Ω ; unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Cyllibol	i didilictor	Odianions	141111	ıур	Max	Oilit
V_{CC}	supply voltage	RF input AC coupled	1.5	-	2.85	V
I_{CC}	supply current		-	4.5	-	mA
T _{amb}	ambient temperature		-40	+25	+85	°C
G _p	power gain	power at LNA input P _i < -40 dBm	1] _	14.5	-	dB
		power at LNA input P _i = −20 dBm	1] _	15.5	-	dB
RL_{in}	input return loss	power at LNA input P _i < -40 dBm	1] _	8.5	-	dB
	power at LNA input P _i = -20 dBm	1] _	10.5	-	dB	
RL _{out}	output return loss	power at LNA input P _i < -40 dBm	1] _	14.5	-	dB
		power at LNA input P _i = −20 dBm	<u>1]</u> _	12.5	-	dB

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Table 11. Characteristics GPS front-end ...continued

f = 1575 MHz; $V_{CC} = 1.8$ V; $V_{ENABLE} \ge 0.8$ V; power at LNA input $P_i < -40$ dBm; $T_{amb} = 25$ °C; input and output matched to 50 Ω ; unless otherwise specified.

Symbol	Parameter	Conditions	М	in	Тур	Max	Unit
NF	noise figure	power at LNA input P _i < -40 dBm	[1] -		1.8	-	dB
		power at LNA input P _i = −20 dBm	[1] -		1.9	-	dB
P _{i(1dB)}	input power at 1 dB gain compression	f = 1575 MHz			-8.2		dBm
		f = 806 MHz to 928 MHz	[2]		31		dBm
		f = 1612 MHz to 1909 MHz	[2]		40		dBm
IP3 _i	input third-order intercept point		[3]		64		dBm
α	attenuation	f = 850 MHz	[4] 95	5	-	-	dBc
		f = 1850 MHz	[<u>4</u>] 90)	-	-	dBc
t _{on}	turn-on time		[5] _		-	2	μS
t _{off}	turn-off time		[5] _		-	1	μS

^[1] Power at GPS front-end input = power at LNA input + attenuation BPF1.

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^[2] Out of band.

^[3] $f_1 = 1713 \text{ MHz}$; $f_2 = 1851 \text{ MHz}$; $P_1 = P_2 = +10 \text{ dBm}$.

^[4] Relative to f = 1575 MHz.

^[5] Within 10 % of the final gain.

9. Package outline

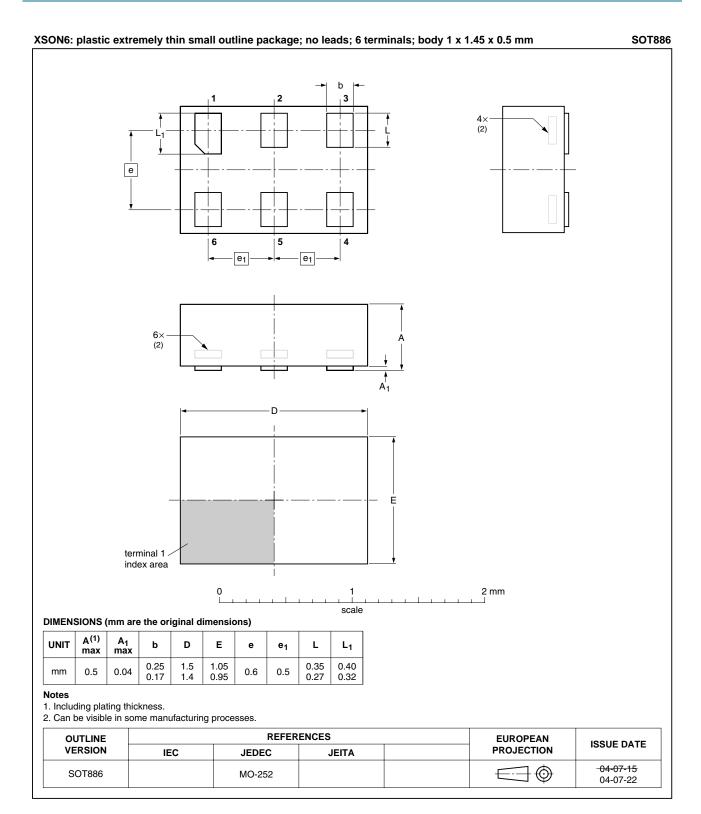


Fig 33. Package outline SOT886 (XSON6)

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10. Abbreviations

Table 12. Abbreviations

Acronym	Description
AC	Alternating Current
GNSS	Global Navigation Satellite System
GPS	Global Positioning System
HBM	Human Body Model
LNA	Low Noise Amplifier
MMIC	Monolithic Microwave Integrated Circuit
PDA	Personal Digital Assistant
RF	Radio Frequency
SAW	Surface Acoustic Wave
SiGe:C	Silicon Germanium Carbon

11. Revision history

Table 13. Revision history

Table 15. Kevisi	ion mistory					
Document ID	Release date	Data sheet status	Change notice	Supersedes		
BGU7005 v.4	20110506	Product data sheet	-	BGU7005 v.3		
Modifications:	Section 1.2 o	n page 1: removed 'optimized fo	r 1.8 V' at list no.12.			
	 <u>Table 1 on page 2</u>: some values I_{CC} have been changed/added. 					
	 Table 5 on pa 	age 3: added V _{ENABLE} ,V _{RF_IN} and	dV_{RF_OUT} and notes [2] are	nd [3].		
	• Table 7 on pa	age 4: several changes have bee	en made.			
BGU7005 v.3	20100623	Product data sheet	-	BGU7005_2		
BGU7005_2	20100304	Product data sheet	-	BGU7005_1		
BGU7005_1	20091028	Preliminary data sheet	-	-		

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12. Legal information

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Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions".
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