# Thick Film Resistor Networks Single-In-Line, Coated SIP 01, 03, 05 Schematics 

FEATURES

- 0.195 " [4.95mm] "A", 0.250 " [6.35mm] "B"
- "A" profile standard in 4 thru 12 pins
- Highly stable thick film
- Low temperature coefficient $\left(-55^{\circ} \mathrm{C}\right.$ to $\left.+125^{\circ} \mathrm{C}\right) \pm 100 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$
- Reduces total assembly costs
- Resistor elements protected by tough epoxy conformal coating
- Wide resistance range
- Available in bag pack or tube pack

STANDARD ELECTRICAL SPECIFICATIONS

| $\begin{aligned} & \text { MODEL/ } \\ & \text { SCHEMATIC } \end{aligned}$ | PROFILE | RESISTOR POWER RATING Max. @ $70^{\circ} \mathbf{C}^{*}$ | RESISTANCE RANGE $\Omega$ | STANDARD TOLERANCE \% | $\begin{gathered} \text { TEMPERATURE } \\ \text { COEFFICIENT } \\ \left(-55^{\circ} \mathrm{C} \text { to }+125^{\circ} \mathrm{C}\right) \end{gathered}$ | TCR TRACKING $\left(-55^{\circ} \mathrm{C}\right.$ to $\left.+125^{\circ} \mathrm{C}\right)$ | OPERATING VOLTAGE VDC Max. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| CSC01 | $\begin{aligned} & \mathrm{A} \\ & \mathrm{~B} \end{aligned}$ | $\begin{aligned} & 0.20 \mathrm{~W} \\ & 0.25 \mathrm{~W} \end{aligned}$ | 10-2.2M | $\pm 2\left(1 \%{ }^{*}\right)$ | $\pm 100 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$ | $\pm 50 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$ | 100 |
| CSC03 | $\begin{aligned} & \hline \text { A } \\ & \text { B } \end{aligned}$ | $\begin{aligned} & 0.30 \mathrm{~W} \\ & 0.40 \mathrm{~W} \end{aligned}$ | 10-2.2M | $\pm 2\left(1 \%{ }^{*}\right)$ | $\pm 100 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$ | $\pm 50 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$ | 100 |
| CSC05 | $\begin{aligned} & \mathrm{A} \\ & \mathrm{~B} \end{aligned}$ | $\begin{aligned} & 0.20 \mathrm{~W} \\ & 0.25 \mathrm{~W} \end{aligned}$ | 10-2.2M | $\pm 2\left(1 \%{ }^{*}\right)$ | $\pm 100 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$ | $\pm 150 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$ | 100 |

* For resistor power ratings @ + $25^{\circ} \mathrm{C}$ see derating curves.
-See derating curves for Package Power Rating. Higher power rated "C" Profile available.
* Contact factory for $1 \%$

TECHNICAL SPECIFICATIONS

| PARAMETER | UNIT | CSC Series |
| :--- | :---: | :---: |
| Voltage Coefficient of Resistance | $\mathrm{V}_{\text {eff }}$ | $<50 \mathrm{ppm}$ typical |
| Dielectric Strength | VAC | 200 |
| Isolation Resistance (03 Schematic) | $\Omega$ | $>100 \mathrm{M}$ |
| Operating Temperature Range | ${ }^{\circ} \mathrm{C}$ | -55 to +125 |

## ORDERING INFORMATION

| 01 and 03 Schematics |  |  | 01 03 |  |  | TOLERANCE |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\begin{aligned} & \text { CSC } \\ & \text { MODEL } \end{aligned}$ | $\begin{gathered} 08 \\ \text { NUMBER OF } \\ \text { PINS } \end{gathered}$ | $\stackrel{\mathbf{A}}{\text { PACKAGE CODE }}$ | $\begin{gathered} 03 \\ \text { SCHEMATIC } \end{gathered}$ |  | 101 TANCE ALUE |  |
|  |  | A $=0.195^{\prime \prime}[4.95 \mathrm{~mm}]$ Height <br> $0.100^{\prime \prime}[2.54 \mathrm{~mm}]$  <br> B $=0.250^{[ }[6.35 \mathrm{~mm}]$ Heaght Spacing <br> $0.100 "[2.54 \mathrm{~mm}]$ Lead Spacing  | $01=$ Pin \#1 common to all resistors 03 = Isolated resistors | First 2 signific Last dig number follow. | gits are nt figures. specifies of zeros to | $\mathrm{G}= \pm 2 \%$ |
| $\begin{aligned} & 05 \text { Schematic } \\ & \text { CSC } \\ & \text { MODEL } \end{aligned}$ | $\begin{gathered} 08 \\ \text { NUMBER OF } \\ \text { PINS } \end{gathered}$ | $\begin{gathered} \text { PaCKAGE CODE } \end{gathered}$ | $\begin{gathered} 05 \\ \text { SCHEMATIC } \end{gathered}$ | $\begin{aligned} & 221 \\ & \text { RESISTANCE }^{\text {VALUE R }} \text {. } \end{aligned}$ | $\begin{aligned} & 331 \\ & \text { RESISTANCE } \\ & \text { VALUE R } 2 \end{aligned}$ | $\begin{gathered} \mathrm{G} \\ \text { TOLERANCE } \end{gathered}$ |
|  |  | $\begin{aligned} & \mathrm{A}=0.195 "[4.95 \mathrm{~mm}] \text { Height } \\ & 0.100^{\prime \prime}[2.54 \mathrm{~mm}] \text { Lead Spacing } \\ & \mathrm{B}\left.=0.2500^{[\mid 6.35 \mathrm{~mm}] \text { Height }} 0.100 \mathrm{l} 2.54 \mathrm{~mm}\right] \text { Lead Spacing } \end{aligned}$ |  | First two digits figures. Th specifies the zeros | are significant third digit number of follow. | $\mathrm{G}= \pm 2 \%$ |

CSC
Thick Film Resistor Networks, Single-In-Line, Coated SIP Vishay Dale
DIMENSIONS in inches [millimeters]


| 01 Schematic | MODEL | NUMBER OF RESISTORS | A (Maximum) | B | C (Maximum) |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | CSC04 | 3 | 0.390 [9.90] | 0.300 [7.62] | $\begin{aligned} \text { "A" Profile } & =0.195[4.95] \\ \text { "B" Profile } & =0.250[6.35] \end{aligned}$ |
|  | CSC05 | 4 | 0.490 [12.45] | 0.400 [10.16] |  |
|  | CSC06 | 5 | 0.590 [14.99] | 0.500 [12.70] |  |
|  | CSC07 | 6 | 0.690 [17.53] | 0.600 [15.24] |  |
|  | CSC08 | 7 | 0.790 [20.07] | 0.700 [17.78] |  |
|  | CSC09 | 8 | 0.890 [22.61] | 0.800 [20.32] |  |
|  | CSC10 | 9 | 0.990 [25.15] | 0.900 [22.86] |  |
|  | CSC11* | 10 | 1.09 [27.69] | 1.00 [25.40] |  |
|  | CSC12* | 11 | 1.19 [30.23] | 1.100 [27.94] |  |
|  | MODEL | NUMBER OF RESISTORS | A (Maximum) | B | C (Maximum) |
|  | CSC04 | 2 | 0.390 [9.90] | 0.300 [7.62] | $\begin{aligned} & \text { "A" Profile }=0.195 \text { [4.95] } \\ & \text { "B" Profile }=0.250[6.35] \end{aligned}$ |
|  | CSC06 | 3 | 0.590 [14.99] | 0.500 [12.70] |  |
|  | CSC08 | 4 | 0.790 [20.07] | 0.700 [17.78] |  |
|  | CSC10 | 5 | 0.990 [25.15] | 0.900 [22.86] |  |
|  | CSC12* | 6 | 1.19 [30.23] | 1.100 [27.94] |  |
| 05 Schematic | MODEL | NUMBER OF RESISTORS | A (Maximum) | B | C (Maximum) |
|  | CSC04 | 4 | 0.390 [9.90] | 0.300 [7.62] | $\begin{aligned} & \text { "A" Profile }=0.195[4.95] \\ & \text { "B" Profile }=0.250[6.35] \end{aligned}$ |
|  | CSC05 | 6 | 0.490 [12.45] | 0.400 [10.16] |  |
|  | CSC06 | 8 | 0.590 [14.99] | 0.500 [12.70] |  |
|  | CSC07 | 10 | 0.690 [17.53] | 0.600 [15.24] |  |
|  | CSC08 | 12 | 0.790 [20.07] | 0.700 [17.78] |  |
|  | CSC09 | 14 | 0.890 [22.61] | 0.800 [20.32] |  |
|  | CSC10 | 16 | 0.990 [25.15] | 0.900 [22.86] |  |
|  | CSC11* | 18 | 1.09 [27.69] | 1.00 [25.40] |  |
|  | CSC12* | 20 | 1.19 [30.23] | 1.100 [27.94] |  |

"B" Profile only.

## MECHANICAL SPECIFICATIONS

| Marking Resistance <br> to Solvents: | Permanency testing per MIL-STD- <br> 202, Method 215. |
| :--- | :--- |
| Solderability: | Per MIL-STD-202, Method 208E, <br> RMA flux. |
| Body: | High alumina, epoxy coated. |
| Terminals: | Copper alloy, solder plated. |

## STOCKED RESISTANCE VALUES IN OHMS ("G" TOLERANCE)

Standard E-24 resistance values stocked. Consult factory.
Many dual terminator resistance values stocked. Consult factory

## "A" Profile



Derating
Ambient Temperature ${ }^{\circ} \mathrm{C}$
"A" PROFILE + 70 C PACKAGE RATINGS

| CSC12A | 1.5 watts |
| :--- | :--- |
| CSC11A | 1.37 watts |
| CSC10A | 1.25 watts |
| CSC09A | 1.12 watts |
| CSC08A | 1.00 watts |
| CSC07A | 0.87 watts |
| CSC06A | 0.75 watts |
| CSC05A | 0.62 watts |
| CSC04A | 0.40 watts |


| "B" PROFILE $\mathbf{+ 7 0}^{\circ} \mathbf{C}$ PACKAGE RATINGS |  |
| :---: | :---: |
| CSC12B | 1.90 watts |
| CSC11B | 1.75 watts |
| CSC10B | 1.60 watts |
| CSC09B | 1.45 watts |
| CSC08B | 1.30 watts |
| CSC07B | 1.15 watts |
| CSC06B | 1.00 watts |
| CSC05B | 0.80 watts |
| CSC04B | 0.60 watts |

## CIRCUIT APPLICATIONS



## 03 Schematic


"A" Profile = 3, 5, 7, 9 and 11 resistors with one pin common
The CSCxxx-01 single-in-line resistor networks provide the user with nominally equal resistors, each connected to a common pin (Pin No. 1). Commonly used in the following applications:

- "Wired OR" Pull-up - Open Collector Pull-up
- Power Gate Pull-up - TTL Input Pull-down
- MOS/ROM Pull-up/Pull-down - TTL Unused Gate Pull-up
* "B" Profile available. Odd pin available in 5, 7, 9, and 11.
"A" Profile = 2 through 6 isolated resistors
The CSCxxx-03 single-in-line resistor networks provide the user with nominally equal resistors. Each resistor is isolated from all others. Commonly used in the following applications:
- "Wired OR" Pull-up • Long-Line Impedance Balancing
- Power Driven Pull-up •LED Current Limiting
- Power Gate Pull-up •ECL Output Pull-down
- Line Termination
- TTL Input Pull-down
* "B" Profile available.



## Pulse squaring and TTL dual-line terminators

The CSCxxx-05 circuits contain series pairs of resistors. Each series pair is connected between two common lines. The junction of these resistor pairs is connected to the input terminals. The 05 circuits are designed for TTL dualline termination and pulse squaring.
*"B" Profile available. Odd pin available in 5, 7, 9 and 11.

| PERFORMANCE |  |  |
| :--- | :--- | :---: |
| TEST | CONDITIONS | MAX. $\Delta R$ (Typical Test Lots) |
| Thermal Shock | 5 cycles between $-65^{\circ} \mathrm{C}$ and $+125^{\circ} \mathrm{C}$ | $\pm 0.50 \% \Delta R$ |
| Short Time Overload | 2.5 x rated working voltage, 5 seconds | $\pm 0.25 \% \Delta R$ |
| Low Temperature Operation | 45 minutes at full rated working voltage at $-65^{\circ} \mathrm{C}$ | $\pm 0.25 \% \Delta R$ |
| Moisture Resistance | 240 hours with humidity ranging from $80 \% \mathrm{RH}$ to $98 \% \mathrm{RH}$ | $\pm 1.00 \% \Delta R$ |
| Resistance to Soldering Heat | Leads immersed in $+350^{\circ} \mathrm{C}$ solder to within $1 / 16$ " of body for 3 seconds | $\pm 0.25 \% \Delta R$ |
| Shock | Total of 18 shocks at 100 G's | $\pm 0.25 \% \Delta R$ |
| Vibration | 12 hours at maximum of 20 G's between 10 and $2,000 \mathrm{~Hz}$ | $\pm 0.25 \% \Delta R$ |
| Load Life | 1,000 hours at $+70^{\circ} \mathrm{C}$, rated power applied 1.5 hours "ON", 0.5 hour <br> "OFF" for full 1000 hour period. Derated according to the curve. | $\pm 1.00 \% \Delta R$ |
| Terminal Strength | 4.5 pound pull for 30 seconds | $\pm 0.25 \% \Delta R$ |
| Insulation Resistance | 10,000 Megohm (minimum) | - |
| Dielectric Withstanding Voltage | No evidence of arcing or damage (200 V RMS for 1 minute) | - |

