

EVALUATION BOARD

General Description

The EV0007 is a 2A evaluation board based on the MP1580 DC to DC Converter. The MP1580 is a monolithic step down switch mode converter with a built in internal Power MOSFET. It achieves 2A continuous output current over a wide input supply range with excellent load and line regulation.

Current mode operation provides fast transient response and eases loop stabilization. Fault condition protection includes cycle-by-cycle current limiting and thermal shutdown. In shutdown mode the regulator draws 23µA of supply current.

The MP1580 requires a minimum number of readily available standard external components. A synchronization pin allows the part to be driven to 600KHz.

Ordering Information

Board Number	MPS IC Number
EV0007	MP1580HS

^{*} For Tape & Reel use suffix - Z (e.g. MP1580HS-Z)

Absolute Maximum Ratings

28V
-1V to VIN +1V
V_{SW} + 6V
-0.3 to 6V
-0.3 to 6V
-0.3 to 6V
-0.3 to 6V

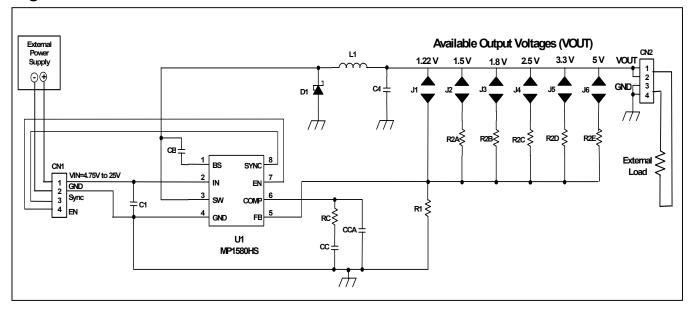
Recommended Operating ConditionsInput Voltage V_{VIN} 4.75V to 25V

Figure 1: EV0007 Evaluation Board



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Figure 2: EV0007 Schematic



Board Operation

This board is laid out to accommodate most commonly used Inductors and Output Capacitors and to be programmed for most standard Output Voltages.

- 1) Attach positive end of load to pin 1 = 2 and negative end of load to pin 3 = 4 of connector CN2.
- 2) For the required output voltage (VOUT = 1.22, 1.5, 1.8, 2.5, 3.3 or 5V) solder blob the appropriate J1 thru J6 jumper respectively.
- 3) Attach input voltage (VIN) and input ground (GND) to Pin 1 and 2 of connector CN1 respectively. Note VIN must be 4.75V < VIN < 25V.
- 4) Pin 3 of CN1 is used to synchronize the internal oscillator frequency of the MP1580 to an external source. The synchronization rate can be vary from 380KHZ (normal operation) to 600KHz. There is an internal $11K\Omega$ pull down resistor to GND **therefore leave this pin unconnected if unused.**
- 5) Applying a voltage greater than 2.495V to pin 4 of CN1 enables the MP1580. Leave the input unconnected if unused. An Under Voltage Lockout (UVLO) function can be implemented by the addition of a resistor divider from VIN to GND. For complete low current shutdown its needs to be less than 0.7V.

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Table 1: EV0007 Bill of Materials

#	Qty	Reference	Part Number	
1	1	U1	MP1580HS	
2	2	CN1, CN2	4 Pin Connector	
3	1	СВ	10nF 0603	
4	1	CC	22nF 0603	
5	1	CCA	100pF 0603	
6	1	C1	10μF 25V 1210	
7	1	C4	22μF 10V Y5V 1210	
8	1	D1	B230/SM 2A 30V Schottky	
9	1	L1	22µH	
10	2	RC, R1	10ΚΩ 1%	
11	1	R2A	2.32K 1%	
12	1	R2B	4.75K 1%	
13	1	R2C	10.5K 1%	
14	1	R2D	16.9K 1%	
15	1	R2E	30.9K 1%	

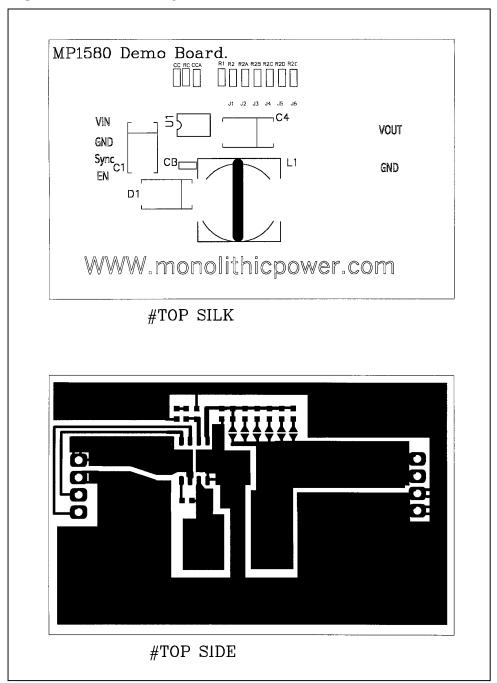
Table 2. Recommended components for standard output voltages

VOUT	Jumper Connection	L1 minimum	RC
1.22V	J1	6.8µH	4.7ΚΩ
1.5V	J2	6.8µH	4.7ΚΩ
1.8V	J3	10µH	4.7ΚΩ
2.5V	J4	10µH	10ΚΩ
3.3V	J5	15µH	10ΚΩ
5.0V	J6	22μΗ	10ΚΩ



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Figure 3: EV0007 Layout



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