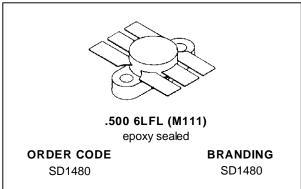


### **SD1480**

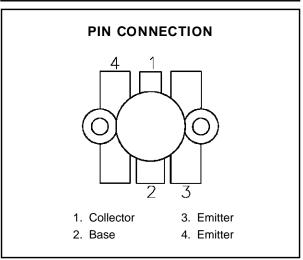
# RF & MICROWAVE TRANSISTORS VHF APPLICATIONS

- 136 175 MHz
- 28 VOLTS
- EFFICIENCY 55%
- COMMON EMITTER
- GOLD METALLIZATION
- INTERNAL INPUT MATCHING
- Pout = 125 W MIN. WITH 9.2 dB GAIN



#### **DESCRIPTION**

The SD1480 is a common emitter 28 V Class C epitaxial silicon NPN planar transistor designed primarily for VHF communications applications. This internally matched device incorporates diffused emitter ballasting resistors nad provides high gain and stable operation across the entire 136 - 175 MHz VHF communications band.



#### **ABSOLUTE MAXIMUM RATINGS** $(T_{case} = 25^{\circ}C)$

Symbol	Parameter	Value	Unit	
V <sub>CBO</sub>	Collector-Base Voltage	65	V	
V <sub>CEO</sub>	Collector-Emitter Voltage	36	V	
V <sub>CES</sub>	Collector-Emitter Voltage	65	V	
VEBO	Emitter-Base Voltage	4.0	V	
Ic	Device Current	20	А	
P <sub>DISS</sub>	Power Dissipation	270	W	
TJ	Junction Temperature +20		°C	
T <sub>STG</sub>	Storage Temperature	– 65 to +150	°C	

#### THERMAL DATA

R <sub>TH(j-c)</sub> Junction-Case Thermal Resistance	0.65	°C/W
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#### **ELECTRICAL SPECIFICATIONS** (Tcase = 25°C)

#### **STATIC**

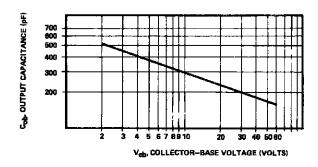
Symbol	Test Conditions	Value			Unit		
		Min.	Тур.	Max.			
ВУсво	I <sub>C</sub> = 100 mA	$I_E = 0 \text{ mA}$		65	_	_	V
BVces	I <sub>C</sub> = 100 mA	$V_{BE} = 0 V$		65	_	_	V
BV <sub>CEO</sub>	I <sub>C</sub> = 100 mA	$I_B = 0 \text{ mA}$		35	_	_	V
BV <sub>EBO</sub>	I <sub>E</sub> = 10 mA	$I_C = 0 \text{ mA}$		4.0	_	_	V
ICES	V <sub>CE</sub> = 30 V	I <sub>E</sub> = 0 mA		_	_	15	mA
hFE	Vce = 5 V	Ic = 5 A		20	_	200	_

#### **DYNAMIC**

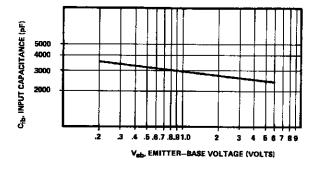
Symbol	Test Conditions		Value		Unit		
Symbol	rest Conditions			Min.	Тур.	Max.	Oiiit
Pout	f = 150 MHz	$P_{IN} = 15 W$	$V_{CE} = 28 \text{ V}$	125	_	_	W
Pg	f = 150 MHz	Pout = 125 W	$V_{CE} = 28 \text{ V}$	9.2		_	dB
ης	f = 150 MHz	$P_{OUT} = 125 \text{ W}$	$V_{CE} = 28 \text{ V}$	55		_	%
СОВ	f = 1 MHz	$V_{CB} = 28 \text{ V}$		_	_	250	pF
Load Mismatch	f = 150 MHz	$P_{IN} = 15 W$	V <sub>CE</sub> = 28 V	20:1	_	_	VSWR

#### **TYPICAL PERFORMANCE**

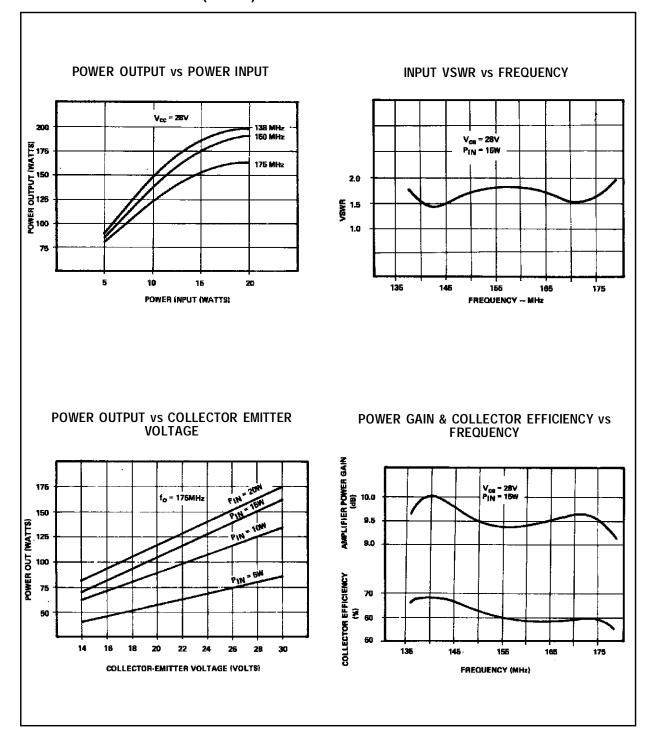
## OUTPUT CAPACITANCE vs COLLECTOR BASE VOLTAGE



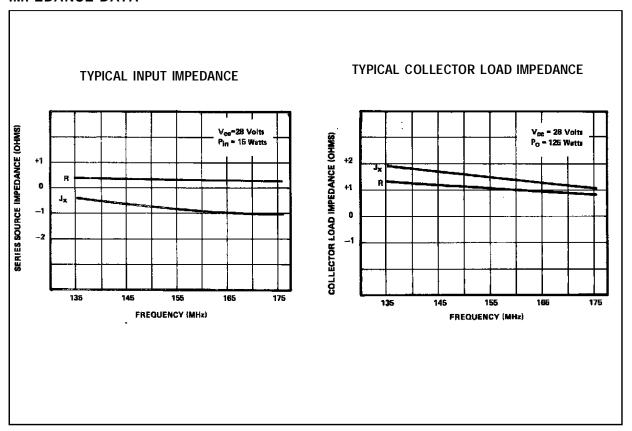
#### INPUT CAPACITANCE vs EMITTER BASE VOLTAGE



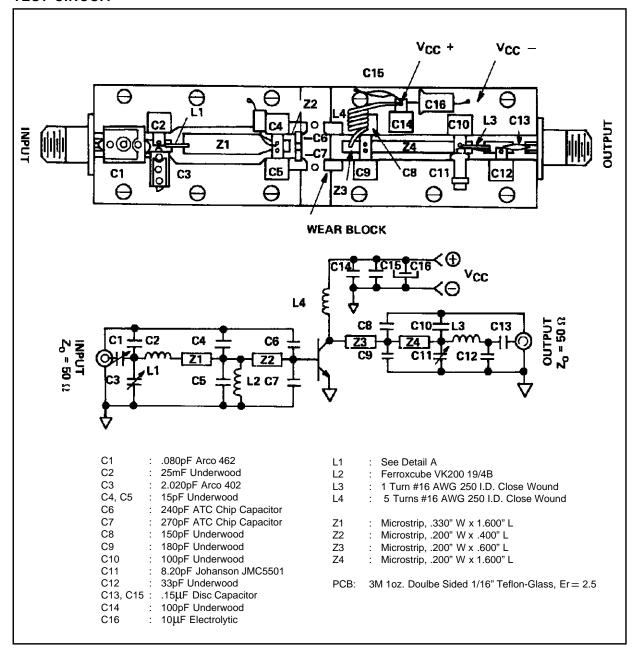
#### TYPICAL PERFORMANCE (cont'd)



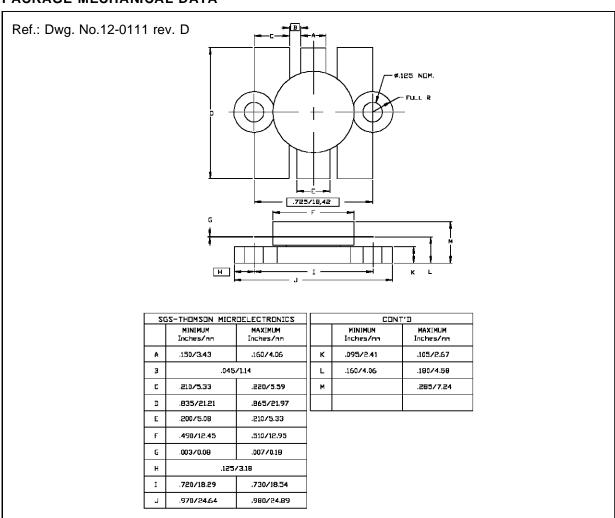
#### **IMPEDANCE DATA**



#### **TEST CIRCUIT**



#### PACKAGE MECHANICAL DATA



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