

LS5018B LS5060B/LS5120B

TRISIL™

FEATURES

- BIDIRECTIONAL CROWBAR PROTECTION.
- BREAKDOWN VOLTAGES RANGE: 18V, 60V and 120V.
- HOLDING CURRENT = 200mA min.
- HIGH SURGE CURRENT CAPABILITY IPP = 100A 10/1000 μs

DESCRIPTION

The LS50xxB series has been designed to protect telecommunication equipment against lightning and transients induced by AC power lines. Its high surge current capability makes the

Its high surge current capability makes the LS50xxB a reliable protection device for very exposed equipment, or when series resistors are very low.

COMPLIES WITH THE FOLLOWING STANDARDS:

CCITT K17 - K20	10/700	μs	1.5 kV
	5/310	μs	38 A
VDE 0433	10/700	μs	2 kV
	5/200	μs	50 A
CNET	0.5/700	μs	1.5 kV
	0.2/310	μs	38 A

ABSOLUTE MAXIMUM RATINGS (Tamb =25°C)



SCHEMATIC DIAGRAM



Symbol	Parameter	Value	Unit	
Ipp	Peak pulse current10/1000 μs 8/20 μs		100 250	A
Ітѕм	Non repetitive surge peak on-state tp = 20 ms		50	A
dl/dt	Critical rate of rise of on-state current Non rep		100	A/μs
dV/dt	Critical rate of rise of off-state voltage V _{RM}		5	kV/μs
T _{stg} Tj	Storage and operating junction temperat	- 40 to + 150 150	°C ℃	
TL	Maximum lead temperature for soldering	230	°C	

September 1998 Ed: 3A

LS5018B/LS5060B/LS5120B

THERMAL RESISTANCE

Symbol	Parameter	Value	Unit
R _{th} (j-a)	Junction to ambient on printed circuit with recommended pad layout	80	°C/W

ELECTRICAL CHARACTERISTICS (Tamb =25°C)

Symbol	Parameter			
Iгм	Leakage current at stand-off voltage			
V _{RM}	Stand-off voltage			
V _{BR}	Breakdownvoltage			
V _{BO}	Breakovervoltage			
Ін	Holding current			
I _{BO}	Breakover current			
I _{PP}	Peak pulse current			
С	Capacitance			



	I _{RM} @ V _{RM}		V _{BR} @l _R		V _{BO} @ I _{BO}		Ι _Η	С
Туре	max.		min.		max.	typ.	min.	max.
.,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,					note 1		note 2	note 3
	μΑ V V mA		mA	V	mA	mA	рF	
LS5018B	5	16	17	1	22	1300	200	150
LS5060B	10	50	60	1	85	1000	200	150
LS5120B	20	100	120	1	180	1250	250	150

Note 1 : Measured at 50Hz (1 cycle) Note 2 : See test circuit Note 3 : $V_R = 5 V$, F = 1MHz.

3/5







LS5018B/LS5060B/LS5120B

Figure 1 : Non repetitive surge peak current versus overload duration



Figure 3 : Relative variation of breakdown voltage versus ambient temperature.



Figure 2 : Relative variation of holding current versus junction temperature.



Figure 4 : Junction capacitance versus reverse applied voltage.



ORDER CODE



LS5018B/LS5060B/LS5120B

MARKING : Logo, Date Code, part Number.

Packaging: Products supplied in antistatic tubes. **Weight :** 0.59g

			DIMENSIONS					
		REF.	Millimetres			Inches		
			Min.	Тур.	Max.	Min.	Тур.	Max.
Г (аннан) Г		a1	0.70			0.027		
		В	1.39		1.65	0.055		0.065
		B1	0.91		1.04	0.036		0.041
	F →	b		0.5			0.020	
		b1	0.38		0.50	0.015		0.020
e3	€ ►	D			9.80			0.385
		E		8.8			0.346	
		е		2.54			0.100	
		e3		7.62			0.300	
		F			7.1			0.280
		Ι			4.8			0.189
		L		3.3			0.130	
		Z	0.44		1.60	0.017		0.063

PACKAGE MECHANICAL DATA DIL 8 Plastic

Information furnished is believed to be accurate and reliable. However, STMicroelectronics assumes no responsibility for the consequences of use of such information nor for any infringement of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of STMicroelectronics. Specifications mentioned in this publication are subject to change without notice. This publication supersedes and replaces all information previously supplied. STMicroelectronics or systems without express written approval of STMicroelectronics.

The ST logo is a registered trademark of STMicroelectronics

© 1998 STMicroelectronics - Printed in Italy - All rights reserved.

STMicroelectronics GROUP OF COMPANIES

Australia - Brazil - Canada - China - France - Germany - Italy - Japan - Korea - Malaysia - Malta - Mexico - Morocco -The Netherlands Singapore - Spain - Sweden - Switzerland - Taiwan - Thailand - United Kingdom - U.S.A.



5/5