

BULD118D-1

HIGH VOLTAGE FAST-SWITCHING NPN POWER TRANSISTOR

- INTEGRATED ANTIPARALLEL COLLECTOR- EMITTER DIODE
- HIGH VOLTAGE CAPABILITY
- LOW SPREAD OF DYNAMIC PARAMETERS
- MINIMUM LOT-TO-LOT SPREAD FOR RELIABLE OPERATION
- VERY HIGH SWITCHING SPEED

APPLICATIONS:

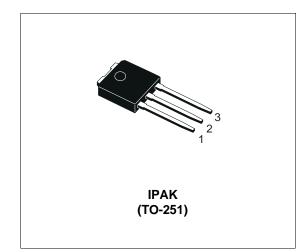
- ELECTRONIC BALLASTS FOR FLUORESCENT LIGHTING
- FLYBACK AND FORWARD SINGLE TRANSISTOR LOW POWER CONVERTERS

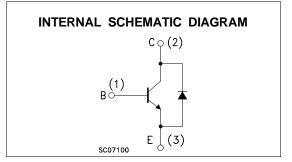
DESCRIPTION

The device is manufactured using high voltage Multi Epitaxial Planar technology for high switching speeds and medium voltage capability.

It uses a Cellular Emitter structure with planar edge termination to enhance switching speeds while maintaining the wide RBSOA.

The device is designed for use in lighting applications and low cost switch-mode power supplies.





ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V _{CES}	Collector-Emitter Voltage (V _{BE} = 0)	700	V
V _{CEO}	Collector-Emitter Voltage $(I_B = 0)$	400	V
V _{EBO}	Emitter-Base Voltage (I _C = 0)	9	V
Ic	Collector Current	2	А
I _{CM}	Collector Peak Current (t _p < 5 ms)	4	А
lв	Base Current	1	А
I _{BM}	Base Peak Current (t _p < 5 ms)	2	А
Ptot	Total Dissipation at $T_c = 25 \ ^{\circ}C$	20	W
T _{stg}	Storage Temperature	-65 to 150	°C
Tj	Max. Operating Junction Temperature	150	°C

June 2001

THERMAL DATA

R _{thj-case}	Thermal Resistar	nce Junction-Case	Max	6.25	°C/W
$R_{thj-amb}$	Thermal Resistar	nce Junction-Ambient	Max	100	°C/W

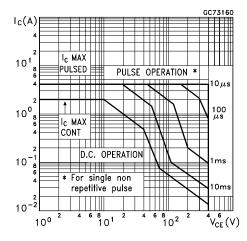
ELECTRICAL CHARACTERISTICS (T_{case} = 25 °C unless otherwise specified)

Symbol	Parameter	Test Conditions		Min.	Тур.	Max.	Unit
ICES	Collector Cut-off Current (V _{BE} = 0)	V _{CE} = 700 V V _{CE} = 700 V	T _j = 125 °C			100 500	μΑ μΑ
V_{EBO}	Emitter-Base Voltage	I _E = 10 mA		9			V
$V_{CEO(sus)}$	Collector-Emitter Sustaining Voltage	I _C = 100 mA	L = 25 mH	400			V
ICEO	Collector-Emitter Leakage Current	V _{CE} = 400 V				250	μA
V _{CE(sat)} *	Collector-Emitter Saturation Voltage	I _C = 0.5 A I _C = 1 A I _C = 2 A	$I_{B} = 0.1 A$ $I_{B} = 0.2 A$ $I_{B} = 0.4 A$			0.5 1 1.5	V V V
V _{BE(sat)} *	Base-Emitter Saturation Voltage	$I_{C} = 0.5 A$ $I_{C} = 1 A$ $I_{C} = 2 A$	$I_{B} = 0.1 A$ $I_{B} = 0.2 A$ $I_{B} = 0.4 A$			1.0 1.2 1.3	V V V
h _{FE} *	DC Current Gain	$I_{C} = 10 \text{ mA}$ $I_{C} = 0.5 \text{ A}$ $I_{C} = 2 \text{ A}$	V _{CE} = 5 V V _{CE} = 5 V V _{CE} = 5 V	10 10 8		50	
tr tf ts	RESISTIVE LOAD Rise Time Fall Time Storage Time group A group B	$V_{CC} = 125 V$ $I_{B1} = 0.4 A$ $t_p = 30 \ \mu s$	I _C = 2 A I _{B2} = -0.2 A	1.4 2	0.7 0.3	0.5 2.1 2.75	μs μs μs μs
t _s t _f	INDUCTIVE LOAD Storage Time Fall Time	$I_{C} = 1 A$ $V_{BE} = -5 V$ $V_{clamp} = 300 V$	I _{B1} = 0.2 A L = 50 mH		0.8 0.10		μs μs
VF	Diode Forward Voltage	$I_C = 1 A$				2.5	V

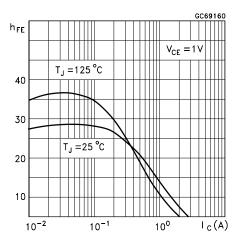
Pulsed: Pulse duration = 300 μs, duty cycle 1.5 % Note : Product is pre-selected in storage time (GROUP A and GROUP B). STMicroelectronics reserves the right to ship either groups according to production availability. Please contact your nearest STMicroelectronics sales office for delivery details.

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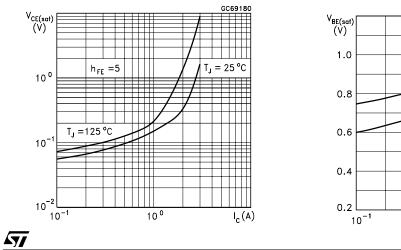
Safe Operating Areas



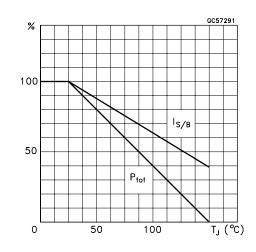
DC Current Gain



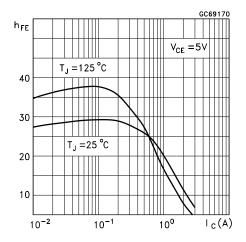
Collector Emitter Saturation Voltage



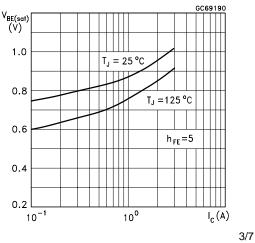
Derating Curve



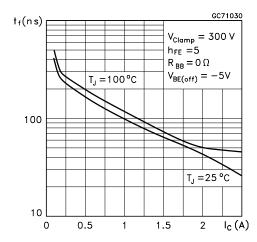
DC Current Gain



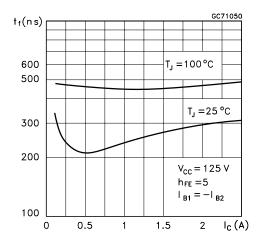




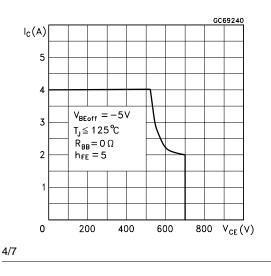
Inductive Fall Time



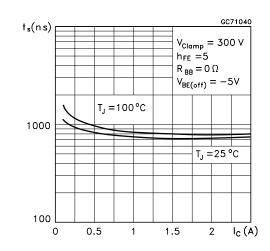
Resistive Fall Time

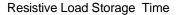


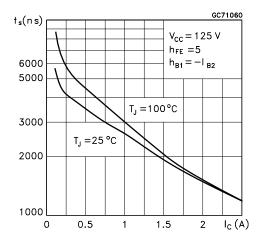




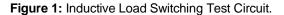
Inductive Storage Time







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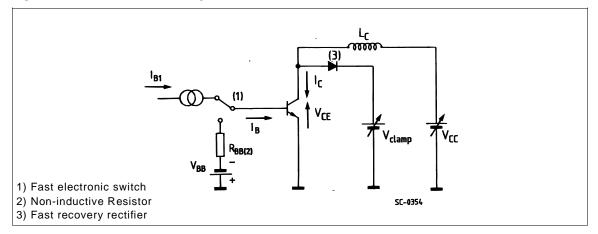
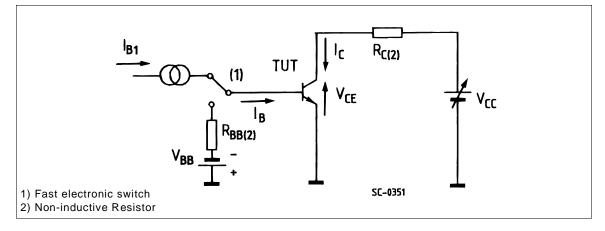


Figure 2: Resistive Load Switching Test Circuit.

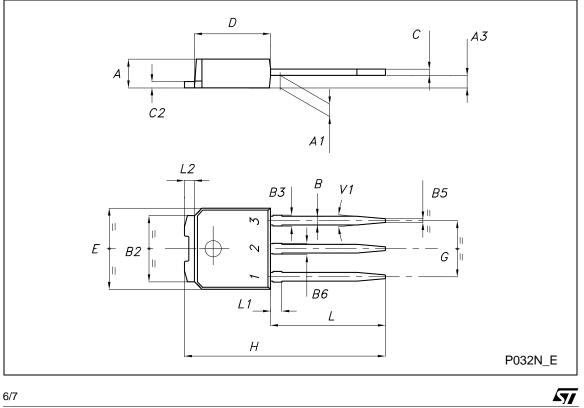


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DIM.		mm		inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
А	2.20		2.40	0.087		0.094
A1	0.90		1.10	0.035		0.043
A3	0.70		1.30	0.028		0.051
В	0.64		0.90	0.025		0.035
B2	5.20		5.40	0.204		0.213
B3			0.85			0.033
B5		0.30			0.012	
B6			0.95			0.037
С	0.45		0.60	0.018		0.024
C2	0.48		0.60	0.019		0.024
D	6.00		6.20	0.237		0.244
Е	6.40		6.60	0.252		0.260
G	4.40		4.60	0.173		0.181
Н	15.90		16.30	0.626		0.642
L	9.00		9.40	0.354		0.370
L1	0.80		1.20	0.031		0.047





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