

SL05 through SL24 Low Capacitance TVS Diode For High Speed Data Interfaces

PROTECTION PRODUCTS

Description

The SL series of TVS arrays are designed to protect sensitive electronics from damage or latch-up due to ESD, lightning, and other voltage-induced transient events. They are available with operating voltages of 5V, 12V, 15V and 24V.

TVS diodes are solid-state devices designed specifically for transient suppression. They offer desirable characteristics for board level protection including fast response time, low operating and clamping voltage, and no device degradation. The SL series devices feature a low capacitance, fast switching compensation diode in series with a standard TVS diode. This effectively reduces the overall capacitance of the device to less than 5pF making it an integrated, low capacitance solution for use on high-speed interfaces.

The SL series devices may be used to meet the immunity requirements of IEC 61000-4-2, level 4.

Features

- ◆ Transient protection for high-speed data lines to IEC 61000-4-2 (ESD) ±15kV (air), ±8kV (contact) IEC 61000-4-4 (EFT) 40A (5/50ns) IEC 61000-4-5 (Lightning) 12A (8/20µs)
- Small package for use in portable electronics
- Two devices will protect one line
- Low capacitance for high-speed data lines
- Working voltages: 5V, 12V, 15V and 24V
- Solid-state silicon avalanche technology

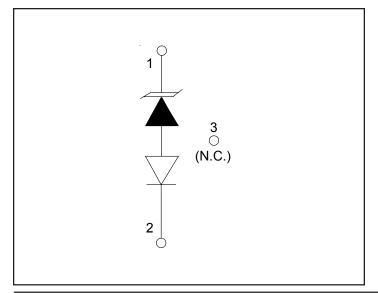
Mechanical Characteristics

- JEDEC SOT23 package
- Molding compound flammability rating: UL 94V-0
- Marking : Marking Code
- Packaging : Tape and Reel per EIA 481

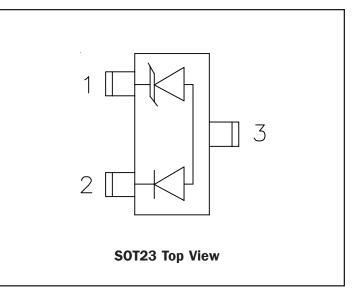
Applications

- High-Speed data lines
- Cellular handsets AND accessories
- Universal Serial Bus (USB) port protection
- Portable instrumentation
- ◆ LAN/WAN equipment
- Peripherals

Circuit Diagram



Schematic & PIN Configuration



SL05 through SL24



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Absolute Maximum Rating

Rating	Symbol	Value	Units
Peak Pulse Power ($t_p = 8/20\mu s$)	P _{pk}	300	Watts
ESD Voltage (HBM per IEC 61000-4-2)	V _{ESD}	>25	kV
Lead Soldering Temperature	Τ _ι	260 (10 sec.)	°C
Operating Temperature	T,	-55 to +125	°C
Storage Temperature	T _{stg}	-55 to +150	°C

Electrical Characteristics

SL05							
Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units	
Reverse Stand-Off Voltage	V _{RWM}				5	V	
Reverse Breakdown Voltage	V _{BR}	I _t = 1mA	6			V	
Reverse Leakage Current	I _R	V _{RWM} = 5V, T=25°C			20	μA	
Clamping Voltage	V _c	Ι _{pp} = 1A, t _p = 8/20μs			9.8	V	
Clamping Voltage	V _c	Ι _{ΡΡ} = 5Α, t _p = 8/20μs			11	V	
Peak Pulse Current	I _{PP}	t _p = 8/20µs			17	А	
Junction Capacitance	C _j	Pin 1 to 2 V _R = OV, f = 1MHz			5	pF	

SL12						
Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units
Reverse Stand-Off Voltage	V _{RWM}				12	V
Reverse Breakdown Voltage	V _{BR}	I _t = 1mA	13.3			V
Reverse Leakage Current	I _R	V _{RWM} = 12V, T=25°C			1	μA
Clamping Voltage	V _c	Ι _{pp} = 1Α, t _p = 8/20μs			19	V
Clamping Voltage	V _c	I _{pp} = 5A, t _p = 8/20μs			24	V
Peak Pulse Current	I _{PP}	t _p = 8/20µs			12	А
Junction Capacitance	C _j	Pin 1 to 2 V _R = OV, f = 1MHz			5	pF

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Electrical Characteristics (Continued)

SL15

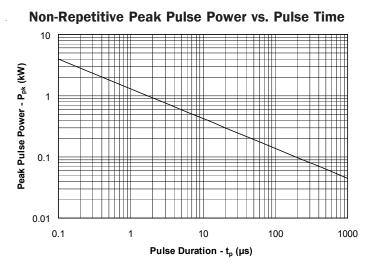
OLIO						
Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units
Reverse Stand-Off Voltage	V _{RWM}				15	V
Reverse Breakdown Voltage	V _{BR}	I _t = 1mA	16.7			V
Reverse Leakage Current	۱ _R	V _{RWM} = 15V, T=25°C			1	μA
Clamping Voltage	V _c	$I_{pp} = 1A, t_{p} = 8/20 \mu s$			24	V
Clamping Voltage	V _c	$I_{pp} = 5A,$ $t_{p} = 8/20\mu s$			30	V
Peak Pulse Current	I _{PP}	t _p = 8/20µs			10	А
Junction Capacitance	C _j	Pin 1 to 2 V _R = OV, f = 1MHz			5	pF

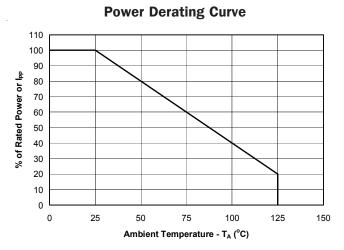
SL24						
Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units
Reverse Stand-Off Voltage	V _{RWM}				24	V
Reverse Breakdown Voltage	V _{BR}	I _t = 1mA	26.7			V
Reverse Leakage Current	I _R	V _{RWM} = 24V, T=25°C			1	μA
Clamping Voltage	V _c	Ι _{pp} = 1A, t _p = 8/20μs			43	V
Clamping Voltage	V _c	Ι _{pp} = 5A, t _p = 8/20μs			55	V
Peak Pulse Current	I _{PP}	t _p = 8/20μs			5	А
Junction Capacitance	C _j	Pin 1 to 2 V _R = OV, f = 1MHz			5	pF



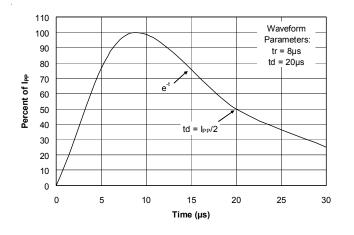
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Typical Characteristics

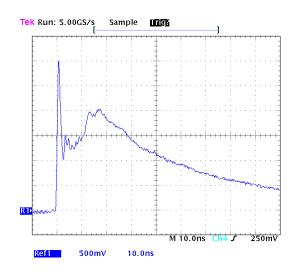




Pulse Waveform



ESD Pulse Waveform (Per IEC 61000-4-2)



IEC 61000-4-2 Discharge Parameters

Level	First Peak Current (A)	Peak Current at 30 ns (A)	Peak Current at 60 ns (A)	Test Voltage (Contact Discharge) (kV)	Test Voltage (Air Discharge) (kV)
1	7.5	4	8	2	2
2	15	8	4	4	4
3	22.5	12	6	6	8
4	30	16	8	8	15

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SL05 through SL24



PROTECTION PRODUCTS Applications Information

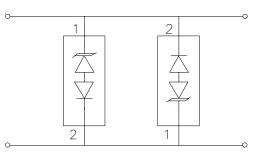
Device Connection for Protection of One High-Speed Data Line

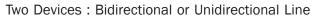
The SL series devices are designed to protect highspeed data lines. The SLxx utilizes a low capacitance compensation diode in series with, but in opposite polarity to a TVS diode in each line to achieve an effective capacitance of less than 5pF per device. During a transient event, the internal rectifier must be forward biased (TVS is reversed biased). Therefore, each device will only suppress transient events in one polarity. To achieve protection in both positive and negative polarity, a second device is connected in antiparallel to the first. On unidirectional lines, a fast switching steering diode may be used as an alternative to using two SL devices.

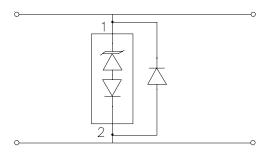
The options for connecting the devices are as follows:

- Low capacitance protection of one high-speed line: Protection of one unidirectional or bidirectional high-speed line is achieved by connecting two devices in anti-parallel. Pin 1 of the first device is connected to the line and pin 2 is connected to ground (or to a second line for differential protection). Pin 2 of the second device is connected to line 1 and pin 1 is connected to ground (or line 2) as shown. Pin 3 is not connected. During positive duration transients, the first device will conduct from pin 1 to 2. The steering diode conducts in the forward direction while the TVS will avalanche and conduct in the reverse direction. During negative transients, the second device will conduct in the same manner. For optimum protection, the ground connections should be made directly to the ground plane for best results. The path length is kept as short as possible to reduce the effects of parasitic inductance in the board traces. The path length between the TVS and the protected line should also be minimized.
- **Connection Option for Unidirectional Lines Only:** An optional method for protecting unidirectional (normal signal polarities above ground) lines is to add a fast switching steering diode in parallel to the SL TVS. Input/outputs are connected to pin 1 of the SL device and the cathode of the rectifier. The anode of the diode and pin 2 of the SL TVS are connected to ground.

SL Connection Options









I/O Line Protection

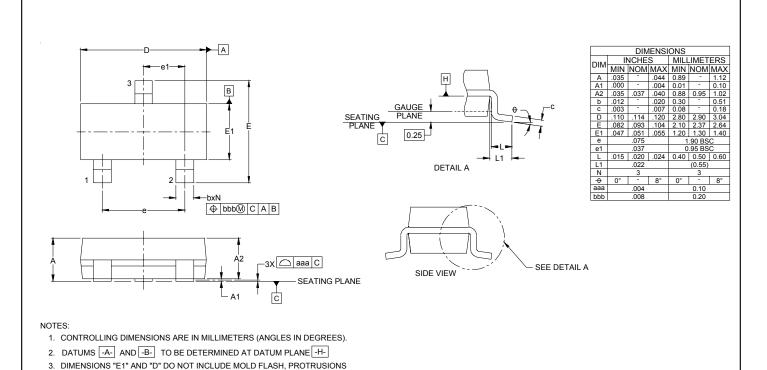
Matte Tin Lead Finish

Matte tin has become the industry standard lead-free replacement for SnPb lead finishes. A matte tin finish is composed of 100% tin solder with large grains. Since the solder volume on the leads is small compared to the solder paste volume that is placed on the land pattern of the PCB, the reflow profile will be determined by the requirements of the solder paste. Therefore, these devices are compatible with both lead-free and SnPb assembly techniques. In addition, unlike other lead-free compositions, matte tin does not have any added alloys that can cause degradation of the solder joint.



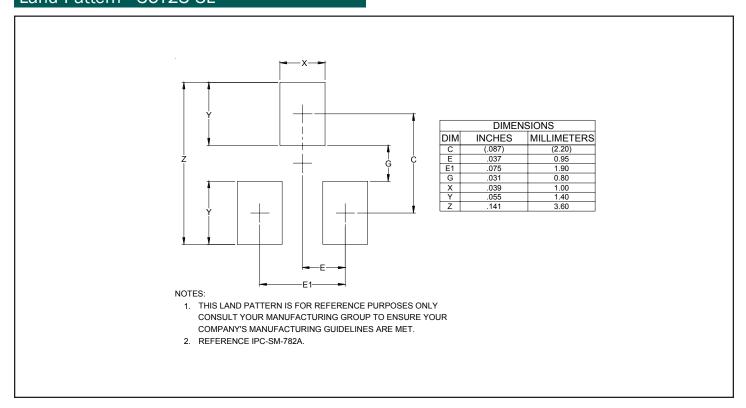
PROTECTION PRODUCTS

Outline Drawing - SOT23 3L



Land Pattern - SOT23 3L

OR GATE BURRS.





PROTECTION PRODUCTS

Marking Codes

Part Number	Marking Code
SL05	L05
SL12	L12
SL15	L15
SL24	L24

Ordering Information

Part Number	Lead Finish	Qty per Reel	Reel Size
SL05.TC	SnPb	3,000	7 Inch
SL12.TC	SnPb	3,000	7 Inch
SL15.TC	SnPb	3,000	7 Inch
SL24.TC	SnPb	3,000	7 Inch
SL05.TCT	Pb Free	3,000	7 Inch
SL12.TCT	Pb Free	3,000	7 Inch
SL15.TCT	Pb Free	3,000	7 Inch
SL24.TCT	Pb Free	3,000	7 Inch

Contact Information

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