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a voltage to pulse width converter that controls the ON time of the output pulse depending on the level of input voltage. The output level remains high when the sawtooth wave is lower than the error amplifier output voltage, the soft start setting voltage, and the idle period setting voltage.

(4) Output circuit

The output circuit can drive an external NPN transistor directly and has a totem pole configuration. The value of the ON/OFF current can be set by a resistor connected to the BR/CTL pin (pin 4). Stand-by mode (supply current 1 μ A or less) can be set by connecting the BR/CTL pin (pin4) to VCC or by making the pin open.

(5) Under-Voltage Lockout (UVLO)

Transients during powering on or instantaneous glitches in the supply voltage can cause the control IC to malfunction and damage the system. To prevent malfunction at low input voltage, the circuit compares the supply voltage to the internal reference voltage to detect a low input voltage. Once detected, the circuit sets the output pin low.

To cancel the status, set V_{CC} below the reset voltage V_R (0.9V) and then set V_{CC} up to the recommended operation range and the latch will be restored to operate normally.

