

# **SRC1212U**

**NPN Silicon Transistor** 

### **Descriptions**

- Switching application
- Interface circuit and driver circuit application

#### Features

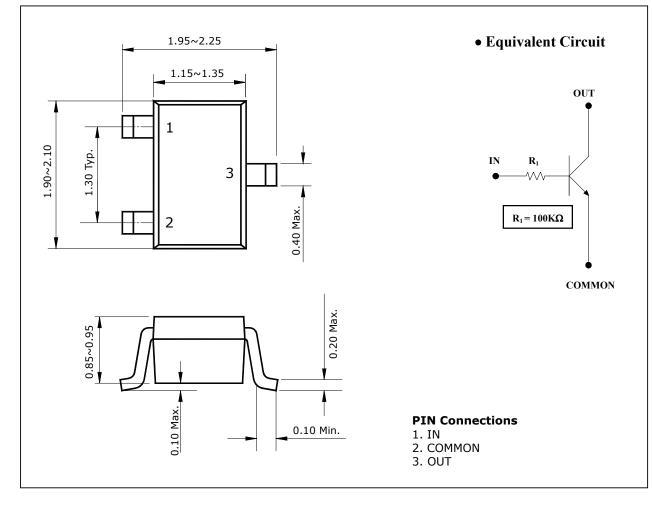
- With built-in bias resistor
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density

## **Ordering Information**

Type NO.	Marking	Package Code		
SRC1212U	RB	SOT-323		

## **Outline Dimensions**

unit : mm



## **SRC1212U**

#### **Absolute Maximum Ratings** (Ta=25°C) Characteristic Symbol Rating Unit 50 V Output voltage $V_{0}$ V 40, -5 Input voltage $V_{\rm I}$ $I_{O}$ 100 Output current mΑ mW Power dissipation $\mathbf{P}_{\mathsf{D}}$ 200 Τյ °C Junction temperature 150 °C Storage temperature range $\mathsf{T}_{\mathsf{stg}}$ -55 ~ 150

## **Electrical Characteristics**

(Ta=25°C)

Characteristic	Symbol	<b>Test Condition</b>	Min.	Тур.	Max.	Unit
Output cut-off current	$I_{O(OFF)}$	V <sub>0</sub> =50V, V <sub>I</sub> =0	-	-	500	nA
DC current gain	GI	$V_0 = 5V, I_0 = 10mA$	120	-	-	-
Output voltage	V <sub>O(ON)</sub>	$I_0=10$ mA, $I_I=0.5$ mA	-	0.1	0.3	V
Input voltage (ON)	V <sub>I(ON)</sub>	V <sub>0</sub> =0.2V, I <sub>0</sub> =5mA	-	-	4.4	V
Input voltage (OFF)	V <sub>I(OFF)</sub>	V <sub>0</sub> =5V, I <sub>0</sub> =0.1mA	0.3	-	-	V
Transition frequency	f <sub>T</sub> *	$V_0=10V$ , $I_0=5mA$ , f=1MHz	-	200	-	MHz
Input current	II	V <sub>I</sub> =5V, I <sub>O</sub> =0	-	-	0.1	mA
Input resistor (Input to base)	$R_1$	-	70	100	130	KΩ

\* : Characteristic of transistor only

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## **Electrical Characteristic Curves**

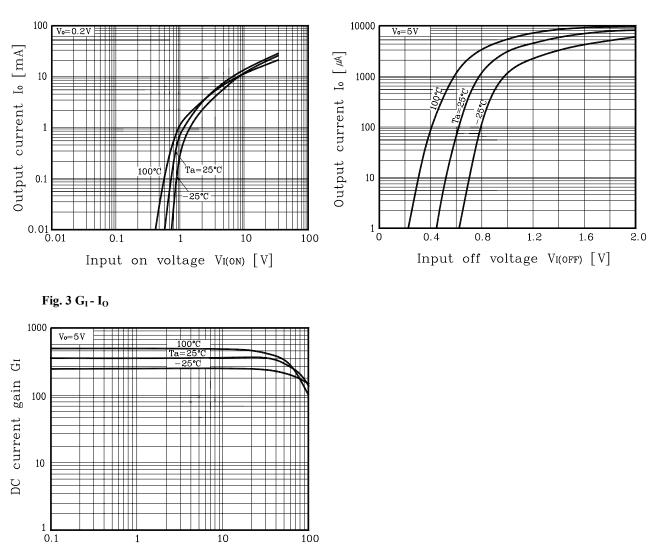


Fig. 1  $I_0$  -  $V_{I(ON)}$ 

Fig. 2 Io - VI(OFF)

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Output current Io [mA]