

SRC1211EF

NPN Silicon Transistor

unit: mm

Descriptions

- Switching application
- Interface circuit and driver circuit application

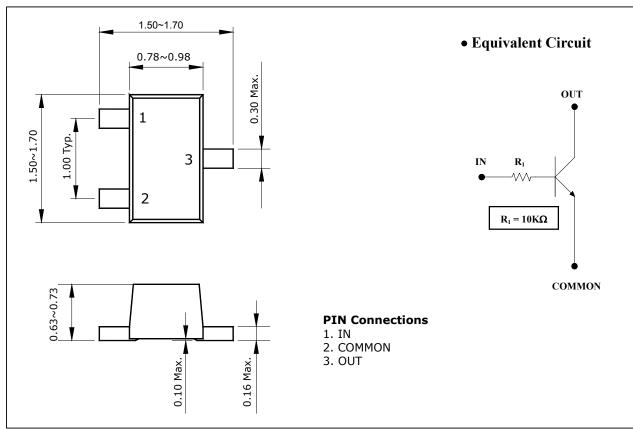
Features

- With built-in bias resistor
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density

Ordering Information

Type NO.	Marking	Package Code	
SRC1211EF	RD	SOT-523F	

Outline Dimensions



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SRC1211EF

Absolute Maximum Ratings

(Ta=25°C)

Characteristic	Symbol	Rating	Unit
Output voltage	Vo	50	V
Input voltage	V_{I}	30, -5	V
Output current	I_{O}	100	mA
Power dissipation	P_{D}	150	mW
Junction temperature	T _J	150	°C
Storage temperature range	T_{stg}	-55 ~ 150	°C

Electrical Characteristics

(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Output cut-off current	$I_{O(OFF)}$	$V_0 = 50V, V_I = 0$	-	-	500	nA
DC current gain	G_{I}	$V_0=5V$, $I_0=10$ mA	120	-	-	ı
Output voltage	$V_{O(ON)}$	I_0 =10mA, I_I =0.5mA	-	0.1	0.3	٧
Input voltage (ON)	$V_{I(ON)}$	V ₀ =0.2V, I ₀ =5mA	-	0.9	1.4	٧
Input voltage (OFF)	$V_{I(OFF)}$	$V_0 = 5V$, $I_0 = 0.1$ mA	0.3	0.55	-	٧
Transition frequency	f_T^*	$V_0=10V$, $I_0=5mA$, $f=1MHz$	-	200	-	MHz
Input current	$I_{\rm I}$	$V_I=5V$, $I_O=0$	-	-	0.88	mA
Input resistor (Input to base)	R ₁	-	7	10	13	ΚΩ

^{* :} Characteristic of transistor only

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Electrical Characteristic Curves

Fig. 1 $I_O - V_{I(ON)}$

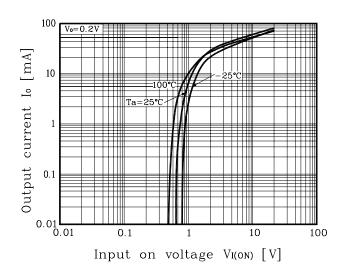


Fig. 2 $I_O - V_{I(OFF)}$

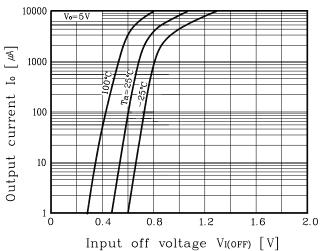
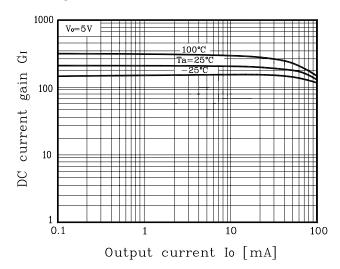


Fig. 3 G_I - I_O



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